Keysight High-Speed Digitizers Technology Expertise

With its PXIe and AXIe high-speed digitizers, Keysight offers you a reliable partnership for your advanced developments.

Keysight unique technology uses proprietary ICs so you can take the most out of the ADCs and FPGAs integrated into the digitizer. On-board real-time algorithm in the FPGA allows fast detection of signal characteristics in various domains.

We offer an extended range of firmware and application options:
- Firmware options: Specific real-time functions speeding-up your overall system response time, such as averager or digital down-conversion.
- Application options: Bundle including software and firmware for specific configurations targeting specific requirements, such as streaming & recording.

Moreover, if you need to integrate your own custom real-time IP algorithm, an additional software bundle, the FPGA Development Kit is available.

Keysight high-speed digitizer cards are used as component in several applications, such as:
- Aerospace & Defense: Radar and satellite
- Physics: Advanced and multichannel experiments
- RF Communication: 5G, wideband, MIMO

Whether your application requirements are clear or you are at the earlier stage of your selection, our digitizer specialists are there to help you define the best solution for your applications.

Added values

Better measurement fidelity and signal integrity
- Display deeper and sharper results
- Repeatability

Higher measurement throughput
- Speed to detection and decision
- Productivity

Smaller footprint
- Design for size and power optimization
- High channel density

Lower total cost of ownership
- Interoperability
- Reusability and upgradability
- Very high reliability
- Support and maintenance
# PXIe High-Speed Digitizers

**Key specifications**

<table>
<thead>
<tr>
<th>Product</th>
<th>PXIe 12-bit High-Speed Digitizer/Wideband Digital Receiver</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of Channels</td>
<td>1 - 2 ch</td>
</tr>
</tbody>
</table>
| Max. Sampling Rate | 3.2 GS/s (1 ch)  
1.6 GS/s (2 ch) |
| ADC Resolution | 12-bit |
| Max. Bandwidth | DC to 2 GHz |
| Max. Memory | 4 GB (1 GSa/ch) |
| FPGA Signal Processing | Xilinx FPGA |
| Full Scale Range (FSR) | Dual, 1 V and 2 V |
| Input Impedance | 50 Ω |
| Form Factor | PXIe, 2-slot, fully shielded, built-in cooling system |
| Soft Front Panel (SFP) and Driver | Included |
| Operating System | Windows, Linux |

**Firmware options**

- DGT: Digitizer firmware
- INT: Interleaved channel sampling functionality
- FDK: Custom firmware capability
- DDC: Wideband real-time digital down-conversion
- TSR: Triggered simultaneous acquisition and readout

**Applications options**

- CB0: Digitizer streaming and recording
- CB2: Digital down converter streaming at 1.6 GS/s
- BB1: High fidelity digitizer

**Related softwares**

- U5340A: FPGA Development Kit for High-Speed Digitizers
- U1092A-S0x: AcqirisMAQS Multichannel Acquisition Software
- W1462BP: SystemVue FPGA Architect
- 89601B: 89600 VSA Software, transportable license

**Related products**

- M9019A: PXIe 18-slot Chassis, 3U, 24 GB/s
- M9021A: PXIe to PCIe Cable Interface
- M9048A: PCIe Desktop Adapter
- M9036A: PXIe Embedded Controller
- M9037A: PXIe High Performance Embedded Controller
- M9393A: PXIe Performance Vector Signal Analyzer, 50 GHz
- M9362AD01: PXIe Quad Downconverter, 10 MHz to 50 GHz

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**Added values**

- Better measurement fidelity and signal integrity
  - Display deeper and sharper results
  - Repeatability
- Higher measurement throughput
  - Speed to detection and decision
  - Productivity
- Smaller footprint
  - Design for size and power optimization
  - High channel density
- Lower total cost of ownership
  - Interoperability
  - Reusability and upgradability
  - Very high reliability
  - Support and maintenance
## AXIe High-Speed Digitizers

<table>
<thead>
<tr>
<th>Key specifications</th>
<th>AXIe 12-bit High-Speed Digitizer/ Wideband Digital Receiver</th>
<th>AXIe 10-bit High-Speed Digitizer/ DAQ</th>
<th>AXIe 8-bit High-Speed Digitizer/ DAQ</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of Channels</td>
<td>4 - 8 ch</td>
<td>2 - 4 ch</td>
<td>32 ch</td>
</tr>
<tr>
<td>Max. Sampling Rate</td>
<td>3.2 GS/s (4 ch)</td>
<td>10 GS/s (2 ch)</td>
<td>1 GS/s</td>
</tr>
<tr>
<td>ADC Resolution</td>
<td>12-bit</td>
<td>10-bit</td>
<td>8-bit</td>
</tr>
<tr>
<td>Max. Bandwidth</td>
<td>DC to 2 GHz</td>
<td>DC to 2.5 GHz</td>
<td>DC to 500 MHz</td>
</tr>
<tr>
<td>Max. Memory</td>
<td>16 GB (1 GSa/ch)</td>
<td>8 GB (1.6 GSa/ch)</td>
<td>16 GB (512 MSa/ch)</td>
</tr>
<tr>
<td>FPGA Signal Processing</td>
<td>Xilinx FPGAs (4x)</td>
<td>Xilinx FPGAs (2x)</td>
<td>Xilinx FPGAs (4x)</td>
</tr>
<tr>
<td>Full Scale Range (FSR)</td>
<td>Dual, 1 V and 2 V</td>
<td>Dual, 250 mV and 1 V</td>
<td>Multiple, 250 mV up to 5 V</td>
</tr>
<tr>
<td>Input Impedance</td>
<td>50 Ohm</td>
<td>50 Ohm</td>
<td>50 Ohm</td>
</tr>
<tr>
<td>Form Factor</td>
<td>AXIe, 1-slot, fully shielded, built-in cooling system</td>
<td>AXIe, 1-slot, fully shielded, built-in cooling system</td>
<td>AXIe, 1-slot, fully shielded, built-in cooling system</td>
</tr>
<tr>
<td>Soft Front Panel (SFP) and Driver</td>
<td>Included</td>
<td>Included</td>
<td>Included</td>
</tr>
<tr>
<td>Operating System</td>
<td>Windows, Linux</td>
<td>Windows, Linux</td>
<td>Windows, Linux</td>
</tr>
</tbody>
</table>

### Firmware options

- **DGT**: Digitizer firmware
- **INT**: Interleaved channel sampling functionality
- **FDK**: Custom firmware capability
- **DDC**: Wideband real-time digital down-conversion
- **LDC**: Limited real-time digital down conversion firmware
- **TSR**: Triggered simultaneous acquisition and readout

### Applications options

- **CB1**: Digital down converter streaming at 1 GS/s
- **CB2**: Digital down converter streaming at 1.6 GS/s
- **B01**: Enhanced signal processing

### Related softwares

- **U5340A**: FPGA Development Kit for High-Speed Digitizers
- **U1092A-S0x**: AcqirisMAQS Multichannel Acquisition Software
- **W1462BP**: SystemVue FPGA Architect
- **89601B**: 89600 VSA Software

### Related products

- **M9502A**: AXIe 2-slot Chassis
- **M9505A**: AXIe 5-slot Chassis
- **M9514A**: AXIe 14-slot Chassis
- **M9537A**: AXIe High Performance Embedded Controller
High-Speed Digitizer Software Platform

**Specific applications**

- Streaming, FFT

**High-Speed Digitizer Application DVD**

- Included with your digitizer option

**Custom real-time processing**

- US340A + FDK option
- FPGA Development Kit for High-Speed Digitizers

**Standard system**

- High-Speed Digitizers

**MD2 Driver & SFP**

- Included:
  - Selected firmware option(s)
  - Soft Front Panel (SFP)

**IO Libraries Suite**

- Windows only

- Included with your digitizer

**Advanced measurements, signal analysis and demodulation**

- VSA 89600 Software
- Digitizer hardware extension

**Multichannel acquisitions**

- U1092A-S0x AcqirisMAQS

- Software requiring additional license
High-Speed Multichannel Data Acquisition Systems

The Keysight AXIe high-speed digitizer provides the ideal solution for advanced experiments in hydrodynamics, plasma fusion, particle physics, and aerospace & defense. With these modules you can build a large number of synchronous acquisition channels with unprecedented measurement fidelity in the smallest footprint. Advanced IP design, state-of-the-art technology and on-board real-time data processing are combined to achieve outstanding performance.

Build multi-channels data acquisition systems

Large systems can be built by simply populating a chassis with your choice of digitizers.

A selection of configurable companion products is also provided, including signal conditioning, chassis and controllers.

Moreover, the AcqirisMAQS Multichannel Acquisition Software, designed specifically for large scale experiments, enables configuration management as well as visualization of data for hundreds of channels from a single console.

Example configuration

Figure 1. Two M9703B AXIe 12-bit digitizers installed in the M9505A 5-slot AXIe chassis to form a 16-channel 12-bit acquisition system.

Figure 2. Five M9703B AXIe 12-bit digitizers installed in the M9505A 5-slot AXIe chassis to form a 40-channel 12-bit acquisition system.

Figure 3. Eight M9703B AXIe 12-bit digitizers installed in the M9514A 14-slot AXIe chassis to form a 64-channel 12-bit acquisition system. Contact factory for possible configuration.
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