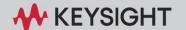
# Attenuator Switch Driver Security Features and Volatility Documentation

This manual document provides the documentation security information for the following instruments:

11713D/E



# **Notices**

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# Memory Information and Procedures

Product: 11713D/E

Memory Type: NAND FLASH Section 1 Memory Size: 2Gbit

Memory Function: Contains the instrument's firmware, failsafe firmware, FPGA image and file system

User Modifiable (Y/N): N Volatile (Y/N): N

#### **Memory Erase Processes:**

Not applicable.

• Contains no application specific information

Memory Type: NAND FLASH Section 2 Memory Size: 8Kbit

Memory Function: Contains user configuration data and attenuator cycle count.

User Modifiable (Y/N): Y Volatile (Y/N): N

#### **Memory Erase Processes:**

- User can perform factory reset and cycle count clear to restore to factory default setting.
- This procedure is not recommended for use in routine applications because of possibility of unintended loss of user data.
- Factory reset
  - Utility -> Factory default -> Yes -> OK
- Cycle Count Clear

Cycle -> Clear All -> OK

or

DIAG:REL:CLEA:ALL

Memory Type: EEPROM Memory Size: 32Kbit

**Memory Function**: Contains basic instrument information such as Board ID, Revision ID, MAC address and Serial number.

User Modifiable (Y/N): N Volatile (Y/N): N

### **Memory Erase Processes:**

- This is only applicable to factory or service only.
- · No user settable information in this memory.

Memory Type: SDRAM DDR Memory Size: 2Gbit

**Memory Function**: When the instrument is power on all of the information is downloaded into the SDRAM for use by instrument.

User Modifiable (Y/N): N Volatile (Y/N): Y

#### **Memory Erase Processes:**

• Power cycle of the unit erases all volatile memory.

Memory Type: NOR FLASH (FPGA)	Memory Size: 32Mbit
Memory Function: Contains main board FPGA binary file.	
User Modifiable (Y/N): N	Volatile (Y/N): N
Memory Erase Processes:	
<ul> <li>Not applicable.</li> </ul>	
<ul> <li>Contains no application specific information</li> </ul>	
···	

# Select one of the following:

- √ Sanitization process is documented above
- Sanitization process could be available with engineering resources
- □ Sanitization process is not possible (i.e. sanitize the product by destruction)