Power Integrity Boot Camp for Designers

with Keysight PathWave ADS

KEYSIGHT TECHNOLOGIES
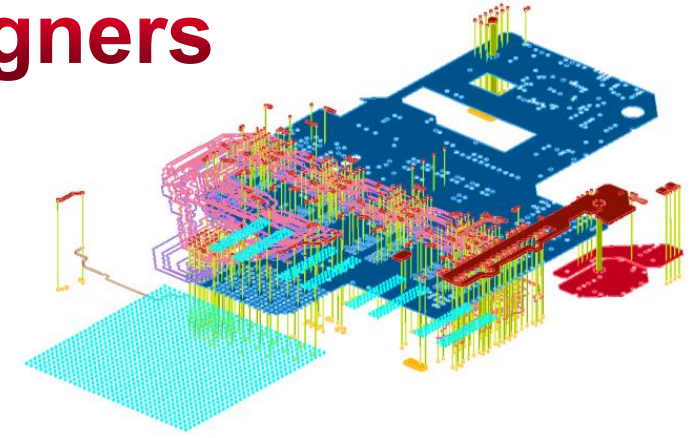
with solutions partner

PICOTEST

AGENDA

15 min   Welcome

60 min   Flat Target Z Design Process
  ➢ PI Basics, Target Z, Step Load, DeCap Selection, Rogue Waves

45 min   Measuring, Modeling, Simulating Capacitors and Inductors
  ➢ 2-Port Shunt Impedance Measurement, PCB Mounting Inductance, RLC Measured Model Extraction

15 min   Measurement Tools

60 min   Lunch

45 min   DC-DC Converter Selection and Modeling
  ➢ Current mode vs. voltage mode, state Based Average Measured Model, Harmonic Balance for Large and Small Signal Transients

45 min   DeCap Optimization with PCB AC Parasitics, DC IR Drop, and Thermal Considerations
  ➢ Caculating C for Flat Z, Parallel Capacitor Parasitics, PIPro DeCap Optimizer, DC IR Drop, Electrothermal

60 min   Hands-On Simulation and Measurement Demo Station Networking

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