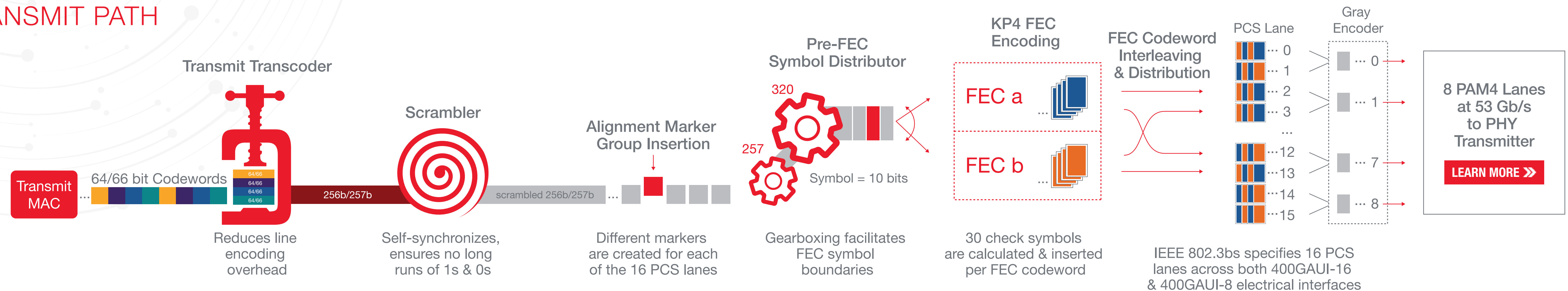


400GE FEC Encode/Decode Processing

The IEEE 802.3bs Task Group defines specifications for 400 Gigabit per second (Gb/s) Ethernet, where forward error correction (FEC) is mandatory. See how a 400GBASE-R Reed-Solomon RS-544 (KP4 FEC) encode/decode works in operation. Note that FEC encoding/decoding takes place at the binary level before the physical layer.

TRANSMIT PATH



RECEIVE PATH

