**NEED**
Explore the architecture while accounting for non-linearity, RF mismatch, finite isolations and antenna coupling.

**PAIN**
Simulation time becomes prohibitively long with the size of the array.

**SOLUTION**
Improved algorithm in SystemVue makes the simulation time almost independent of the size of the array.

**NEED**
Simulate the entire system that includes DSP and RF in one tool.

**PAIN**
Risk not being uncovered until verification stage, and thus causing another re-spin.

**SOLUTION**
SystemVue facilitates the DSP and RF designs in one environment allowing concurrent design of DSP and RF.

**NEED**
Increase the accuracy of prediction while simulating at the architectural level.

**PAIN**
Inability to use measurement based models or circuit level models at the architectural level.

**SOLUTION**
SystemVue allows high-level, low-fidelity models to begin with for a quick architectural design.

**NEED**
Integrate designs in both simulation and hardware to verify system level performance.

**PAIN**
Inability to connect hardware measurement and simulation, thereby restricting the complete verification until every thing is realized in hardware and integrated.

**SOLUTION**
SystemVue allows the user to connect measurement with simulation seamlessly through instrument connectivty and hardware in loop.


Free trial  Brochure