

Keysight M3202A firmware

3.73.00 Version Information

Released Date:	June, 2018
Operative systems:	N/A
Supported Modules:	M3202A
File Name:	N/A

Bug Fixes

- Synchronization bug that could cause a bad SYNC initialization
- Queue bug that wasn't allowing the use of certain HVI registers

3.69.00/3.70.01 Version Information

Released Date:	April, 2018
Operative systems:	N/A
Supported Modules:	M3202A
File Name:	N/A

Bug Fixes

- PXI bug that caused malfunction when using certain chassis with new BIOS

New Features

- Added new calibration procedures to ensure a more robust operation under temperature changes

3.67.00 Version Information

Released Date:	February, 2018
Operative systems:	N/A
Supported Modules:	M3202A
File Name:	N/A

Bug Fixes

- AWG queue parser multiple channels queued at the same time
- Fix PXI trigger set clear that was always Sync even when no Sync

New Features

- Added Support for Register in AWGqueue for the WF#
- Added marker delay feature in AWGqueue

3.64.00 Version Information

Released Date:	August, 2017
Operative systems:	N/A
Supported Modules:	M3202A
File Name:	N/A

Bug Fixes

- AWG ignores triggers while the clock is being reconfigured

3.63.00 Version Information

Released Date:	July, 2017
Operative systems:	N/A
Supported Modules:	M3202A
File Name:	N/A

New Features

- HVI Sequencer can now address HVI Ports through register values.

Bug Fixes

- Fixed HVI Sequencer issue which caused synchronized/unsynchronized Wait instructions to malfunction.
- Fixed AWG cyclic queue mode which caused incoherent waveform playback. The minimum waveform length for cyclic mode is 2us of playback.
- I/O trigger fixed
- Synchronized output I/O trigger jitter solved
- Reduced HVI port latency in fix clock models to 80 ns