

Agilent M9502A and M9505A AXIe Chassis Firmware Revision History

The current version of your chassis' firmware is listed on the Home Page of the web interface. You can update the AXIe chassis firmware to take advantage of refinements and added features as they become available. If the version in your chassis does not match the most recent version available on the product website, you should update your chassis firmware.

Note: F2AX - Identifies a firmware revision string for the M9502A 2 slot chassis, F5AX - Identifies a firmware revision string for the M9505A 5 slot chassis. Both chassis use the same revision number (for example, 1.3.37). There are multiple internal-only releases between the major public releases listed below. For a complete list see the ReleaseNotes.txt file in the firmware update .zip file.

The revision string indicates several components such as A002e, B12060816, CA1.0, etc. The actual content of these components is for Agilent internal use only.

Revision: F2AX-1.3.41-0108-A002e-B14043011-CA1.1-DA1.0-Ex.4
F5AX-1.3.41-0108-A002e-B14043011-CA1.2-DA1.0-Ex.4

- Update USB-equipped ESMs to version 14043011
-- Disable reset pulse immediately after configuration of USB FPGA

Revision: F2AX-1.3.40-0108-A002e-B12062214-CA1.1-DA1.0-Ex.4
F5AX-1.3.40-0108-A002e-B12062214-CA1.2-DA1.0-Ex.4

- Kernel support for hardware change to Shmm500 Shelf Manager assembly on ESM (M9505-00130 or M9505-00230). These assemblies, designated with an R2 label on the shelf manager assembly sticker, must run firmware 1.3.39 or later.

Revision: F2AX-1.3.38-0108-A002e-B12062214-CA1.1-DA1.0-Ex.4
F5AX-1.3.38-0108-A002e-B12062214-CA1.2-DA1.0-Ex.4

- Note: the **B12062214** only appears on USB-equipped ESMs.
- Allow multiple-slot AXIe modules to consume up to 500 W total. Single-slot AXIe modules have no change.

Revision: F2AX-1.3.37-0108-A002e-B12062214-CA1.0-DA1.0-Ex.4
F5AX-1.3.37-0108-A002e- B12062214-CA1.0-DA1.0-Ex.4

- Note: the **B12062214** only appears on USB-equipped ESMs.

- Chassis fan cooling algorithm improvements.
- Correct fan firmware (0108) rollback issue from watchdog timer.
- Support option USB for specialized instrumentation.
- Add PCIe root complex control for auto, x8 front panel, instrument hub slot 1, or USB on module configuration web page.
- Add PCIe enumeration startup delay to handle modules that may take several seconds to be ready for PCIe enumeration.
- Front panel Status LED blinking pattern (500ms on/50ms off) to indicate additional hold off pause period.
- Resolve LAN .local dns names to fully qualified domain names when available.
- Minimum fan level setting persists across firmware updates.
- Update PXI chassis eeprom to rev 2 to support AXIe PCIe link assignments (link 1, slot 1; link 2, slot 2-5).
- Updates to web page descriptions.
- Performance improvements for IPMC module updates.
- Fix *IDN? query race condition.
- Agilent IO libraries 16.2 and 16.3 support.

Revision: F2AX-1.3.23-0107-A002e-B*-CA1.0-DA1.0-Ex.3

F5AX-1.3.23-0107-A002e-B*-CA1.0-DA1.0-Ex.3

- Improvements to chassis fan component – fan speed is 35 to 100, et al
- Web page update for fan speed
- Support for M9536A identification as system controller in v102 update E-key support
- Firmware mismatch check
- Update power record for 5AX chassis
- Improved temperature/fan monitoring algorithm sensitivity
- AXIe and PICMG E-Keying support for chassis and ESM, E-Key activity table to web page
- IDT Switch configuration for gen1 or gen2 configuration
- Compact PCIe chassis eeprom, configuration
- Trigger FPGA update to version 0x2e for DCM fix

Revision: F2AX-1.3.8-0103-A002b

F5AX-1.3.8-0103-A002b

- Add support for IVI-VISA software drivers release 1.1.1.1
- Integration with Agilent I/O libraries 16.0

Revision: F2AX-1.2.10-0103-A0027

F5AX-1.2.10-0103-A0027

- High/low line power detection and limits for M9505A

Revision: F2AX-1.2.6-0103-A0026

F5AX-1.2.6-0103-A0026

- Add support for M9505A

Revision: F2AX-1.1.0-0100-A0022

- Initial Release for M9502A