

Agilent Logic and Protocol Analyzer Software (32-bit Application)

Version 5.90.1100 – September 2015

Operating System Compatibility

Version 5.90 of the Agilent Logic and Protocol Analyzer software is a 32-bit application and therefore it is supported for use with 32-bit as well as 64-bit operating systems.

Recommended operating systems:

- 32-bit Operating System installations:
 - o Windows XP (SP3)
 - o Windows Server 2003 (SP2)
- 32-bit or 64-bit Operating System installations (64-bit recommended *)
 - o Windows Vista (SP2)
 - o Windows Server 2008 (SP2)
 - o Windows 7 (SP1)
 - o Windows Server 2008 R2 (SP1)
 - o Windows 8 and 8.1
 - o Windows 10

Notes:

- Keysight recommends you install the latest service pack offered for your operating system. In addition, apply all critical operating system updates.
- Each OS variant places a different limit to the amount of physical memory supported to the system. Users should consult their system's documentation to determine how much memory their specific operating system variant will support. For optimum performance of the Logic and Protocol Analyzer software, Keysight recommends installing more than the minimum required system memory.
- * Version 6.00 or higher of the Logic and Protocol Analyzer software is recommended for solutions based on AXIe modular systems and 16850 series logic analyzers. Version 6.00 or higher is compatible with 64-bit operating systems only.

Hardware Prerequisites

Keysight 16850A Series Portable Logic Analyzers and the Keysight M9536A embedded controller for AXIe modular systems are fully compatible with the Logic and Protocol Analyzer software.

The following recommendations are for M950xA AXIe modular systems with an external host PC (desktop or laptop) running the Logic and Protocol Analyzer software.

- The minimum CPU, memory, and graphics specified for your chosen operating system
- Increase RAM to 4 GB minimum, or more (e.g. 8 GB is recommended for a multiple module logic and protocol analysis system)
- Increase CPU performance to a minimum of 2 GHz dual core processing
- 80 GB minimum hard drive, SSD recommended for loading/storing configurations and acquired data

Additionally, for an external host PC (desktop or laptop) connected to an M9502A or M9505A AXIe chassis or a U4002A AMP chassis, the appropriate IO support for the chassis is required.

- Cabled PCIe is the recommended IO option

Supported Instruments

If you do not see your specific logic or protocol analyzer listed here, please refer to www.keysight.com/find/la-sw-download to determine the correct software to use for your instrument.

- 16850A Series Portable Logic Analyzers **
- 16800 Series Portable Logic Analyzers
- 16900 Series Modular Logic Analyzers
- The following logic and protocol analyzer modules in an M9502A or M9505A modular chassis:
 - o U4154A 4 Gb/s Logic Analyzer Module **
 - o U4301A Protocol Analyzer Module for PCIe 8 Gbps **
 - o U4421A Protocol test module for MIPI D-PHY **
 - o U4431A protocol test module for MIPI M-PHY **
 - o U4998A HDMI 1.4 protocol/audio/video analyzer and generator
- Additionally, the following modules are supported for use in the U4002A modular chassis:
 - o U4301A protocol analyzer module for PCIe 8 Gbps **
 - o U4998A HDMI 1.4 protocol/audio/video analyzer and generator

*** Release 6.03 (or higher) of the Logic and Protocol Analyzer software is recommended for these products. Enhancements and defect repair will be provided only in 64-bit versions (6.xx versions).*

How to Download

- Click the download button, select file and save.

How to Install

The Logic and Protocol Analyzer software install directory is:

- "C:\Program Files(x86)\Agilent Technologies\Logic Analyzer\" (for 64-bit Windows OS installs)
- "C:\Program Files\Agilent Technologies\Logic Analyzer\" (for 32-bit Windows OS installs)

Licenses for options to the U4154A, U4301A, and U4421A modules, as well as licenses for software add-ins and features released prior to version 6.00, are installed in the following directory:

- "C:\Program Files (x86)\Agilent Technologies\Logic Analyzer\License" (for 64-bit Windows OS installs)
- "C:\Program Files\Agilent Technologies\Logic Analyzer\License" (for 32-bit Windows OS installs)

Licensing

- The Logic and Protocol Analyzer application software does not require a license.
- Some hardware and software features and options are licensed. See the appropriate product data sheet for more information.

Licenses for options to the U4154A, U4301A, and U4421A modules, as well as licenses for software add-ins and features released prior to version 6.00, are installed in the following directory:

- "C:\Program Files (x86)\Agilent Technologies\Logic Analyzer\License" (for 64-bit Windows OS installs)
- "C:\Program Files\Agilent Technologies\Logic Analyzer\License" (for 32-bit Windows OS installs)

Supporting Documentation

- See Logic and Protocol Analyzer Readme (version 5.90)
- See Logic and Protocol Analyzer Online Help Files (Version 5.90)
- See Logic and Protocol Analyzer Online Help – PDF Version (Version 5.90)

Add-On Software / Utilities

See www.keysight.com/find/la-sw-download for the most up-to-date list of software applications supported by version 5.90.1100.

- B4621B DDR2/3/4 Bus Decoder
- B4622B DDR2/3/4, LPDDR2/3/4 Protocol Compliance and Analysis Tool
- B4623B LPDDR2/3/4 Bus Decoder
- DDR/LPDDR Memory Support Tools
- B4655A FPGA Dynamic Probe for Xilinx
- B4656A FPGA Dynamic Probe for Altera
- E9528A ARM Cortex ETM Trace Decoder
- E9529A ARM Cortex PTM Trace Decoder
- Analysis AddIn Wizard – Create custom plug-ins for the Logic and Protocol Analyzer
- Protocol Development Toolkit – Create custom protocol decoders
- License Server – Administer floating licenses shared within an organization
- COM Automation – Use the Logic and Protocol Analyzer COM interface from a PC or a laptop where the application has not been installed

Start the application software

1. Double click the Agilent LPA icon located on your desktop.

Or type "Analyzer" into the Search box in the Windows 7 start menu.

Release Contents

Logic and Protocol Analyzer Software Version 5.90.1100 (September 2015)

Additions

- Support for Windows 10

Modifications

- **Logic and Protocol Analyzer application - General**
 - o Code has been added to prevent the host PC from 'sleeping' while the chassis/blade upgrade utility is open.
 - **16850 Series Portable Logic Analyzers**
 - o Enhanced the set of general purpose Logic probes.
 - **B462XX DDR Tools**
 - o Fixed an issue with the MRS decode as related to DDR3. Sample positions were adjusted to not be near transitions on ADDR signals (WIT 379062).
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Logic and Protocol Analyzer Software Version 5.90 (January 2015)

Modifications

- **Logic and Protocol Analyzer application - General**
 - o Fixed a library registration issue found when using an FFT function, where a VBA error message claimed "Library not Registered". Root cause was found to be a recompile issue with a 3rd party software application that had moved to a new version of the add-in packaging (WIT 242845).
 - o Updated the General Purpose Probe definition file to include several missing probe definitions, including U4203A, U4204A, U4205A, E5381B and E5382B (WIT 360564).
 - o Cleaned up a relatively rare UI starting/stopping issue that intermittently appeared on certain machines when an executable file was launched (WIT 175670).
 - o Improved a performance issue found during certain repetitive runs, due to a misplaced delay in the code that manages repetitive runs with stand-alone analyzers (WIT 350474). See Logic and Protocol Analyzer 6.XX 64 bit releases for additional on-going product software improvements.
- **View Scope**
 - o Added the DSA91804A as a supported Scope (WIT 358783)
- **16850 Series Portable Logic Analyzers**
 - o Fixed an issue where the analyzer would lock up under certain "Repetitive Run" conditions, typically while in state or transitional timing mode with lots of data in waveform. Issue could also be found during overnight runs where the analyzer may lock up or report error. Root cause was related to a timing issue that was fixed by adding a delay. (WIT 358781).
- **U4301A PCIe Gen3 8 Gb/s Protocol Analyzer**
 - o Improved a performance issue found during certain repetitive runs, due to a misplaced delay in the code that manages repetitive runs (WIT 350474).
 - o See Logic and Protocol Analyzer 6.XX 64-bit releases for additional on-going product software improvements.
- **U4421A MIP D-PHY Protocol Analyzer**
 - o No issues fixed in 5.90 release – see Logic and Protocol Analyzer 6.XX 64 bit releases for on-going product software improvements.

- **U4431A MIPI M-PHY Protocol Analyzer**

- o Fixed an issue where the MIPI M-PHY protocol analyzer did not properly track Hibern8, and therefore did not properly display it in Raw mode (WIT 315118).
- o See Logic and Protocol Analyzer 6.XX 64 bit releases for additional on-going product software improvements.

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