

Keysight U4431A MIPI M-PHY Protocol Analyzer

User Guide

Notices

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Safety Notices

CAUTION

A CAUTION notice denotes a hazard. It calls attention to an operating procedure, practice, or the like that, if not correctly performed or adhered to, could result in damage to the product or loss of important data. Do not proceed beyond a CAUTION notice until the indicated conditions are fully understood and met.

WARNING

A WARNING notice denotes a hazard. It calls attention to an operating procedure, practice, or the like that, if not correctly performed or adhered to, could result in personal injury or death. Do not proceed beyond a WARNING notice until the indicated conditions are fully understood and met.

Safety Summary

The following general safety precautions must be observed during all phases of operation of this instrument. Failure to comply with these precautions or with specific warnings or operating instructions in the product manuals violates safety standards of design, manufacture, and intended use of the instrument. Keysight Technologies assumes no liability for the customer's failure to comply with these requirements. Product manuals are provided with your instrument on CD-ROM and/or in printed form. Printed manuals are an option for many products. Manuals may also be available on the Web. Go to www.keysight.com and type in your product number in the Search field at the top of the page.

General	Do not use this product in any manner not specified by the manufacturer. The protective features of this product may be impaired if it is used in a manner not specified in the operation instructions.
Before Applying Power	Verify that all safety precautions are taken. Make all connections to the unit before applying power. Note the instrument's external markings described in "Safety Symbols".
Ground the Instrument	If your product is provided with a grounding type power plug, the instrument chassis and cover must be connected to an electrical ground to minimize shock hazard. The ground pin must be firmly connected to an electrical ground (safety ground) terminal at the power outlet. Any interruption of the protective (grounding) conductor or disconnection of the protective earth terminal will cause a potential shock hazard that could result in personal injury.
Fuses	See the user's guide or operator's manual for information about line-fuse replacement. Some instruments contain an internal fuse, which is not user accessible.
Do Not Operate in an Explosive Atmosphere	Do not operate the instrument in the presence of flammable gases or fumes.
Do Not Remove the Instrument Cover	Only qualified, service-trained personnel who are aware of the hazards involved should remove instrument covers. Always disconnect the power cable and any external circuits before removing the instrument cover.
Cleaning	Clean the outside of the instrument with a soft, lint-free, slightly dampened cloth. Do not use detergent or chemical solvents.
Do Not Modify the Instrument	Do not install substitute parts or perform any unauthorized modification to the product. Return the product to a Keysight Sales and Service Office for service and repair to ensure that safety features are maintained.
In Case of Damage	Instruments that appear damaged or defective should be made inoperative and secured against unintended operation until they can be repaired by qualified service personnel.

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WARNING

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Safety Symbols

Table 1 Safety Symbol

Symbol	Description
	Direct current
	Alternating current
	Both direct and alternating current
	Three phase alternating current
	Earth ground terminal
	Protective earth ground terminal
	Frame or chassis ground terminal
	Terminal is at earth potential
	Equipotentiality
N	Neutral conductor on permanently installed equipment
L	Line conductor on permanently installed equipment
	On (mains supply)
	Off (mains supply)
	Stand by (mains supply). The instrument is not completely disconnected from the mains supply when the power switch is in the stand by position
	In position of a bi-stable push switch
	Out position of a bi-stable push switch

Symbol	Description
	Equipment protected throughout by DOUBLE INSULATION or REINFORCED INSULATION
	Caution, refer to accompanying documentation
	Caution, risk of electric shock
	Do not apply around or remove from HAZARDOUS LIVE conductors
	Application around and removal from HAZARDOUS LIVE conductors is permitted
	Caution, hot surface
	Ionizing radiation
CAT I	IEC Measurement Category I
CAT II	Measurement Category II
CAT III	Measurement Category III
CAT IV	Measurement Category IV

Compliance and Environmental Information

Table 2 Compliance and Environmental Information





Safety Symbol	Description
	CSA is the Canadian certification mark to demonstrate compliance with the Safety requirements.
	The C-tick mark is a registered trademark of the Spectrum Management Agency of Australia. This signifies compliance with the Australia EMC Framework regulations under the terms of the Radio Communication Act of 1992.
	CE compliance marking to the EU Safety and EMC Directives. ISM GRP-1A classification according to the international EMC standard. ICES/NMB-001 compliance marking to the Canadian EMC standard.
	KC certification mark to demonstrate compliance with the South Korean EMC requirements. South Korean Class A EMC declaration This equipment is Class A suitable for professional use and is for use in electromagnetic environments outside of the home.

Table 3 Environmental Information

Safety Symbols	Description
	This product complies with the WEEE Directive (2002/96/EC) marking requirements. The affixed label indicates that you must not discard this electrical/electronic product in domestic household waste. <i>Product Category: With reference to the requirement types in the WEEE Directive Annex I, this product is classed as a "Monitoring and Control Instrumentation" product.</i> Do not dispose in domestic household waste.
	To return unwanted products, contact your local Keysight office, or see www.keysight.com/environment/product/ for more information.

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1 Introduction

The U4431A MIPI M-PHY Analyzer module is an acquisition module that captures and decodes data as per MIPI M-PHY standards including UniPro, UFS, SSIC, MPCle, and CSI-3. Using this module, you can get insight into the M-PHY protocol stack.

It allows you to test and troubleshoot the data link, network, transport, and application layers of the M-PHY protocol stack.

You can use multiple U4431A modules in a chassis to test and debug multi-bus systems. You can also time-correlate M-PHY busses with MIPI D-PHY CSI-2 and DSI-1, MPCle, DDR and HDMI busses by using multiple Keysight AXIe based modules in a time-correlated setup.

For the display and analysis of the captured data, the U4431A module presents the data in a Protocol Viewer, Waveform Viewer, and a Listing Data View.

This module needs to be installed in a Keysight AXIe chassis (for example, the M9502A 2 slot chassis). When a controller PC is connected to the AXIe chassis via an external PCIe / USB interface and a cable, the Keysight Logic and Protocol Analyzer application (running on the controller PC) lets you configure, control, and use the U4431A module.

NOTE

You can also use the U4431A module for transmitting stimulus and capturing bidirectional data, that is, capturing the data that it transmits as stimulus as well as the data that it receives from a DUT. For this usage scenario, you need the **Keysight Command Line Packet Generator (CLPG)**, which is available as a licensed software option (U4431A-613) of the U4431A module,

For detailed information on CLPG and how to use it for stimulus transmission and capture, refer to the **Keysight MIPI M-PHY Command Line Packet Generator User Guide** installed with the CLPG software at the following location.

C:\Program Files\Keysight Technologies\Logic Analyzer\Help\pdfs



Protocols Supported

The U4431A module currently supports capture and decode for the following protocols:

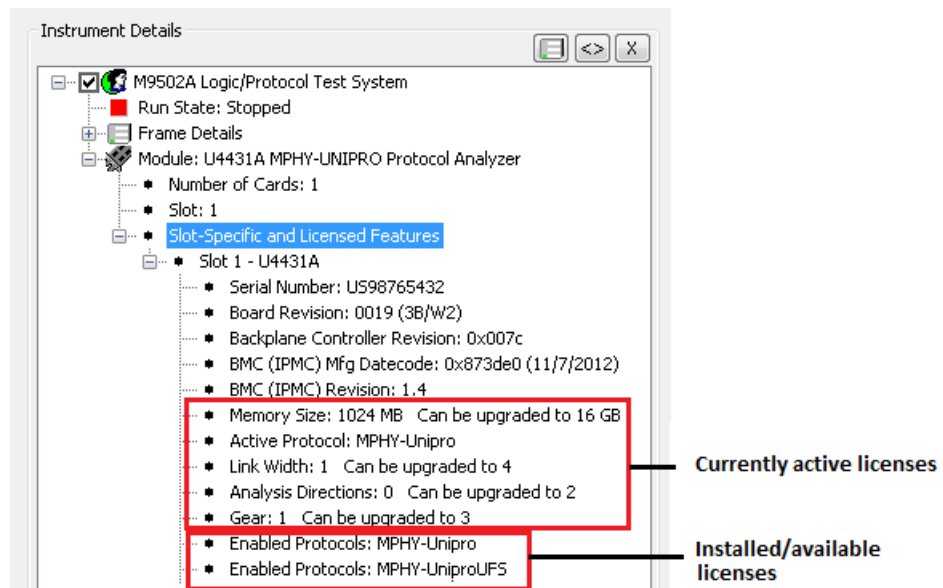
- M-PHY 2.0
- Unified Protocol (UniPro) 1.41.00 and 1.6
- Universal Flash Storage (UFS) v1.1
- SSIC
- CSI-3
- MPCle

By default, the base protocol supported on the U4431A module is M-PHY. As and when needed, you can add other supported protocol capabilities to your U4431A module by purchasing and installing appropriate U4431A licenses. Refer to the U4431A module data sheet (part number 5991-2544EN.pdf) available at www.keysight.com/find/mphy_analyzer to know about various licensed options available for the module.

NOTE

You can view which licenses are currently active/in use on your U4431A module and which licensed options are installed/available for use. For this, you can use the Keysight Notification Center  icon. This icon is displayed as a green dot on the taskbar (lower, right-hand corner of your desktop) if the Keysight Logic and Protocol Analyzer GUI is installed. If the GUI is open and running, the Notification Center icon changes to .

1. Right-click the Notification Center icon and select **Show Instrument Details** from the displayed menu.
2. In the **Instrument Details** dialog box, expand the Module: U4431A Protocol Analyzer node.
3. Note the currently active and available licensed options.



U4431A Features

A U4431A module:

- Can acquire protocol level data as well as raw signal level data.
- Can transmit stimulus to a DUT and also capture the DUT data if the licensed software option Command Line Packet Generator (CLPG) is installed.
- Provides upto 16 GB of memory that you can divide and allocate for capturing raw signal level and protocol level data.
- Supports preempting of UniPro data.
- Supports noise filtering for PWM gears.
- Syncs up to the DUT's link configurations based on the settings that you define.
- Can track DUT's link configurations automatically.
- Supports descrambling of scrambled data for SSIC data acquisition.
- Provides a number of viewers to view the acquired data.
- Allows you to set the following attributes independently for the Transmit and Receive sublinks.
 - Link width
 - x1, x2, x3 and x4 for UniPro/UFS/CSI-3
 - x1, x2, and x4 for SSIC (Same link width is applicable for both sublinks in case of SSIC.)
 - x1, x2, and x4 for PCIe
 - Burst mode - PWM or HS Gear per sublink.
 - PWM Gear 1 to 7 and HS Gear 1, 2, and 3 for UniPro/UFS/CSI-3
 - PWM Gear 1 and HS Gear 1, 2, and 3 for SSIC and PCIe
- Sublink based enabling and disabling of raw signal level and protocol level (UniPro, CSI-3, SSIC, PCIe, and UFS) data capture.
- Sublink based division and allocation of memory between raw and protocol level data acquisition.
- Allows you to set the following attributes at the link level (applicable for both sublinks of the link).
 - Data rate series applicable for the link in the High Speed (HS-Mode).
 - Link Synchronization
 - HS Gear profile (only for SSIC). Same HS Gear is used for both sublinks in case of bidirectional link configuration.
 - Link width profile (applicable for SSIC and PCIe). Same link width is used for both sublinks in case of bidirectional link configuration.
 - PWM Filtering (for UniPro)

NOTE

The U4431A module allows cross-triggering with an external oscilloscope and making time-correlated measurements (using markers).

You can display data captured by the oscilloscope in the Waveform and Listing display windows. This external oscilloscope correlation and data display feature is also referred to as View Scope. To know more about this feature and how to use it with the U4431A Analyzer, refer to the External Oscilloscope Time Correlation and Data Display Online Help.

Related Documents

Besides using this U4431A User Guide, you can also access the following documents that provide related information about the U4431A module.

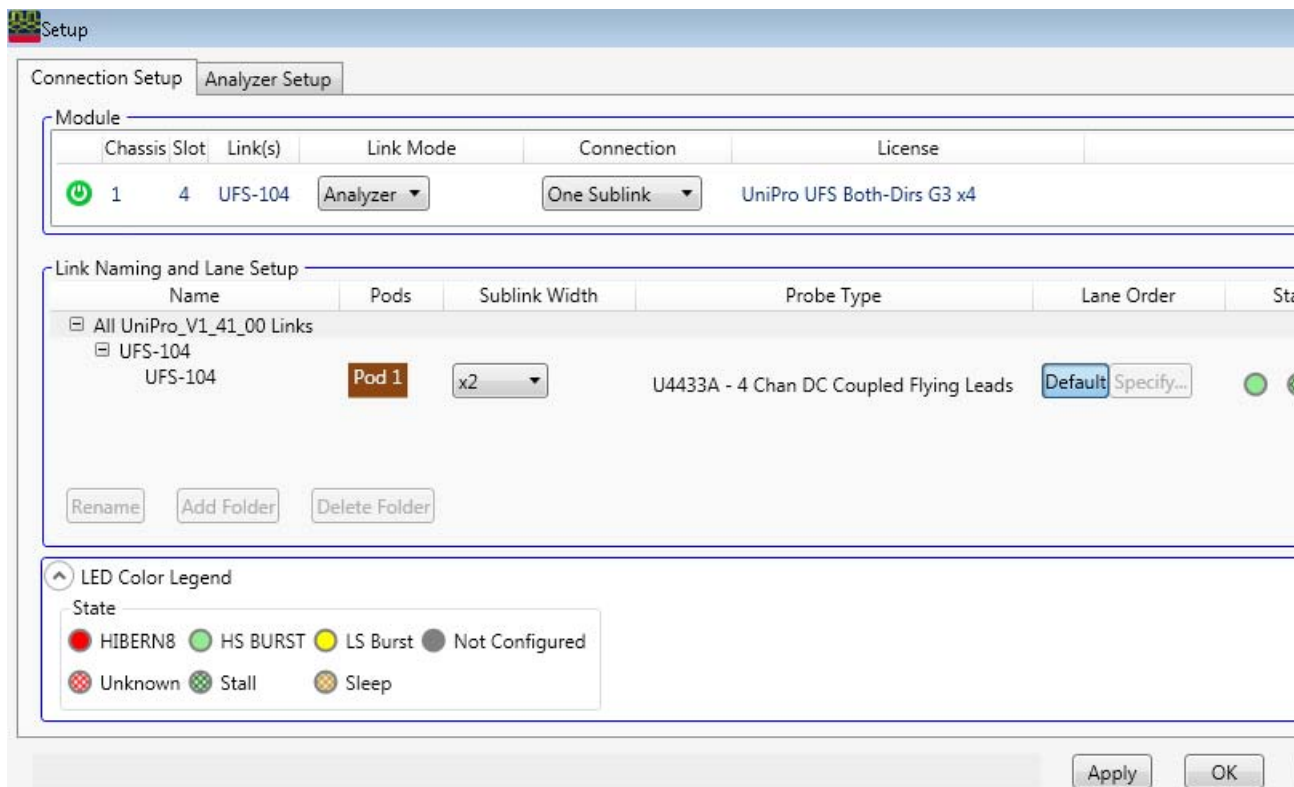
- **MIPI M-PHY Protocol Analyzer Hardware and Probing Guide** – This guide provides information on the acquisition probing options available for use with the U4431A module. This guide is available on www.keysight.com/find/mphy_analyzer and is also installed with the Logic and Protocol Analyzer software at *<logic Analyzer Install location>\help\pdfs*. It describes how to make probing connections from the DUT to the Keysight U4431A module for various probing situations.
- **AXIe Based Logic Analysis and Protocol Test Modules Installation Guide** – This guide provides information on the Keysight AXIe chassis, the U4431A module, how to set up the chassis, module, and host computer and how to obtain and install the associated software components. This guide is available on www.keysight.com/find/mphy_analyzer and is also installed with the Logic and Protocol Analyzer software at *<logic Analyzer Install location>\help\pdfs*.
- **MIPI M-PHY Protocol Analyzer and Exerciser Online Help** – This online help describes how to configure and use the U4431A module to capture M-PHY data. This online help is integrated with the Logic and Protocol Analyzer software and is accessible from its Help menu.
- **Context-sensitive help** – A context-sensitive HTML help page is available with each window and dialog box of the U4431A module on clicking the Help button displayed within the GUI element.
- **Keysight MIPI M-PHY Command Line Packet Generator User Guide** – This guide describes how to use the Keysight Command Line Packet Generator tool for stimulus generation on an M-PHY link and for capturing bidirectional M-PHY data. This tool is a licensed software option for use with the Keysight U4431A MIPI M-PHY Protocol Analyzer module. This guide gets installed with the CLPG software at *<logic Analyzer Install location>\help\pdfs*.

2 Configuring U4431A Connection Settings

By default, you can set up a single direction (Tx or Rx) link between the U4431A module and DUT for data capture.

- If you have the Bidirectional (Tx and Rx) license installed, you can set up a dual-simplex link for capturing Tx as well as Rx data. The module allows you to use one or more unidirectional lanes for each direction. You can choose different number of lanes and lane properties for each direction except in case of an SSIC setup in which same link width is used for both sublinks.
- If you have the Command Line Packet Generator (CLPG) license installed, you can set up a Data Generation + Capture link, thereby sending stimulus from U4431A to a DUT and capturing the DUT data.

You set up a link by configuring the module's connection setup in the Keysight Logic and Protocol Analyzer application after completing the hardware setup for the U4431A module in the AXIe chassis and connecting it to the DUT using appropriate probing hardware. You use the **Connection Setup** tab of the U4431A module's **Setup** dialog to configure its connection setup.



The connection setup details that you specify in this tab tells the Logic and Protocol Analyzer software how the U4431A module is connected to the DUT for data capture.

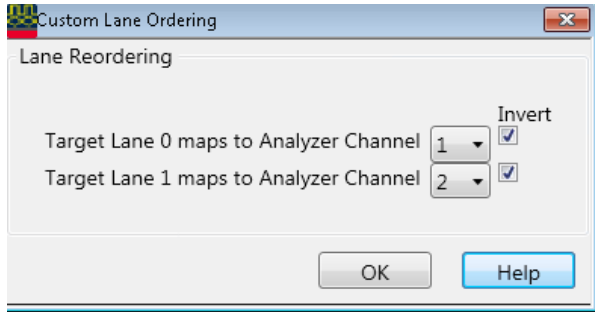
To configure U4431A connection setup

- 1 In the Logic and Protocol Analyzer GUI, access the Setup dialog box of the U4431A module by clicking **Setup** > **Setup** from the GUI's menubar.

The **Connection Setup** tab is displayed in the Setup dialog box.

- 2 Configure connection settings in this tab. The tab has the following fields.

Field	Description
Module - This group box displays read-only and editable fields for the U4431A module hardware.	
Chassis	Read-only field that displays the AXIe chassis number in which you installed the U4431A hardware module.
Slot	Read-only field that displays the slot number of the AXIe chassis in which you installed the U4431A hardware module.
Link	Displays the type of link between the U4431A module and DUT based on the protocol family that you selected at the time of starting up the Logic and Protocol Analyzer GUI session.
Link Mode	<p>Currently, U4431A supports the following connection modes.</p> <ul style="list-style-type: none"> ▪ Analyzer - In this mode, the U4431A module can only be used as an analyzer to passively probe and acquire data from DUTs. This mode doesn't allow the usage of the U4431A module as an Exerciser to stimulate the DUT. In this mode, U4431A can acquire both protocol level and raw signal level data simultaneously. ▪ Data Gen + Analyzer - This mode is applicable only when you have installed the Keysight Command Line Packet Generator (CLPG), which is a licensed software option (U4431A-613) of the U4431A module. Selecting this mode ensures that the U4431A link is configured as a packet generator for stimulus transmission as well as an analyzer for data capture. For more information on CLPG and stimulus transmission, refer to the <i>Keysight MIPI M-PHY Command Line Packet Generator User Guide</i> installed with the CLPG software at C:\Program Files\Keysight Technologies\Logic Analyzer\Help\pdfs.
Connection	<p>Allows you to select the type of connection you want to set up between the U4431A module and DUT for data capture. The following two connection options are available.</p> <ul style="list-style-type: none"> ▪ One Sublink - This is the default option. It allows you to set up a single unidirectional link (Tx or Rx) between the U4431A module and DUT. ▪ Both Sublinks - This is a licensed option. It allows you to set up a bidirectional link (Tx as well as Rx) between the U4431A module and DUT. When you select this option, you get options in the Link Naming and Lane Setup section to set up a Tx and an Rx sublink independently of each other. <p>Note: If you have selected the Data Generator + Analyzer link mode for U4431A, the One Sublink option means that the sublink will only be used for capturing data transmitted from DUT. The data transmitted from the U4431A module will not be captured. The Both Sublinks option in this case means that the sublink on Pod 1 will be used for capturing data transmitted from DUT and the sublink on Pod 2 will be used for capturing the data transmitted as stimulus by the U4431A module. For more information on Data Generation + Analysis scenario, refer to the <i>Keysight MIPI M-PHY Command Line Packet Generator User Guide</i> installed with the CLPG software at C:\Program Files\Keysight Technologies\Logic Analyzer\Help\pdfs.</p>
License	Displays the type of U4431A license(s) currently installed on the system.
Link Naming and Lane Setup	
Name	<p>Displays the name of the M-PHY link that you set up. For a bidirectional link, the names of both Tx and Rx sublinks are displayed.</p> <p>If needed, you can rename these links.</p>
Pods	<p>The U4431A module has two pods labeled as Pod 1 and Pod 2 on its front panel. These pods are used to connect the module to the DUT using probe cables.</p> <p>For a single sublink (Tx or Rx), Pod 1 is used and displayed in this field.</p> <p>For Both sublinks (Tx and Rx), Pod 1 and Pod 2 are used (one for each direction).</p> <p>Ensure that you set up the probing hardware configuration appropriately matching the pod used and displayed for the link. For detailed information on probing, refer to the <i>U4431A MIPI M-PHY Analyzer - Hardware and Probing Guide</i>. The guide is available on www.keysight.com/find/mphy_analyzer and also installed with the Keysight Logic and Protocol Analyzer software.</p>

Field	Description
Sublink Width	<p>Select the maximum link width capability that you want to define for the sublink (Tx or Rx). For instance, if you select x4 in this listbox, then you can set x1 to x4 as the initial or current link width of this sublink in the Analyzer Setup tab. This listbox provides options to select a link width from x1 to x4 individually for each sublink. However, some of the options in this listbox are not available if you do not have the appropriate link width license installed. By default, only x1 is available as the link width. Other options are licensed.</p> <p>NOTES</p> <ul style="list-style-type: none"> - Ensure that you make appropriate hardware and probing setup for the U4431A module to match the link width capability you want to set in the Sublink Width field. - For a bidirectional SSIC setup, the same link width is applicable for both sublinks.
Probe Type	<p>Detects and displays the probing option that you have used in the U4431A hardware setup to probe the DUT. The following probing options are available for use with the U4431A module:</p> <ul style="list-style-type: none"> ▪ U4433A Differential ZIF Flying Lead probe ▪ U4432A SMA probe <p>For detailed information on probing, refer to the <i>U4431A MIPI M-PHY Analyzer - Hardware and Probing Guide</i>. The guide is available on www.keysight.com/find/mphy_analyzer and also installed with the Keysight Logic and Protocol Analyzer software.</p>
Lane Order	<p>The Lane Order option lets you perform the ordering of the physical probe channels of the U4431A module with the logical data lanes probed by each of these channels. You can either retain the <i>Default</i> lane ordering which means channel 0 of the module maps to logical lane 0 and so on. If you want to change this default mapping of channels with logical lanes, then select the <i>Custom</i> option from Lane Order and click <i>Specify</i> to display the <i>Custom Lane Ordering</i> dialog box. In this dialog box, select the module's channel with which you want to map a target logical lane.</p> <p>You can also set the polarity inversion property for each data lane using the <i>Custom Lane Ordering</i> dialog box. The polarity of a data lane is not inverted when the positive and negative sides of the probe channel's differential pair are connected to the positive and negative sides of the signal in the DUT.</p> 
State Activity	<p>Displays the current status of the four data lanes LEDs located on the front panel of the U4431A module. The following color coding is used for these LEDs to indicate the state activity on the associated lanes.</p> <ul style="list-style-type: none"> ▪ Red - This means that the lane is in the ultra-low power state (HIBERN8) without loss of configuration information. ▪ Blinking Red - This means that the lane state is unknown. This can happen when the U4431A module is in the process of syncing up to the current link configurations. ▪ Green - This means that the lane is transmitting HS-BURST in HS-MODE. ▪ Blinking Green - This means that the lane is in the STALL state - the power saving state (periods of inactivity) when operating in HS-MODE. ▪ Yellow - This means that the lane is transmitting LS-BURST in Low Speed-MODE. ▪ Blinking Yellow - This means that the lane is in the SLEEP state - the power saving state (periods of inactivity) when operating in Low Speed-MODE. ▪ Off - This means that the lane is not configured/available. Based on the link width that you select in the U4431 Connection Setup tab of the Logic and Protocol Analyzer GUI, the number of lanes on the U4431A module are used. The LEDs of only the used lanes glow. For example, if you are using the x2 link width, then the LEDs of only two lanes being used will glow and the rest of the LEDs will be off. <p>You can also refer to the LED Color Legend section at the bottom of the tab to know what each LED color represents.</p>
LED Color Legend	<p>This section displays a description for various colors of the data lanes status LEDs on the front panel of the U4431A module. Each LED color represents a specific state activity on the associated lane. Refer to the <i>State Activity</i> field description in this table to know more about the state activity on lanes.</p>

3 Switching M-PHY Protocols

If you have a single (Unipro, Unipro+UFS, Unipro+CSI-3, or SSIC) protocol license for the U4431A module, then by default, the Logic and Protocol Analyzer GUI is started using that licensed protocol option when you launch this GUI.

If you have purchased multiple protocol licenses for the U4431A module, then the Logic and Protocol Analyzer GUI is started using the currently selected protocol option. In situations when multiple protocol licenses are available, you can choose the protocol license option with which you want to launch the Logic and Protocol Analyzer GUI. This feature allows you to switch among the Unipro, Unipro+UFS, Unipro+CSI-3, MPCle, and SSIC analysis when using the U4431A module with these multiple protocol license options.

You use the U4421A-U4431A-Protocol-Selector.bat script to select Unipro, Unipro+UFS, Unipro+CSI-3, MPCle or SSIC protocol when accessing the Logic and Protocol Analyzer GUI.

NOTE

The U4421A-U4431A-Protocol-Selector.bat script puts any active Logic and Protocol Analyzer GUI sessions to the offline mode during protocol change. Therefore, you must stop any ongoing analysis work and save your captured data/configurations before you start using this script.

To select M-Phy protocols (Unipro, UFS, CSI-3, MPCle or SSIC)

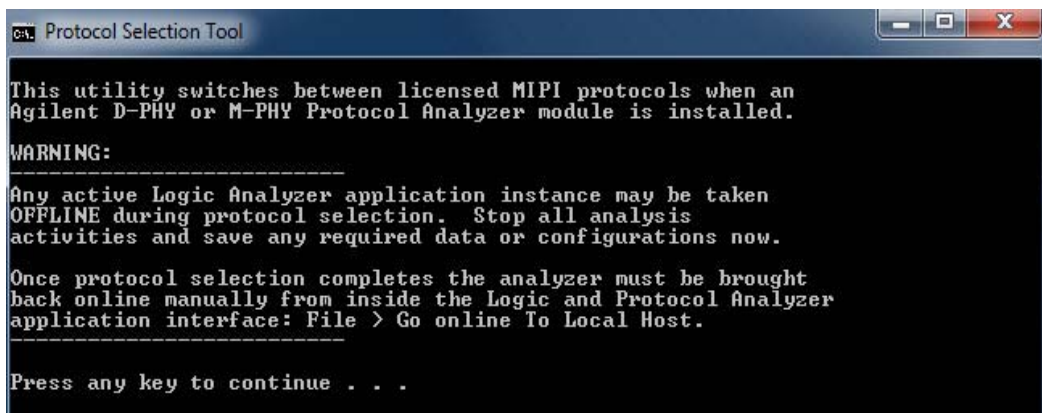
- 1 Double-click the U4421A-U4431A-Protocol-Selector.bat script to launch it. The script is installed at the following location when you install the Keysight Logic and Protocol Analyzer software (version 5.80.0000 or later).

C:\Program Files (x86)\Keysight Technologies\Logic Analyzer\Contributed Files (for 64 bit OS installs)

or

C:\Program Files\Keysight Technologies\Logic Analyzer\Contributed Files (for 32 bit OS installs)

- 2 The script starts running in the Command window with warning messages displayed. Press any key to continue.



```
C:\> Protocol Selection Tool

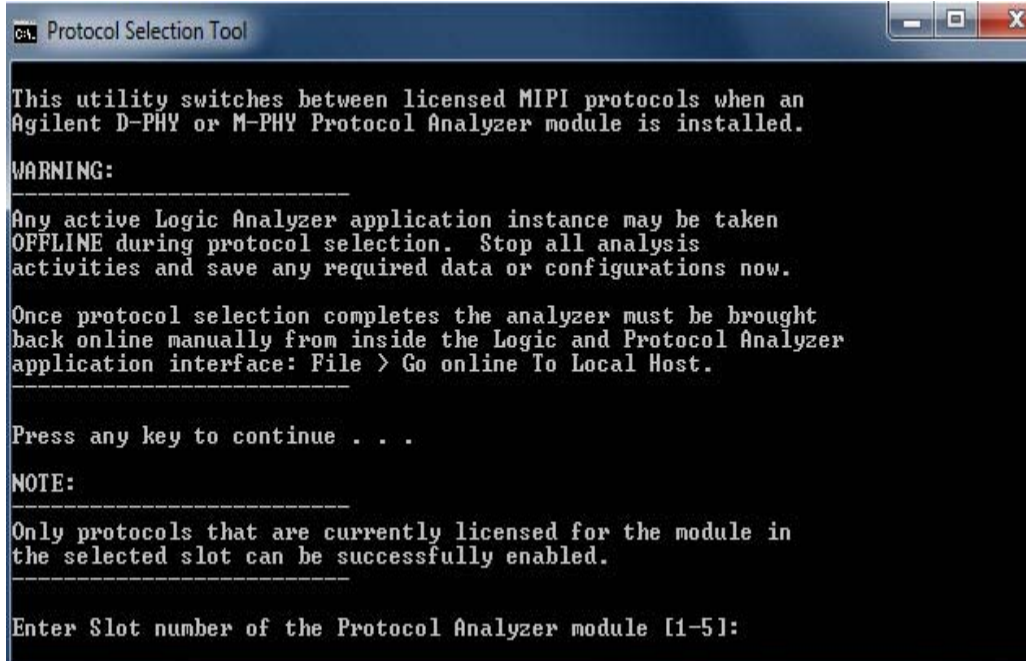
This utility switches between licensed MIPI protocols when an
Agilent D-PHY or M-PHY Protocol Analyzer module is installed.

WARNING:
-----
Any active Logic Analyzer application instance may be taken
OFFLINE during protocol selection. Stop all analysis
activities and save any required data or configurations now.

Once protocol selection completes the analyzer must be brought
back online manually from inside the Logic and Protocol Analyzer
application interface: File > Go online To Local Host.
-----

Press any key to continue . . .
```

- 3 Specify the slot number in which you installed the U4431A module for which you want to select the protocol. Press <Enter>.

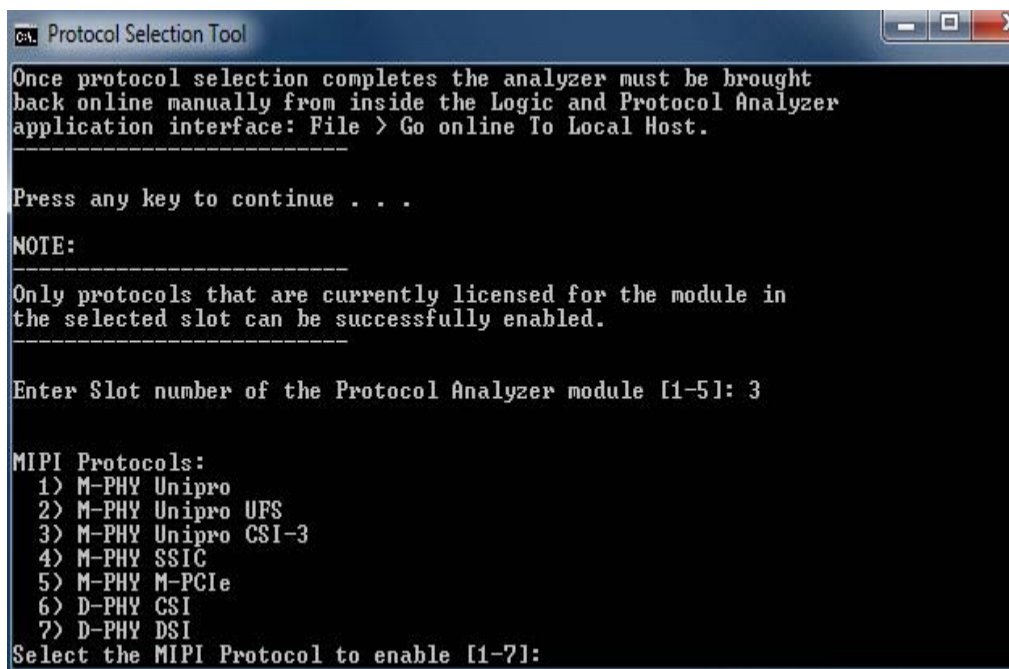
**NOTE**

If an invalid slot is used or an input other than the number 1 to 5 is entered (for example- any string), the script will run as usual, but the background operation that the script performs will not succeed. The script will crash in case a string with a space is entered.

- 4 Specify the MIPI protocol that you want to select for the module. Specify M-phy Unipro to select the Unipro protocol, M-phy Unipro UFS to select the Unipro+UFS protocol, M-phy Unipro CSI-3 to select the Unipro+CSI-3 protocol, and M-phy SSIC to select the SSIC protocol. Press <Enter>.

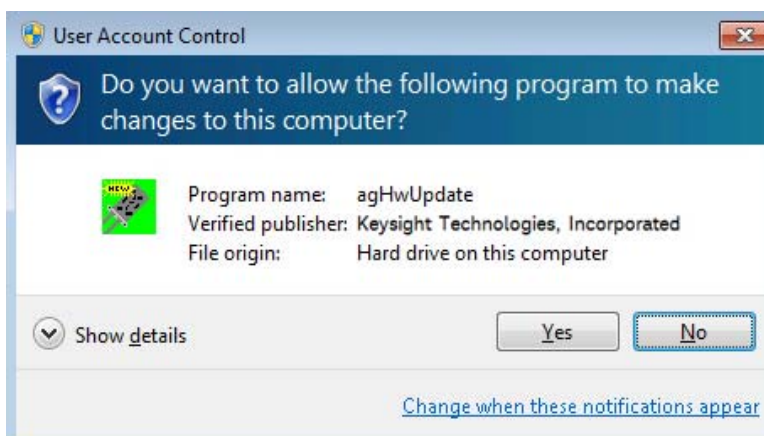
NOTE

UFS and CSI-3 protocol licenses are available in combination with the Unipro license.

**NOTE**

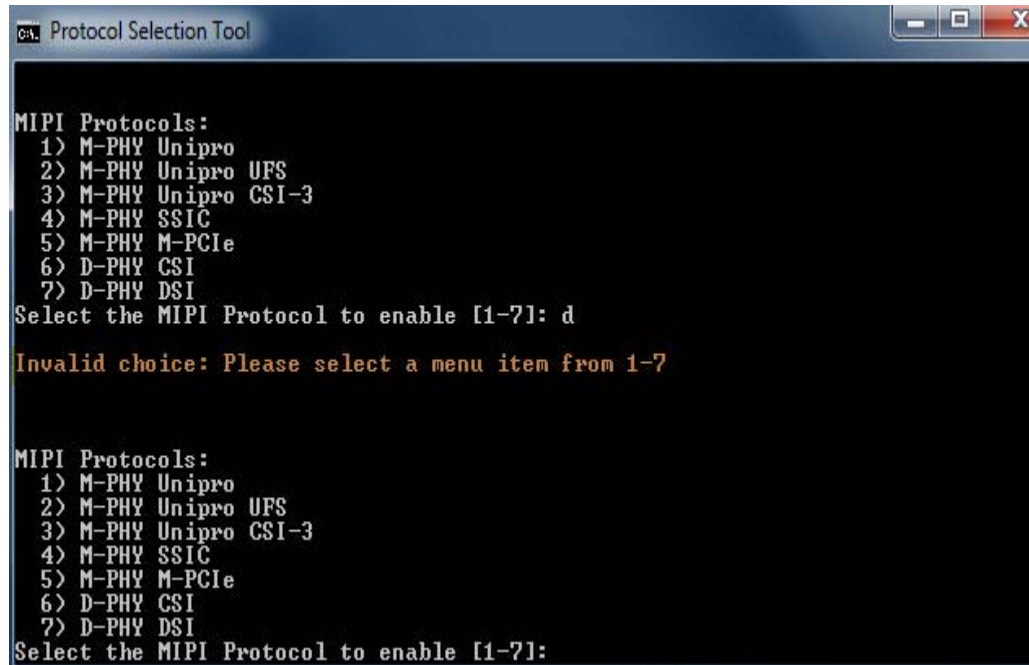
The CSI and DSI options are available for the U4421A MIPI D-PHY module and these options will be ignored for the U4431A module.

- 5 The **User Account Control** dialog box is displayed. Click **Yes** to proceed.



The script runs to perform the protocol change as per your selections. The protocol change is successfully completed if you have specified the correct slot number for the U4431A module and appropriate license is available for the selected protocol.

If an invalid option is entered (other than the number 1 to 7), the script fails to execute and keeps on prompting until a valid choice is made.



```
Protocol Selection Tool

MIPI Protocols:
1> M-PHY Unipro
2> M-PHY Unipro UFS
3> M-PHY Unipro CSI-3
4> M-PHY SSIC
5> M-PHY M-PCIE
6> D-PHY CSI
7> D-PHY DSI
Select the MIPI Protocol to enable [1-7]: d

Invalid choice: Please select a menu item from 1-7

MIPI Protocols:
1> M-PHY Unipro
2> M-PHY Unipro UFS
3> M-PHY Unipro CSI-3
4> M-PHY SSIC
5> M-PHY M-PCIE
6> D-PHY CSI
7> D-PHY DSI
Select the MIPI Protocol to enable [1-7]:
```

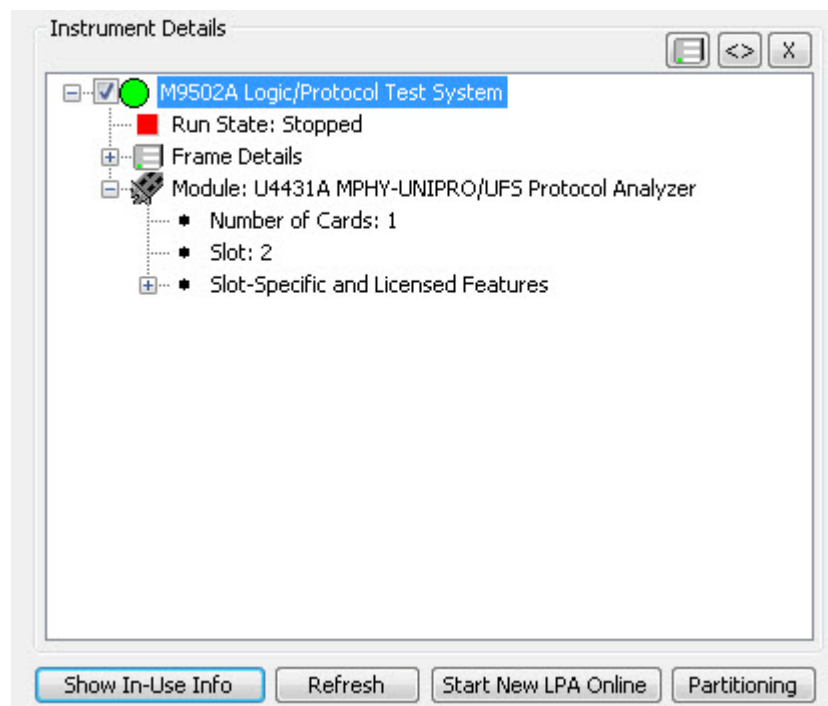
- 6 When the protocol change completes, you need to bring any open offline sessions of the Logic and Protocol Analyzer GUI to the Online mode by clicking File > Go online to Local Host option in the GUI's menubar. If there are no existing open sessions of the GUI, launch the GUI in the Online mode.

The Logic and Protocol Analyzer GUI should now launch with the selected protocol.

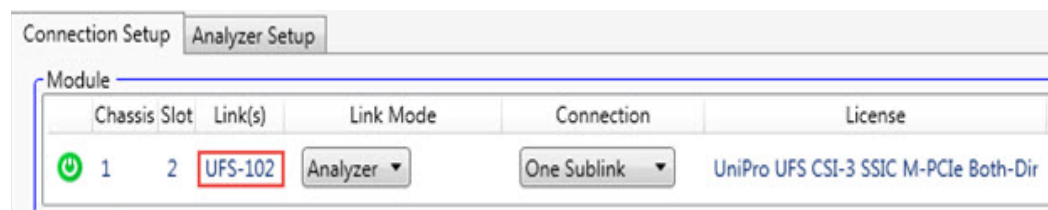
To verify if protocol change is successful

- Using the Keysight Notification Center icon
 - 1 Double-click the **Keysight Notification Center** icon displayed in Window's taskbar.
 - 2 In the **Instrument Details** dialog box, expand the U4431A module option for which you changed the protocol.

The protocol currently selected for the module is displayed.



- Using the Logic and Protocol Analyzer GUI
 - 1 Launch the Logic and Protocol Analyzer GUI in the Online mode.
 - 2 Click **Setup > Setup** from the GUI's menubar to access the **Setup** dialog box of the U4431A module for which you changed the protocol.
 - 3 In the **Connection Setup** tab, the currently selected protocol (Unipro, UFS, CSI-3, MPCle or SSIC) for the module is displayed.



4 Capturing M-PHY Data

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This chapter provides information on how to configure the U4431A module as an analyzer to capture M-PHY data.

Before you Start

Before you start capturing data, ensure that the following tasks are performed.

- You have connected the U4431A module to the DUT using the appropriate acquisition probe in the required configurations.
- You have installed the Keysight Logic and Protocol Analyzer GUI, version 5.80.0000 or higher.
- You have configured the U4431A module's connection setup in the Keysight Logic and Protocol Analyzer GUI.
- You have specified the sync related settings in the Analyzer Setup tab. The U4431A module uses these settings to sync up to the DUT's link configurations before data capture.

Supported Probes

The following probes are supported for use with the U4431A module.

- **U4433A Differential ZIF Flying Lead probe** - Can probe individual signals from multiple randomly located points on the target system. For detailed probing information and how to make connections with the U4431A module and DUT, refer to the *U4431A MIPI M-PHY Analyzer Hardware and Probing Guide*. The guide is available on www.keysight.com/find/mphy_analyzer and also installed with the Keysight Logic and Protocol Analyzer software.
- **U4432A SMA probe** - Provides coaxial SMA connections for each of the lines of the M-PHY bus. You can use the U4432A SMA probe in the following two scenarios with the U4431A module.
 - **For Data Capture** - For detailed probing information and how to make connections with the U4431A module and DUT, refer to the *U4431A MIPI M-PHY Analyzer Hardware and Probing Guide*. The guide is available on www.keysight.com/find/mphy_analyzer and also installed with the Keysight Logic and Protocol Analyzer software.
 - **For Packet Generation as well as data capture** - You can also use the U4432A SMA probe with the Command Line Packet Generator (CLPG) option (U4431-613) of the U4431A module to transmit stimulus and receive DUT data. You can find the details of this usage scenario in the *Keysight MipiMphy Command Line Packet Generator User Guide* which is installed with the Keysight Command Line Packet Generator software.

Configuring Data Capture and Sync Settings

You can configure the data capture settings at the link and sublink(s) levels using the **Analyzer Setup** tab in the **Setup** dialog box of the Logic And Protocol Analyzer GUI.

The U4431A module performs the sync process before it starts data capture to sync up to the DUT's link configurations such as link width, burst mode, link speed, and HS rate series. To enable the U4431A module to perform this sync process, you define link/sublink settings that the module should use during this sync process and also instruct the U4431A module on how it should perform this sync process. You use the **Analyzer Setup** tab to define these settings.

NOTE

The U4431A module does not start capturing data until it completes the sync process.

The following screen displays the capture settings fields available for a bidirectional UniPro/UFS setup.

The screenshot shows the 'Setup' window with the 'Analyzer Setup' tab selected. The interface is divided into several sections:

- Link Setup:** Includes 'Enable preemption support' (unchecked), 'PWM Filtering Level' (set to 'All Data Rates'), and 'UniPro Spec Version' (set to '1.6').
- Link Configuration:** Includes 'HS Rate' (set to 'A-Series'), 'Link Configuration Tracking' (set to 'Track Link Configuration automatically'), 'Sync on exit from hibern8 or power off' (unchecked), and 'Direction to monitor for' (set to 'UFS-005A (Pod 1)').
- Sublink Setup:** Divided into two columns for 'UFS-005A (Pod 1)' and 'UFS-005B (Pod 2)'. Each column has:
 - Raw Data Capture:** 'Raw Data' (checked), 'Depth' (set to '1 MB'), and 'Mode' (set to '8B Deskewed Data').
 - Protocol Data Capture:** 'Unipro Data' (checked), 'UFS Data' (checked), and 'Depth' (set to '1 MB').
 - Sublink Configuration:** 'Initial or current burst mode' (set to 'PWM Gear 1'), 'Initial or current link width' (set to 'x1'), and 'Initial or current data is scrambled' (unchecked).
- Link Synchronization:** Includes a 'Sync Analyzer to Link Configuration' button, 'Sync Status' (set to 'In Sync'), and 'UFS-005B (Pod 2)' (set to 'Off').

The **Analyzer Setup** tab has the following fields that you can modify to configure the capture and sync related settings.

Some fields are license-specific and are displayed only when an appropriate U4431A license is installed.

NOTE

If you configured the U4431A module's link mode as Data Generation + Analyzer and Connection as Both Sublinks, then:

- the settings you configure in the Pod 1 Sublink Setup section are applicable for capturing data transmitted from DUT
- the settings you configure in the Pod 2 Sublink Setup section are applicable for capturing data transmitted from U4431A.

For detailed information on this usage scenario, refer to the Keysight MIPI M-PHY Command Line Packet Generator User Guide installed with the Command Line Packet Generator (CLPG) software at the following location.

C:\Program Files\Keysight Technologies\Logic Analyzer\Help\pdfs

Field	Description
Link Setup	
Enable Preemption Support (Applicable only for a UniPro setup)	<p>Allows you to enable or disable the preemption support for the link. The preemption support is based on the UniPro specifications. Therefore, this option is applicable and visible only when you install the UniPro specific license on the system.</p> <p>When this support is enabled, the U4431A module identifies and supports preemption in the captured data, for instance, preemption for higher priority frames into lower priority data frames. For preemption, U4431A supports the nesting limited to level 1.</p> <p>With preemption enabled, you can view the preempted frames in the captured trace displayed in the Protocol Viewer.</p>
Data on link is scrambled (Applicable for SSIC and MPCle setup)	<p>Allows you to instruct the U4431A module on whether or not the data on the link is scrambled. Depending on the protocol license that is either SSIC or MPCle whichever is currently applicable, the scrambling is based on the SSIC or MPCle specifications. Therefore, this option is applicable and visible only when you install the SSIC or MPCle specific license on the system.</p> <p>If you select this checkbox, the U4431A module expect scrambled data on the link and enables descrambling of the scrambled data.</p> <p>If you do not select this checkbox, the U4431A module does not expect scrambled data and therefore descrambling is not enabled and the data is presented as is.</p> <p>You can view the descrambled data in the captured trace using the Waveform Viewer.</p> <p>To know how to view scrambled data, refer to the topic "Viewing Deskewed and Descrambled Data" on page 72.</p>
PWM Filtering Level	<p>Allows you to instruct the U4431A module on whether or not it should implement the noise filtering when capturing PWM signals which are more prone to noise. This listbox also allows you to set the amount of noise filtering to be applied for PWM gears based on the actual bit rate being transmitted by the DUT. You can select from the following filtering options.</p> <ul style="list-style-type: none"> ▪ No Filtering - This option means that there is no noise filtering applied for PWM gears. ▪ All Data Rates - This option means that the U4431A module applies the maximum possible filtering even when the DUT is transmitting at the maximum data rate from the data rate range defined for all PWM gears. This is the default setting and is suitable for most of the situations. ▪ Rates < = 75% Max - This option means that the U4431A module applies more filtering than applicable for the All Data Rates option. However, for this option, the DUT should transmit at less than or equal to 75% of the maximum data rate from the data rate range defined for all PWM gears. ▪ Rates < = 25% Max - This option means that the U4431A module applies more filtering than applicable for the Rates < = 75% Max option. However, for this option, the DUT should transmit at less than or equal to 25% of the maximum data rate from the data rate range defined for all PWM gears. <p>NOTE: PWM Filtering is applicable only to lane 0 of each sublink. The U4431A module does not perform PWM filtering for lanes 1-3. Therefore, to ensure that you get good data in case of multilane PWM configurations, enable terminations on the link when the link is in the PWM mode. This can usually be done by issuing a power mode request.</p>

Field	Description
HS Profile (Applicable only for an SSIC setup)	<p>This option is displayed only when you install the SSIC license for the U4431A module. The option is enabled only when you select the Track Link Configuration Automatically option in this tab.</p> <p>The HS Profile option has the following two components:</p> <ul style="list-style-type: none"> ▪ Gear - Allows you to set the HS Gear profile for the U4431A module. This setting is applicable for both sublinks in case of a bidirectional link configuration. The U4431A module can dynamically change to this profile in response to the changes in the probed link's configurations. For instance, you can set the initial burst mode as PWM Gear 1 in the Initial or current burst mode listbox and set the HS Profile to HS Gear 2. This will ensure that initially, PWM Gear 1 is used as the burst mode and then later HS Gear 2 is used in response to the probed link's configuration changes. ▪ Width - This is a read-only field that displays the link width that you configured for the U4431A module in the Connection Setup tab. This link width profile is applicable for both sublinks in case of a bidirectional link configuration.
UniPro Spec Version (Applicable only for a UniPro setup)	<p>Allows you to select the version of the UniPro specifications based on which you want the U4431A module to decode and present the captured data. For instance, if you select the 1.6 version, you get an option to instruct the U4431A module on whether or not the data on a sublink is scrambled.</p> <p>The listbox is visible only when you install the UniPro specific license.</p>
Link Configuration	
HS Rate	<p>Select the data rate series at which you want the U4431A module to capture data in the High Speed (HS-Mode). The selected rate series is applicable for both sublinks in the HS-Mode.</p> <p>As per the M-PHY specifications, the following two Data Rate series are available for selection:</p> <ul style="list-style-type: none"> ▪ A-Series ▪ B-Series <p>The data rate applicable for various gears in these series is as per the data rate defined in the M-PHY specifications.</p>
Link Configuration Tracking	<p>Allows you to configure how you want the U4431A module to track and act for the changes in the probed link configurations such as link speed and link width.</p> <ul style="list-style-type: none"> ▪ Track Link Configuration Automatically - Select this option to ensure that the U4431A module automatically tracks the probed link's configuration changes such as changes in sublink speed and width. On enabling this option, the U4431A module automatically changes its link configurations in response to the changes in the probed link's configurations. Such automatic tracking and adjustments in link configurations are then viewable in the data captured and displayed in the Protocol Viewer. ▪ Lock Analyzer at Specified Link Configuration - Select this option to ensure that the link configurations such as sublink speed and sublink width of the U4431A module are fixed to the values that you currently selected in the Sublink Setup group box. On selecting this option, the U4431A module does not change its link configurations automatically in response to the changes in the probed link's configurations. When locked to a specific configuration, the U4431A module will capture data specific to only that configuration.
Sync on exit from hibern8 or power off	<p>Select this checkbox to instruct the U4431A module to sync up to the DUT configurations on DUT's exit from the hibern8 or power off state.</p> <p>This option is disabled and therefore fixed if you select the Lock Analyzer at Specified Link Configuration option. This option is enabled only when you have instructed the U4431A module to track and respond to the changes in the probed link configurations automatically.</p> <p>Selecting this checkbox is particularly useful when the U4431A module is in the process of syncing to the DUT's configurations and you want to convey the hibern8 or power off state of DUT to the U4431A module. When you select this checkbox, the U4431A module gets to know about the hibern8 or powered off state of DUT and then consequently starts ignoring all data from the DUT until it detects the DUT's exit from HIBERN8.</p> <p>To ensure that the dynamic changes to this checkbox take effect, you also need to click the <i>Sync Analyzer to Link Configuration</i> button after selecting/deselecting this checkbox.</p> <p>NOTE: When the U4431A module completes its sync process, it can automatically track the DUT's entry and exit from hibern8.</p>
Direction to monitor for configuration tracking	<p>This option is available only when you have chosen to analyze both Tx and Rx sublinks (Both Sublinks option in the Connection Setup tab).</p> <p>This option allows you to select the sublink that you want the U4431A module to monitor for tracking the link configuration requests.</p> <p>NOTE: This field is not available for a "Both Sublinks" configuration if you have configured the U4431A module in the Packet Generator + Analyzer link mode. In this link mode, the U4431A module always monitors the sublink on Pod 1 (used for DUT data capture) for tracking the link configuration requests. The sublink on Pod 2 is not used for this purpose in any scenario.</p>
Sublink setup	

Field	Description
<p>Raw Data Capture - The U4431A module can capture both protocol level and raw signal level data simultaneously. The raw data capture allows you to view the time-correlated 8b/10b data or 8b Deskewed data that underlies a protocol. This data can be displayed in the Waveform or Listing viewer, providing insight into how a packet is formed at the physical layer.</p> <p>Note: When you configure the U4431A module in the Data Generator + Analyzer mode, the raw data capture is not supported for capturing the data transmitted from the U4431A module. Therefore, in such a scenario, the Raw Data Capture section is disabled for the Pod 2 sublink setup.</p> <p>The Raw Data Capture group box contains the following fields to enable and set the raw data capture setting for a sublink.</p>	
Raw Data	<p>You can choose to enable or disable the raw signal level data capture for the applicable sublink.</p> <p>You can instruct the U4431A module to capture the raw signal level data by selecting the Enable checkbox displayed with the Raw Data field.</p>
Depth	<p>You can divide the total memory depth that is licensed and available for the U4431A module between the configured sublink(s) of the link. For each sublink, you further divide the allocated memory between the protocol level data acquisition and raw signal level data acquisition (if enabled).</p> <p>From the Depth listbox, select the memory depth that you want to allocate to the acquisition of the raw signal level data for the sublink. This listbox is disabled if you deselect the Enable checkbox displayed with the Raw Data field.</p> <p>You can allocate a value ranging from 64 KB to 4 GB as the raw data capture memory depth.</p>
Mode	<p>Select the mode of raw signal level data acquisition. You can select from the following three options:</p> <ul style="list-style-type: none"> ▪ 8b Data - Select this option if you want to display 8b symbol data. Data will not be deskewed in this case. ▪ 10b data - Select this option if you want to display 10b raw data. Data will not be deskewed/descrambled in this case. ▪ 8b Deskewed Data - Select this option if you want the acquired data to be deskewed and aligned across lanes. ▪ 8B Descrambled and Deskewed Data - This option is applicable for SSIC and MPCle setup. Select this option if you are expecting scrambled data on the sublink and want the U4431A module to descramble and deskew the acquired data. On selecting this option, the module will, however, descramble the acquired data only if the Data on link is scrambled checkbox is selected. ▪ 8B Descrambled Data - This option is applicable only for an SSIC setup. Select this option if you are expecting scrambled data on the sublink and want the U4431A module to descramble the acquired data. On selecting this option, the module will, however, descramble the acquired data only if the Data on link is scrambled checkbox is selected.
<p>Protocol Data Capture - The U4431A module can capture both protocol level and raw signal level data simultaneously. The protocol data capture allows you to view the protocol level data in the Protocol viewer, providing an insight into packet trace and packet debug.</p> <p>The Protocol Data Capture group box contains the following fields to enable and set the protocol data capture setting for a sublink.</p>	
UniPro Data (Applicable only for a UniPro setup)	<p>You can choose to enable or disable the decoding and extraction of the UniPro protocol level data for the applicable sublink. You can instruct the U4431A module to decode and extract the UniPro protocol level data by selecting the Enable checkbox displayed with the UniPro Data field. If this checkbox is not selected, the UniPro protocol level data is not available for viewing in the Protocol Viewer tool of the Keysight Logic and Protocol Analyzer application.</p> <p>This field is displayed only when you install the UniPro specific license for the U4431A module.</p>
UFS Data (Applicable only for a UFS setup)	<p>You can choose to enable or disable the decoding and extraction of the UFS protocol level data for the applicable sublink. You can instruct the U4431A module to decode and extract the UFS protocol level data by selecting the Enable checkbox displayed with the UFS Data field. If this checkbox is not selected, the UFS protocol level data is not available for viewing in the Protocol Viewer tool of the Keysight Logic and Protocol Analyzer application.</p> <p>This field is displayed only when you install the UniPro+UFS license for the U4431A module.</p>
CSI-3 Data (Applicable only for a CSI-3 setup)	<p>You can choose to enable or disable the decoding and extraction of the CSI-3 protocol level data for the applicable sublink. You can instruct the U4431A module to decode and extract the CSI-3 protocol level data by selecting the Enable checkbox displayed with the CSI-3 Data field. If this checkbox is not selected, the CSI-3 protocol level data is not available for viewing in the Protocol Viewer tool of the Keysight Logic and Protocol Analyzer application.</p> <p>This field is displayed only when you install the UniPro+CSI-3 license for the U4431A module.</p>
SSIC Data (Applicable only for an SSIC setup)	<p>You can choose to enable or disable the decoding and extraction of the SSIC protocol level data for the applicable sublink. You can instruct the U4431A module to decode and extract the SSIC protocol level data by selecting the Enable checkbox displayed with the SSIC Data field. If this checkbox is not selected, the SSIC protocol level data is not available for viewing in the Protocol Viewer tool of the Keysight Logic and Protocol Analyzer application.</p> <p>This field is displayed only when you install the SSIC license for the U4431A module.</p>
MPCle Data (Applicable only for MPCle setup)	<p>You can choose to enable or disable the decoding and extraction of the MPCle protocol level data for the applicable sublink. You can instruct the U4431A module to decode and extract the MPCle protocol level data by selecting the Enable checkbox displayed with the MPCle Data field. If this checkbox is not selected, the MPCle protocol level data is not available for viewing in the Protocol Viewer tool of the Keysight Logic and Protocol Analyzer application.</p> <p>This field is displayed only when you install the MPCle license for the U4431A module.</p>

Field	Description
Depth	<p>You can divide the total memory depth that is licensed and available for the U4431A module between the configured sublink(s) of the link. For each sublink, you further divide the allocated memory between the protocol level data acquisition and raw signal level data acquisition (if enabled).</p> <p>From the Depth listbox, select the memory depth that you want to allocate to the acquisition of the protocol level data for the sublink. This listbox is disabled if you deselect the Enable checkboxes displayed in the Protocol Data Capture section. You can allocate a value ranging from 64 KB to 4 GB as the protocol data capture memory depth.</p>
Sublink Configuration	
Initial or current burst mode	<p>Allows you to select the burst mode that you want the U4431A module to initially use for the sublink during the process of syncing to the DUT's link configurations. Once initially set, you can later use this listbox to dynamically change the burst mode of the sublink as and when you need. For such dynamic changes to take effect, you also need to click the Sync Analyzer to Link Configuration button after setting the burst mode.</p> <p>In this listbox, by default, you get options to set:</p> <ul style="list-style-type: none"> HS Gear 1 or PVM Gear 1 to 7 for a UniPro/UFS/CSI-3 sublink. HS Gear 1 or PVM Gear 1 for SSIC and PCIe sublinks. <p>The HS Gear 2 and 3 are licensed options and are displayed only when you install the appropriate license.</p> <p>If you select a PVM Gear, then the minimum and maximum range of bit rates defined for that PVM gear in the specifications are applicable for the sublink.</p> <p>If you select an HS Gear, then the data rate defined for that gear in the selected HS Rate series is applicable for the sublink.</p>
Initial or current link width	<p>Allows you to select the link width that you want the U4431A module to initially use for the sublink during the process of syncing to the DUT's link configurations. Once initially set, you can later use this listbox to dynamically change the link width of the sublink as and when you need. For such dynamic changes to take effect, you also need to click the Sync Analyzer to Link Configuration button after setting the link width.</p> <p>In this listbox, by default, you get the option to set x1 as the link width for a sublink. The x2 to x4 are licensed options and are displayed only when you install the appropriate license.</p> <p>Notes</p> <ul style="list-style-type: none"> The link width options displayed in this listbox also depend on the link width capabilities that you defined in the Sublink Width field in the Connection Setup tab. For instance, if you set the link width capabilities of a sublink as x2 in the Connection Setup tab, then only x1 and x2 options are displayed in the Initial or Current link width listbox. For SSIC and PCIe, only x1 is displayed as the link width option in case you select the PWM Gear 1 burst mode.
Initial or current data is scrambled (Applicable only for a UniPro setup)	<p>This checkbox is displayed only when you select the 1.6 option from the UniPro Spec Version listbox for a UniPro setup. This checkbox allows you to instruct the U4431A module on whether or not the data on the sublink is scrambled initially during the process of syncing to the DUT's link configurations. Once initially set, you can later use this checkbox to dynamically instruct the module on scrambled data on the sublink. For such dynamic changes to take effect, you also need to click the Sync Analyzer to Link Configuration button after setting the scrambling.</p> <p>When this checkbox is selected, the U4431A module enables descrambling for the scrambled data on the link.</p>
Link Synchronization	
Sync Analyzer to Link Configuration	<p>Click this button to synchronize the U4431A analyzer module to the currently set link configurations such as current link width and current link speed.</p> <p>On clicking this button, the current status of the sublink(s) in terms of their synchronization to current link configurations is displayed.</p> <p>This option is particularly useful when you dynamically changed the sublink configurations using the <i>Initial or current burst mode</i> or <i>Initial or current link width</i> listboxes and want to synchronize the U4431A module to these changes.</p>
Sync Status	<p>Displays the current status of the U4431A sublink(s) in the context of its synchronization to the currently set link configurations.</p> <p>A sublink can have one of the following link synchronization status:</p> <ul style="list-style-type: none"> In Sync - Indicates that the sublink is in sync to the current link configurations. Off - Indicates that the sublink is not connected/active. Syncing - Indicates that the U4431A sublink is currently in the process of syncing to the current link configurations. If it is taking too long to sync, it may indicate that the U4431A module is not able to sync to the currently set link configurations. In such a situation, you may want to check if the link configurations that you are trying to set are matching the DUT's configurations/capabilities. <p>The Sync Status is updated:</p> <ul style="list-style-type: none"> when you click the <i>Sync Analyzer to Link Configuration</i> button. when you change one of the settings in the Setup dialog box that impacts the link configuration and then click <i>Apply</i> or <i>OK</i>. or automatically when there is a change in the probed link configurations and you have instructed the U4431A module to automatically track and sync to such changes.

After you have configured the capture settings, save the settings in a Logic Analyzer configuration (.ala) file. To do this, click File > Save, select a location and name for the configuration file, select Standard Configuration (.ala) from the Save as type listbox and then click Save.

Setting up Triggers

Trigger - Overview

You can trigger the U4431A module to start storing the captured data in its memory when the specified trigger condition is met.

Using triggers, you can isolate events of interest in traffic. You can use triggers to detect errors at each layer of the protocol. You can set triggers for:

- Specific UniPro/UFS/SSIC/CSI-3/MPCIe packet types (based on the protocol license(s) you have installed)
- Physical layer triggers – Currently Crc, disparity, and symbol errors are supported.
- Occurrence counter
- Global counter
- Timer

You can set a *Simple* or an *Advanced* trigger.


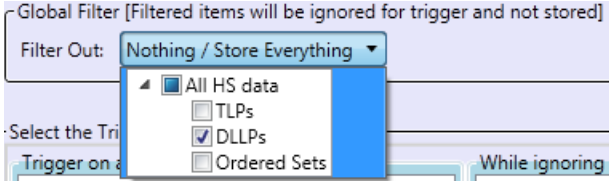
- Simple trigger – A simple trigger allows you to quickly set up a trigger without getting involved into setting up multi-level sequenced steps for the trigger. In this type of trigger, you can include one or more trigger events whose occurrence causes the module to trigger the storage of the captured data.
- Advanced trigger – An advanced trigger allows you to set up a complex multi-level sequenced trigger. In an advanced trigger, you can include multiple steps. In each step, you can define one or more trigger events and the action that should be performed with the occurrence of that event.

Trigger Settings

Based on the type of trigger (Simple or Advanced) that you are setting up, a trigger setup may be comprised of a trigger position, type, mode, store qualification, condition(s), and action. All these components of a trigger are set up using the fields displayed in the *Trigger* dialog box.


You access the Trigger Dialog box by selecting **Setup>Trigger....** from the *Keysight Logic and Protocol Analyzer* GUI's menu bar.

The following table has descriptions of all the fields available in this dialog box for setting up a simple or an advanced trigger. (See [“Triggering Based on Packet Types”](#), [“To Set up a Simple Trigger”](#), and [“To Set up an Advanced Trigger”](#) for more information.)

Field	Description
Link tabs	The top of the Trigger dialog box allows you to add tabs that let you set up separate triggers for different MIPI M-PHY links. You can add tabs using the  icon and then apply these tabs to different links that are set up in the Connection Setup tab of the Setup dialog box.
Applies to	Displays the MIPI M-PHY links that are set up in the Connection Setup tab of the Setup dialog box. Selecting one or more links from the Applies to drop-down listbox applies the trigger settings in the current tab page to the data captured on the selected links.
Trigger Position	Defines the trigger position within the acquisition memory of the U4431A module. The selected trigger position on the slider sets the pre and post trigger memory ratio. By default, the available acquisition memory is equally divided between the pre and post trigger memory.
Favorite Triggers	Displays a drop-down menu for: <ul style="list-style-type: none"> opening a previously saved trigger setup. saving the currently configured trigger setup in a Trigger Setup (.trg) file. viewing and accessing a list of recently accessed trigger setup files. Note: The favorite trigger setups referred in this drop-down menu are different from the favorites list that appear in the left-side events pane. In the left pane, the list of favorites refer to the favorite trigger events that you added, edited, and saved to the favorites list using the Event Editor dialog.
Clear	Clears the current trigger settings that you configured in the current tab and restores the default settings.
Global Filter [Filtered items will be ignored for trigger and not stored]	<p>The Global Filter option is visible only for SSIC and M-PCIe protocols. In the Global Filter section, you can select an option from the Filter Out list-box to specify whether or not to store certain type of packets after triggering. You can also choose whether or not to consider the filtered items for trigger. By default, all the packets are stored. If you don't want to store certain types of packets after triggering, select the required option from the Filter Out list-box. For example, if you don't want to store DLLPs after triggering, select the DLLPs checkbox in the Filter Out list-box. The DLLPs will not be considered for the trigger.</p> 
For setting up a Simple Trigger - The following fields are applicable for a simple trigger.	
Simple Trigger	Select the Simple Trigger radio button. Selecting this radio button displays the fields relevant for setting a simple trigger in the Trigger dialog box. These fields are described below.
List of events	<p>In the left pane of the Trigger dialog box, an expandable list of events is displayed based on the protocol family (M-PHY, UniPro, or UFS) currently active for the U4431A module. The events in this list are organized based on categories such as:</p> <ul style="list-style-type: none"> Types of packet - Displayed on the basis of the protocol license(s) you have installed. Physical Layer triggers - The transmission of an erroneous packet, for instance, with Crc, symbol, or disparity error sets the trigger. Arming - The U4431A module can receive triggers from another module installed in the AXIe chassis or another device with which it is connected via the Trigger in Connector on the AXIe chassis. <p>You can select one or more of these events to act as trigger condition(s).</p>
Trigger Mode	<ul style="list-style-type: none"> Select the Trigger on Packets or Ordered Sets radio button to ensure that the U4431A module is triggered on the occurrence of the trigger event(s) included in the <i>Select the Trigger(s) to use</i> section. Select the Trigger when Stop button pressed radio button to ensure that the U4431A module is triggered to store the captured data when you click the Stop button to stop the data capture. On selecting this option, the trigger events are not applicable. Therefore, the <i>Select the Trigger(s) to use</i> section is disabled.

Field	Description
Select the Trigger(s) to use	<ul style="list-style-type: none"> ▪ Trigger on any of these events - You can drag events from the list of events displayed in the left pane and drop these events in this section. The U4431A module is triggered on the occurrence of any of the events included in this section. ▪ While ignoring any of these events - You can drag events from the list of events displayed in the left pane and drop these events in this section. The U4431A module is not triggered on the occurrence of any of the events included in this section. <p>To edit events included in the above-mentioned sections, click the underlined event name. This displays the <i>Event Editor</i> dialog box to let you edit the event properties or add the event to the list of favorite events.</p> <p>To remove events from the above-mentioned sections, click the "X" displayed to the left of the event name.</p>
For setting up an Advanced Trigger- The following fields are applicable for an advanced trigger.	
Advanced Trigger	Select the Advanced Trigger radio button. Selecting this radio button displays the fields relevant for setting an advanced trigger in the Trigger dialog box. These fields are described below.
List of events	<p>In the left pane of the Trigger dialog box, an expandable list of events is displayed based on the protocol family (M-PHY, UniPro, or UFS) currently active for the U4431A module. The events in this list are organized based on categories such as:</p> <ul style="list-style-type: none"> ▪ Type of packet - Displayed on the basis of the protocol license(s) you have installed. ▪ Physical Layer triggers - The transmission of an erroneous packet, for instance, with Crc, symbol, or disparity error sets the trigger. ▪ Timers, counters, and flags - Sets trigger on a timeout or the expiry of a counter. The U4431A module can also receive triggers from another module installed in the AXIe chassis or another device with which it is connected via the Trigger in Connector on the AXIe chassis. The <i>Arm In from</i> event is used in such a situation. ▪ Comment - Allows you to add comments to your advanced trigger setup. <p>You can select one or more of these events to act as trigger condition(s).</p>
Select the Trigger(s) to use	<p>By default, this section displays a single step with an 'If - then' specification for the trigger condition and subsequent action. If required, you can add more steps (maximum eight) in this section to set up a complex multi-level sequenced trigger.</p> <p>This section has the following fields:</p> <ul style="list-style-type: none"> ▪ The Step button allows you to add or delete steps from the sequence. ▪ The If/Else if buttons let you insert additional "If" clauses in the same step or delete an additional "if" clause from the step. ▪ To add multiple conditions / actions for a step, drag the required event from the list of events in the left pane and drop it to the step box. ▪ The event chevron buttons let you insert, delete, or logically group (or negate) events. ▪ The action chevron buttons let you insert or delete actions.

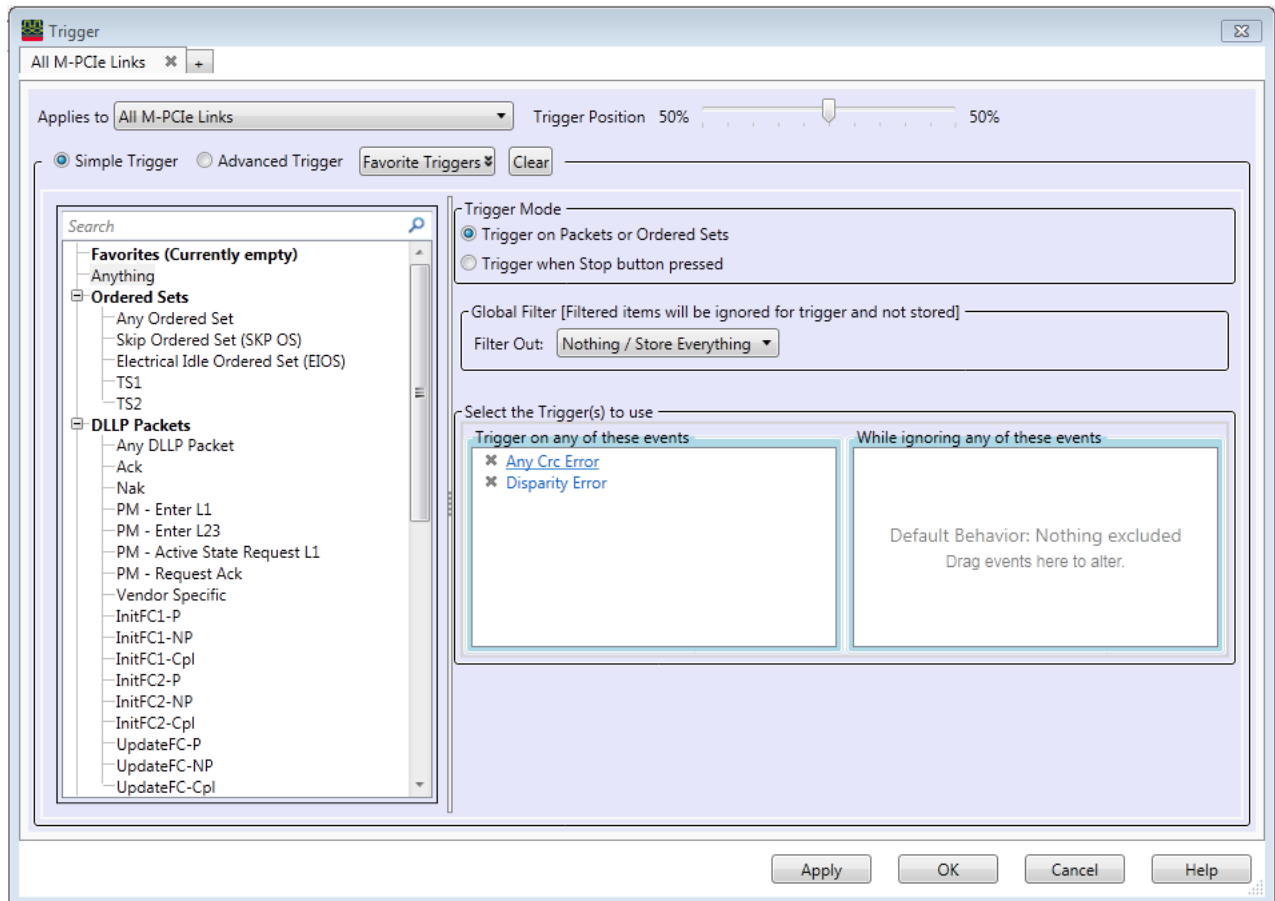
To Set up a Simple Trigger

- 1 In the *Keysight Logic and Protocol Analyzer* GUI's Overview window, click the M-PHY module and select **Setup>Trigger....** from the drop-down menu. Alternatively, click the  icon displayed for the M-PHY module.
The **Trigger** dialog box is displayed.
- 2 From the **Applies to** listbox, select the MIPI M-PHY link(s) to which you want to apply the trigger settings.
- 3 If required, use the **Trigger Position** slider to change the default trigger position (50%) for the allocation of pre and post trigger memory.
- 4 Select the **Simple Trigger** radio button. All simple trigger related fields are then displayed.
- 5 Select the **Trigger Mode**. On selecting the **Trigger on Packets or Ordered Sets** radio button, the **Select the Trigger(s) to use** section is displayed in which you can add trigger events. For the **Trigger when Stop button is pressed** option, trigger events are not applicable. Therefore, you cannot add any trigger events on selecting this option.
- 6 If you don't want to store certain packet types, select the packet category from the **Filter Out** listbox in the **Global Filter** section.
- 7 Drag and drop the desired trigger event(s) from the event list in the left pane to the **Trigger on any of these events** section on the right.


- 8 To change an added trigger event's properties, click that event's underlined name in the **Trigger on any of these events** section.
- 9 To delete an added trigger event from the **Trigger on any of these events** section, click the "X" to the left of the event name.
- 10 From the event list in the left pane, drag the required events that you want to exclude and drop these in the **While ignoring any of these events** section.
- 11 Click **Apply**.
- 12 To save the currently configured trigger setup in a Trigger Setup (.trg) file, click the **Favorite Triggers** button and then select the **Save** option from the displayed drop-down menu.
- 13 Click **OK**.




Trigger Example

The following trigger setup aims at triggering the U4431A module on the transmission of *M-PCIe* packet with Crc or disparity error.



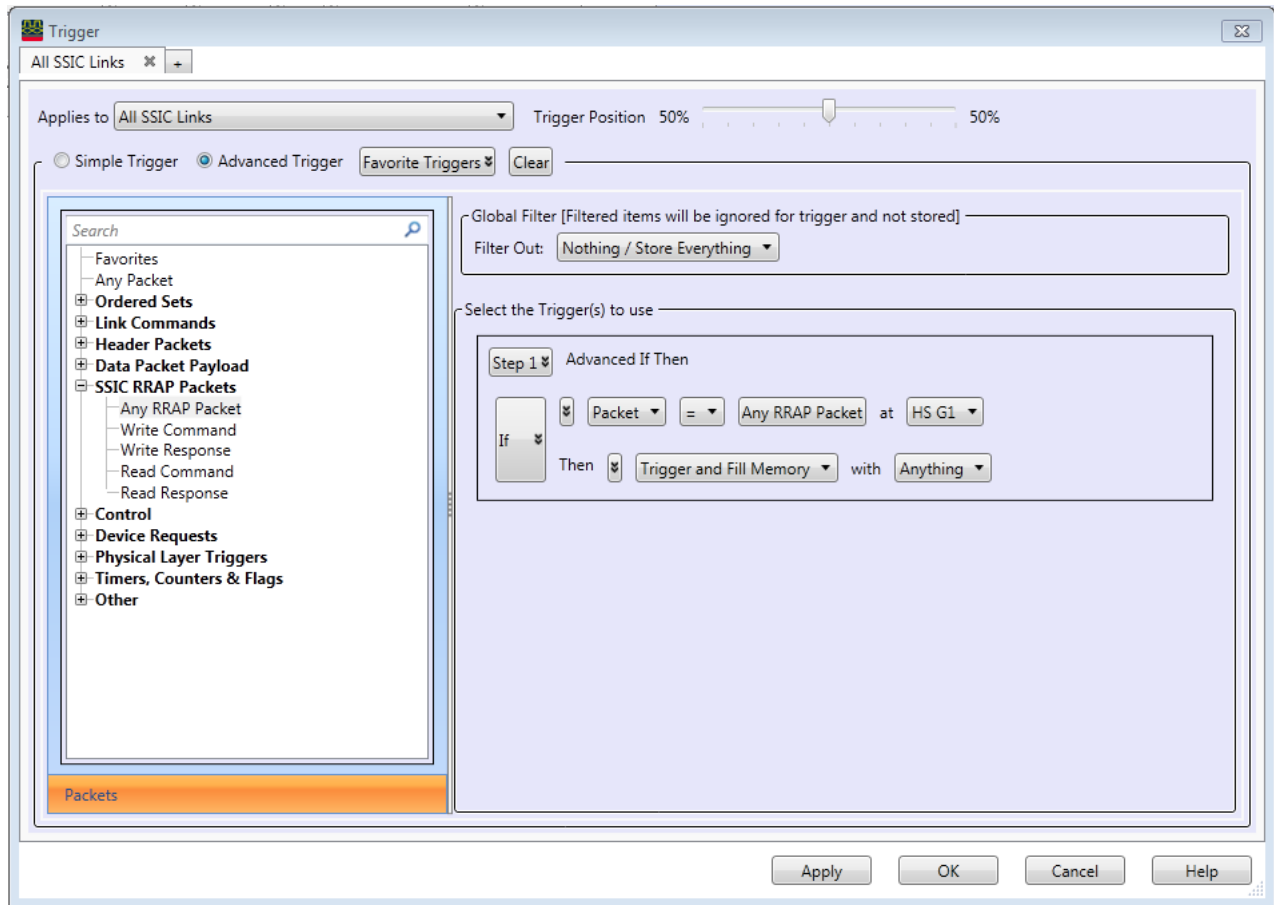
To Set up an Advanced Trigger

- 1 In the *Keysight Logic and Protocol Analyzer* GUI's Overview window, click the M-PHY module and select **Setup>Trigger....** from the drop-down menu. Alternatively, click the  icon displayed for the M-PHY module.
The **Trigger** dialog box is displayed.

- 2 From the **Applies to** listbox, select the MIPI M-PHY link(s) to which you want to apply the trigger settings.
- 3 If required, use the **Trigger Position** slider to change the default trigger position (50%) for the allocation of pre and post trigger memory.
- 4 Select the **Advanced Trigger** radio button. All advanced trigger related fields are then displayed.
- 5 As per your specific requirements:
 - a Edit the default step (Step 1) to select the trigger condition and action in the “If-Then” clause.
 - b Add more “If-Then” clauses to a step by clicking the If/Else If button in the step.
 - c Add more than one And/Or condition to an “If” clause of a step by clicking the  button for events. You can also negate a condition by selecting the “Insert Not at beginning of row” option from the  drop-down menu.
 - d Add more than one action to “Then” clause of a step by clicking the  button for actions.
 - e Add more steps before or after a step by clicking a Step button.
- 6 If you don't want to store certain packet types, select the packet category from the **Filter Out** listbox in the **Global Filter** section.
- 7 Click **Apply**.
- 8 To save the currently configured trigger setup in a Trigger Setup (.trg) file, click the **Favorite Triggers** button and then select the **Save** option from the displayed drop-down menu.
- 9 Click **OK**.

Trigger Example 1

The following trigger setup aims at triggering the U4431A module on the transmission of any RRAP SSIC packet at HS Gear 1 speed.

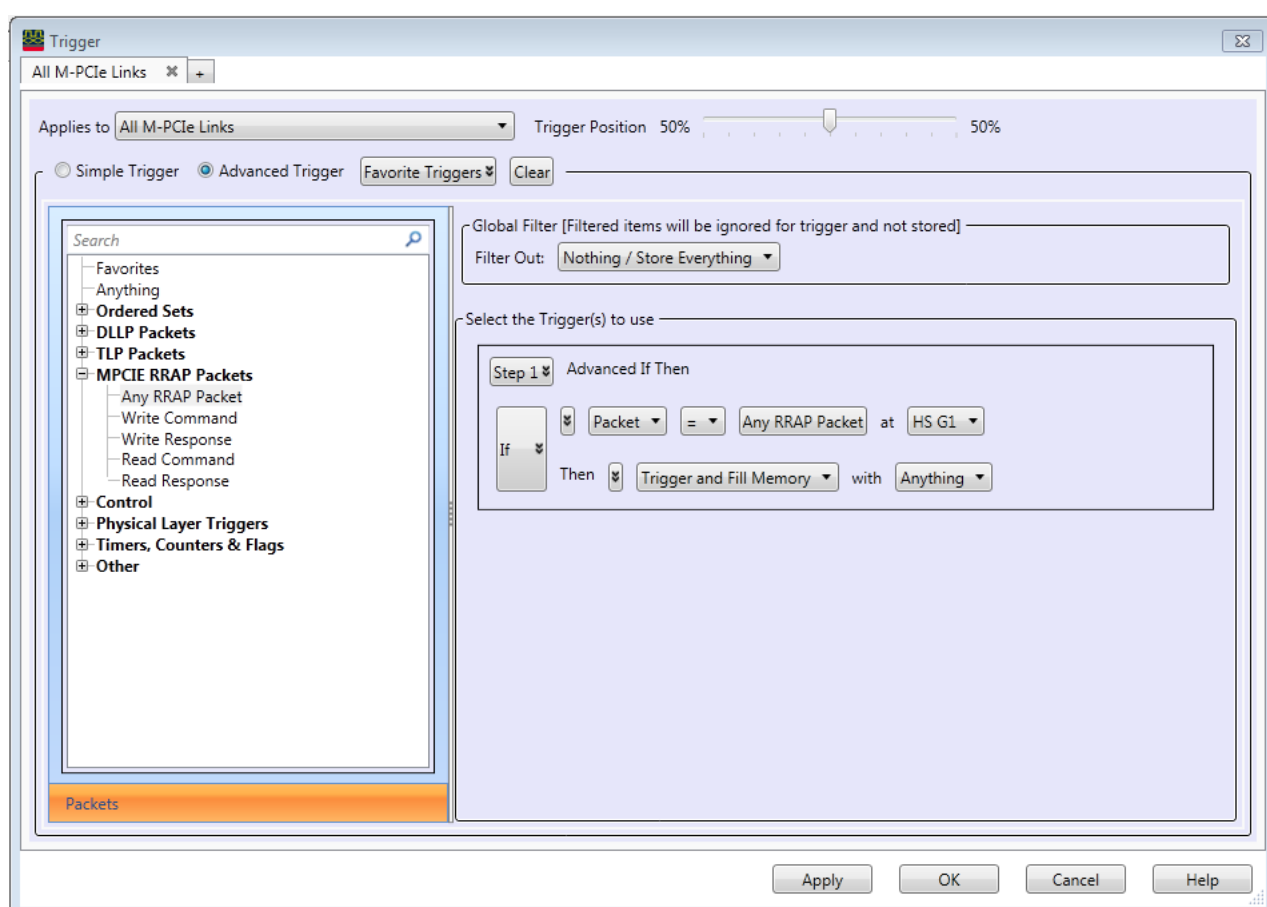


Trigger Example 2

The following trigger setup aims at triggering the U4431A module on the transmission of any RRAP M-PCIe packet at HS Gear 1 speed.

NOTE

In case of MPCle, setting a trigger on **Start of Burst** packet would ensure that the U4431 module is triggered on the transmission of **Start of Burst** as well as **TS1 with SOB** packets. Similarly, during the Search process if you search for any **Start of Burst** packets the search results would display even **TS1 with SOB** packets also.

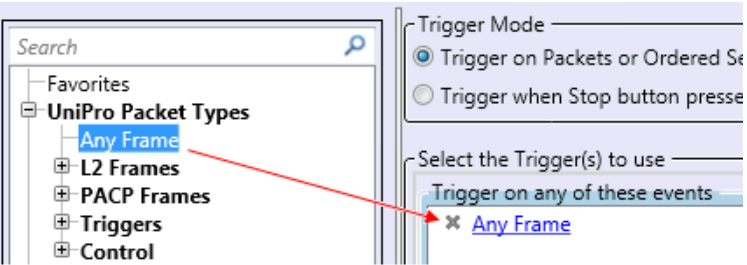
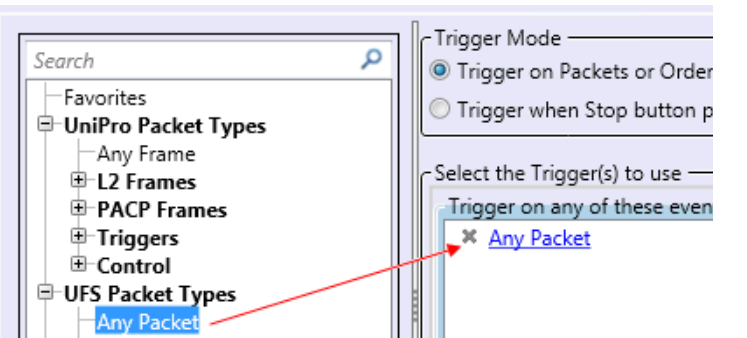


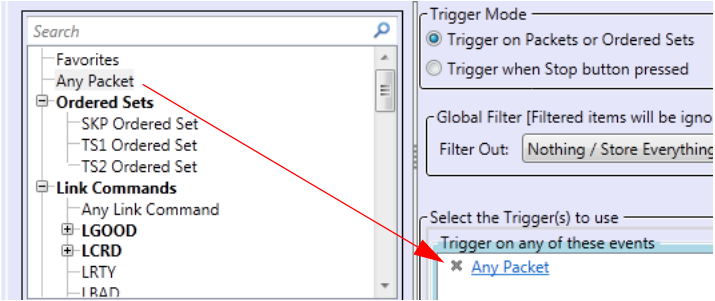
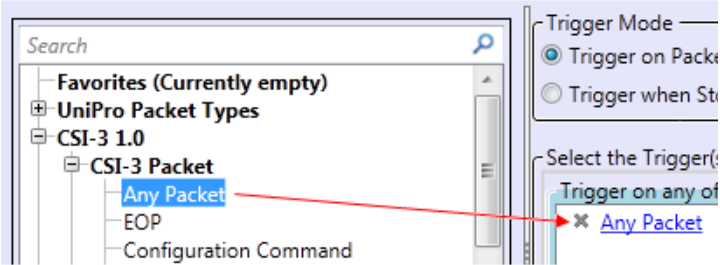
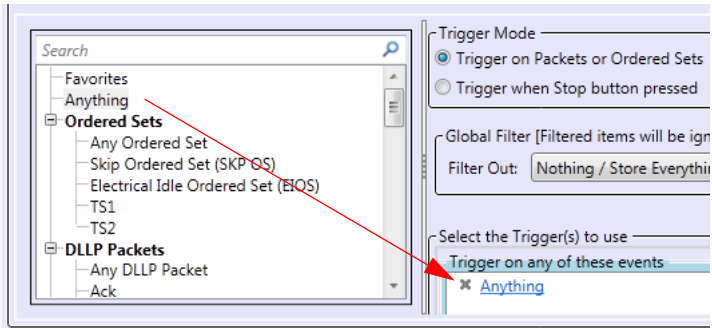
Triggering Based on Packet Types

You can configure the U4431A module to trigger on any or specific packet types. The packet types displayed in the simple and advanced trigger dialogs depend on the license(s) that you have installed. For instance, if you have the SSIC license installed, the SSIC-specific packet types are displayed. In case you have the UniPro and UFS license installed, the packet types for both UniPro as well as UFS are displayed.

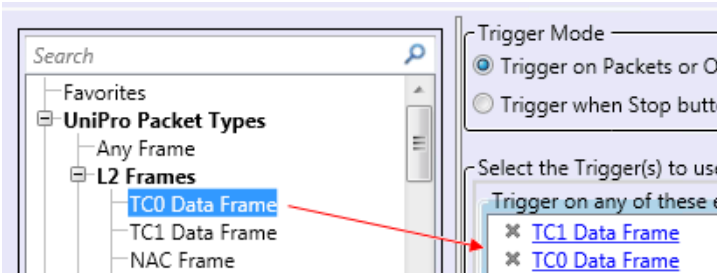
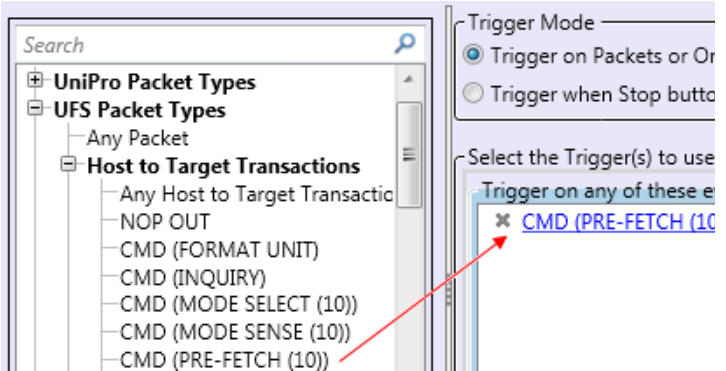
In a simple trigger

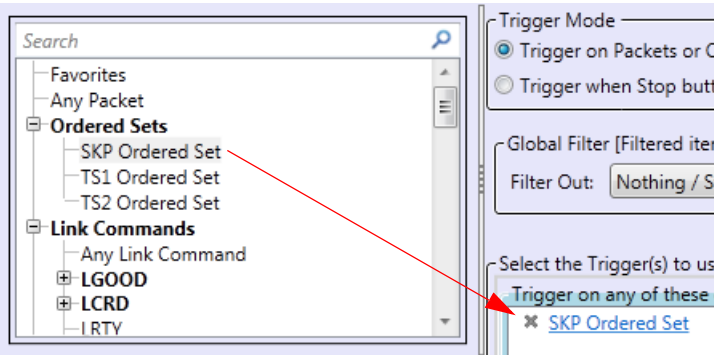
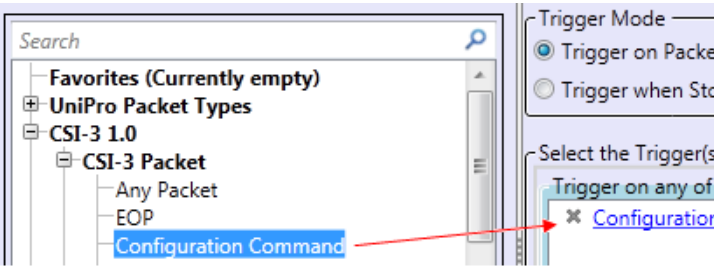
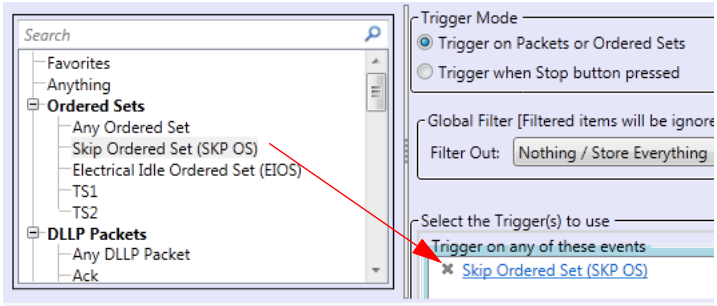
To trigger on “any packet” of the applicable protocol

Protocols	Description
For UniPro	<p>Drag and drop the Any frame option from the UniPro Packet Types list in the left pane of the Trigger dialog.</p> 
For UFS	<p>Drag and drop the Any Packet option from the UFS Packet Types list in the left pane of the Trigger dialog.</p> 

Protocols	Description
For SSIC	<div>Drag and drop the Any Packet option from the left pane of the Trigger dialog.</div> <div></div>
For CSI-3	<div>Drag and drop the Any Packet option from the CSI-3 1.0 list in the left pane of the Trigger dialog.</div> <div></div>
For M-PCle	<div>Drag and drop the Anything option from left pane of the Trigger dialog.</div> <div></div>

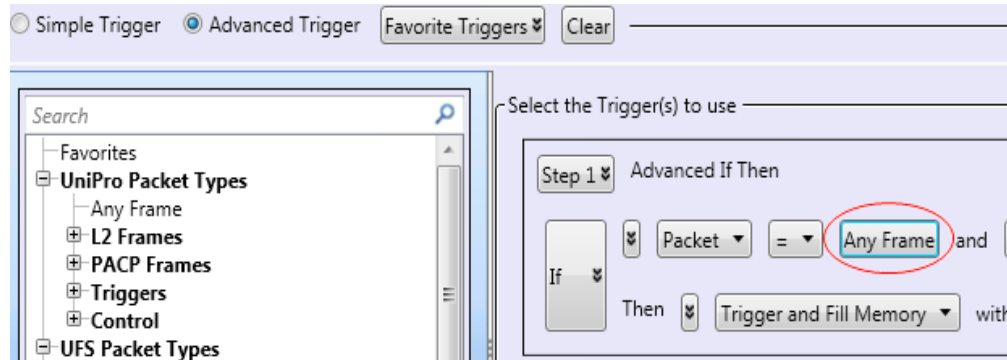
To trigger on specific packet(s) of the applicable protocol

Protocol	Description	
For UniPro	Drag and drop specific packet type(s) from the UniPro Packet Types list in the left pane of the Trigger dialog.	
For UFS	Drag and drop specific packet type(s) from the UFS Packet Types list in the left pane of the Trigger dialog.	

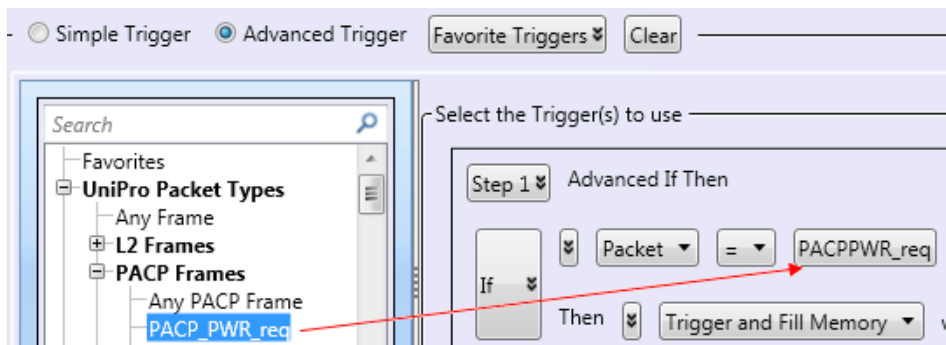
Protocol	Description
For SSIC	<div>Drag and drop specific packet type(s) from the left pane of the Trigger dialog.</div> <div></div>
For CSI-3	<div>Drag and drop specific packet type(s) from the CSI-3 1.0 list in the left pane of the Trigger dialog.</div> <div></div>
For MPCle	<div>Drag and drop specific packet type(s) from the left pane of the Trigger dialog.</div> <div></div>

In an advanced trigger

If you have the UniPro and UFS license, by default, a step added in an advanced trigger has the Any Frame option selected as the packet type. Any Frame, in this case represents any UniPro frame.



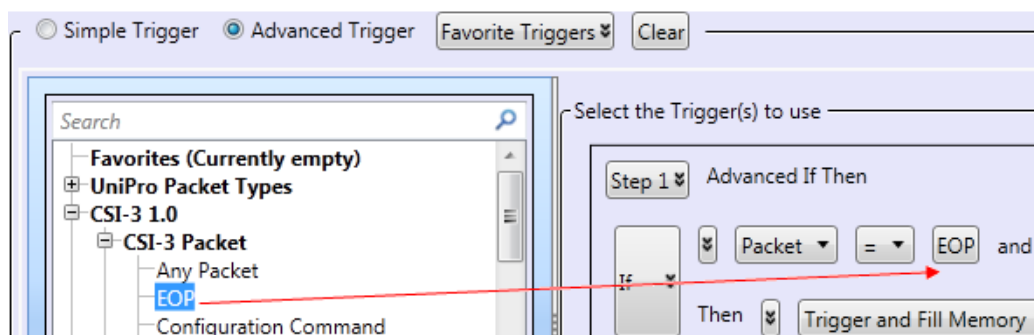
In an advanced trigger step, if you want to configure a trigger on a specific UniPro frame or a specific UFS packet, simply drag and drop that frame/packet from the **UniPro Packet Types/ UFS Packet Types** lists in the left to the **Any Frame** option in the step.



For SSIC, by default, a step added in an advanced trigger has **Any packet** selected as the packet type. Any packet, in this case represents any SSIC packet. If you want to trigger on a specific SSIC packet, you can drag and drop any SSIC packet from the left to the **Any Packet** option in the step.

For MPCle, by default, a step added in an advanced trigger has Anything selected as the packet type. Anything, in this case represents any MPCle packet. If you want to trigger on a specific MPCle packet, you can drag and drop any MPCle packet from the left to the Anything option in the step.

If you have the UniPro/CSI-3 license, by default, a step added in an advanced trigger has **Any frame** selected as the packet type. Any Frame in this case represents any UniPro frame. If you want to trigger on a specific UniPro frame or a specific CSI-3 packet, simply drag and drop that frame/packet from the **UniPro Packet Types/ CSI-3** lists in the left to the Any Frame option in the step.



Using Timers in Advanced Triggers

You can use timers in events and actions of sequence step(s) of an advanced trigger setup.

In a trigger setup, timers are used to check the amount of time that has elapsed between events. For example, if you want to trigger on an End of Burst packet that occurred within 800 ns of a Start of Burst packet, you can use a timer.

When using timers in a U4431 trigger setup, following are some points to remember:

- Timers do not start automatically. You need to start a timer by using the Start Timer action in the trigger setup before you can use the timer to check time condition in an event statement. (Refer to the timer example given below in this topic)
- Starting a timer does not reset the timer. It only starts/restarts the timer from its existing count. Therefore, you should reset the timer before starting it to get accurate triggering results. You use the Reset Timer action to accomplish this. (Refer to the timer example given below in this topic)
- The U4431 module provides two timers, Timer 1 and 2. Ensure that you use the correct timer in both event and action.

To insert a timer in an event statement

- 1 Drag the **Timer** event from the events list on the left of the Trigger dialog and drop it to the event statement on the right.
- 2 Select the timer operator (\geq or $<$) and timer value.



To insert a timer action

- 1 Drag the **Timer** event from the events list on the left of the Trigger dialog and drop it to the action statement on the right.
- 2 Select the timer action **Start Timer**, **Reset Timer** or **Stop Timer**.



Timer Example

The following example illustrates the usage of a timer to trigger on an End of Burst packet that occurred within 800 ns after a Start of Burst packet.

In step 1, the Timer 1 is first reset and then started when the event condition (Start of Burst packet) is met. In step 2, the Timer 1 is tested to check if an End of Burst packet occurred within 800 ns. If the condition is met, the module is triggered to start capture else the timer keeps running till it exceeds 800 ns without triggering.

On exceeding 800 ns without triggering, the Else If statement is executed to take the sequential flow of the trigger back to step 1 to again start looking for a Start of Burst packet.

Select the Trigger(s) to use

Step 1 ▾ Advanced If Then

☐ If ▾ Packet ▾ = ▾ Start of Burst / Deskew ▾ and Preempting ▾ at Any Gear ▾

Then ▾ Timer ▾ 1 ▾ Reset Timer ▾
☐ Timer ▾ 1 ▾ Start Timer ▾
☐ Goto ▾ Step 2 ▾

Step 2 ▾ Advanced If Then

☐ If ▾ Packet ▾ = ▾ End of Burst GT ▾ and Preempting ▾ at Any Gear ▾ And ▾

☐ Timer ▾ 1 ▾ < ▾ 800 ▾ ns


Then ▾ Trigger and Fill Memory ▾ with Anything ▾

☐ Else if ▾ Timer ▾ 1 ▾ >= ▾ 800 ▾ ns

Then ▾ Goto ▾ Step 1 ▾

Starting and Stopping the Data Capture

Once the capture setup and trigger setup are ready, you can start capturing the M-PHY data.


To start the data capture, choose **Run/Stop>Run** from the Logic and Protocol Analyzer GUI's menubar. Alternatively, click the  icon from the toolbar.

For more information, see the *Running/Stopping Measurements* topic in the *Logic and Protocol Analyzer Online Help*.

On starting data capture, the Status section at the bottom of the Logic and Protocol Analyzer GUI is updated to reflect the current status of the data capture procedure.

The status moves from the Processing, Running, Waiting, and finally to Filling Memory state.

Capture Status	Description
Processing	The module is processing the data capture settings to initiate data capture. <div>The status bar shows 'Status.' followed by 'Processing...' in a green bar.</div>
Running	The U4431A module is now in the Running state to capture M-PHY data. <div>The status bar shows 'Status.' followed by 'Running...' in a green bar.</div>
Waiting	The U4431A module is waiting for the trigger condition to meet to start data capture. <div>The status bar shows 'Status...' followed by 'Waiting [UFS-104] for trigger...' in a yellow bar.</div>
Filling Memory	The trigger condition is met. The U4431A module is now triggered to start storing the captured data in its memory. <div>The status bar shows 'Status.' followed by 'Filling Memory [DSI-101] ...' in a yellow bar.</div>

To stop the data capture, click the  icon from the toolbar.

5 Viewing and Analyzing Captured Data

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Viewing Packet and Raw Signal Data using the Waveform Viewer / 68

Viewing Time Synchronized Protocol Data in Display Windows / 75

This chapter provides information on how to view and analyze the captured M-PHY data for testing and debugging purposes using various viewers available in the Keysight Logic and Protocol Analyzer GUI.

Overview

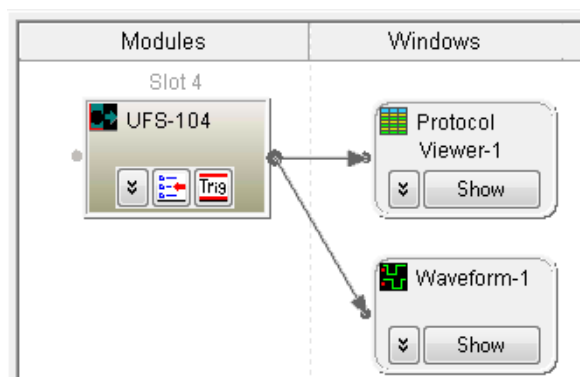
After you have captured M-PHY data using the U4431A module, you can view and analyze this data in the Keysight Logic and Protocol Analyzer GUI in the following two modes:

- Online - In the Online mode, the U4431A hardware is still connected and needed for viewing the captured data.
- Offline - For the Offline mode, you can save the captured data and configurations in a Logic Analyzer configuration file (.ala extension). You can later load this file in the Logic and Protocol Analyzer GUI to view and analyze data offline (without the U4431A module connected).

You can also export the captured data to CSV format files for offline viewing and analysis in applications other than the Logic and Protocol Analyzer GUI.

For viewing the captured data in online or offline mode, various viewers are available in the Logic and Protocol Analyzer GUI. Each viewer has its own set of features for the presentation of captured data and is useful for specific situations.

Viewer	Usage	Notes
Protocol Viewer	To view protocol level packet data. Packet details are organized and viewable in different forms using different views available in Protocol Viewer	An instance of Protocol Viewer is automatically added and available in the Logic and Protocol Analyzer GUI when you create a new session of the U4431A module.
Waveform Viewer	To view both packet as well as raw signal data.	An instance of Waveform Viewer is automatically added and available in the Logic and Protocol Analyzer GUI when you create a new session of the U4431A module.
Listing	To view raw signal level data	If needed, you can add instance(s) of this viewer to the U4431A module by selecting <i>New Window > Listing</i> option in the Overview pane.



In the above screen, a Protocol Viewer and a Waveform Viewer are already added on starting a new session. A Listing viewer can be added manually.

The topics that follow describe how to use each of the available viewers for viewing and analyzing captured data.

NOTE

The information about viewers in the following topics is specific to viewing and analyzing M-PHY data. To get general information about a viewer, its fields, or how to use it, refer to the topics in **Reference > Windows** section of the Logic and Protocol Analyzer Online Help.

Viewing M-PHY Packet Data Using the Protocol Viewer Display

NOTE

The information about the Protocol Viewer in this topic is specific to viewing and analyzing M-PHY packets. To get general information about the Protocol viewer, its fields, or how to use it, refer to the following topics in the Logic and Protocol Analyzer Online Help.

- **Reference > Windows > Protocol Viewer Display Window**
- **Analyzing the Captured Data > Analyzing Packet Data**

The Protocol Viewer window provides various ways of viewing the captured M-PHY packet data. You can view summarized as well as detailed packet information at the same time within the upper and lower panes in this window.

The upper pane of the Protocol Viewer displays a summarized list of captured M-PHY packets. The Protocol Viewer window is customized for the protocol family being decoded. For an M-PHY packet, it displays the M-PHY related decoded fields. In the following screen, the captured packet details specific to the UniPro protocol are displayed.

The screenshot displays the Protocol Viewer-1 window. The top pane shows a list of captured M-PHY packets. The bottom pane shows the Traffic Overview summary.

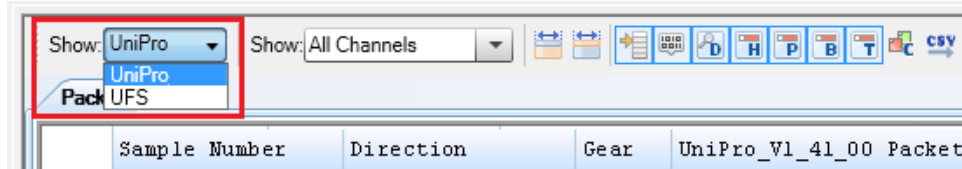
Gear	UniPro_V1_41_00 Packet	Sequence Number	L4 Payload	CRC	Time
HS G1	Start of Burst / Deskew				0 s
M3	PACP_TEST_MODE_req			6385 (GOOD)	16 ns
M1	PACP_PWR_req			CA44 (GOOD)	32 ns
M2	PACP_PWR_cnf			EC4E (GOOD)	176 ns
HS G1	TCO Data Frame	02	0000 0000 0000 0...	54EB (GOOD)	305 ns
HS G1	TCO Data Frame	03	0000 0000 0000 0...	44CA (GOOD)	497 ns
HS G1	TCO Data Frame	04	0000 0000 0000 0...	342D (GOOD)	705 ns
HS G1	TCO Data Frame	05	0000 0000 0000 0...	240C (GOOD)	897 ns
HS G1	TCO Data Frame	06	0000 0000 0000 0...	146F (GOOD)	1.106 us
HS G1	TCO Data Frame	07	0000 0000 0000 0...	044E (GOOD)	1.298 us
HS G1	TCO Data Frame	08	0000 0000 0000 0...	F5A1 (GOOD)	1.507 us
HS G1	TCO Data Frame	09	0000 0000 0000 0...	E580 (GOOD)	1.699 us
HS G1	AFC0 Frame	08		0E43 (GOOD)	1.907 us
HS G1	TCO Data Frame	0A	0203 0405 0607 0...	BC31 (GOOD)	1.923 us
HS G1	NAC Frame			97D4 (GOOD)	3.109 us
HS G1	End of Burst				3.125 us

UniPro Packet Types	Packets	UFS-104	Total
L2 Frames			
PACP Frames			
Triggers			
Control			
Errors			
PACP_PWR_req	319567	319567	
PACP_PWR_cnf	319566	319566	
TCO Data Frame	2876094	2876094	
AFC0 Frame	319566	319566	

Viewing Packet Data Specific to a Protocol

The U4431A module supports acquisition and decoding of the M-PHY, UniPro, UFS, SSIC, MPCle, and CSI-3 protocols depending on which licenses you have installed. From the captured data, if required, you can view packet data specific to a U4431A supported protocol in the Protocol Viewer.

The Show listbox at the top in the Protocol Viewer displays the currently active licensed protocol options.



Selecting a protocol option from this listbox displays the captured data specific to only that protocol in the Protocol Viewer window. In the following screen, selecting the UFS protocol from the Show listbox displays the UFS specific captured data.

Show: UFS Show: All Channels							
Packets							
Sample Number	UFS_V1_1 Packet	Task Tag	Data	Gear	Time	Rate	Series
3	RESPONSE	00		HS G1	-7.128 us	B-Series	
4	TM RESPONSE (Compl...	00		HS G1	-6.854 us	B-Series	
5	NOP OUT	00		HS G1	-6.579 us	B-Series	
6	DATA OUT	00	0100 0302 0504 0...	HS G1	-6.305 us	B-Series	
7	READY TO XFER	00		HS G1	-5.811 us	B-Series	
8	DATA OUT	00	0100 0302 0504 0...	HS G1	-5.509 us	B-Series	
9	REJECT UPIU	00		HS G1	-5.015 us	B-Series	
10	QRESP (Success)	00		HS G1	-4.741 us	B-Series	
11	DATA IN	00	0100 0302 0504 0...	HS G1	-4.466 us	B-Series	
12	CMD (FORMAT UNIT)	00		HS G1	0 s	B-Series	
13	CMD (INQUIRY)	00		HS G1	206 ns	B-Series	

Viewing Preempted Data

If you enabled preemption support for the U4431A module in the Analyzer Setup tab, you can view the captured frames that have been preempted by the module in the Protocol Viewer. If the preemption support is not enabled for the module, then for all the captured frames, the Preempting field is set to No to indicate that the frames have not been preempted.

The preemption support is not applicable for SSIC and MPCle setups.

The following screen displays a sample preempted frame with the Preempting field set to Yes to indicate that the frame has been preempted.

The screenshot displays the Keysight U4431A Protocol Analyzer interface. At the top, the 'Show:' dropdown is set to 'UniPro_V1_41_00' and the 'Show:' dropdown is set to 'All Channels'. Below this is the 'Packets' tab, which shows a list of captured packets. The selected packet is packet 8, which is an 'AFC0 Frame' with a 'Sequence Number' of '1F'. The packet details are shown in the 'Details' tab, which includes a 'Generated Fields' section. In this section, the 'Preempting' field is set to 'Yes', which is highlighted with a red box. Other fields include 'Direction = UFS-104', 'Packet Length = 48', 'Link Width = 1', 'Gear = HS G1', and 'Rate Series = A-Series'. The 'UniPro_V1_41_00' section is also expanded, showing 'Data Link (L2)'.

Sample Number	Direction	Gear	UniPro_V1_41_00 Packet	Sequence Number
6	UFS-104	HS G1	TCO Data Frame	05
7	UFS-104	HS G1	TCO Data Frame	06
8	UFS-104	HS G1	AFC0 Frame	1F
9	UFS-104	HS G1	TCO Data Frame	07
10	UFS-104	HS G1	End of Burst	

Generated Fields

- Direction = UFS-104
- Packet Length = 48
- Link Width = 1
- Gear = HS G1
- Rate Series = A-Series
- Preempting = Yes**

UniPro_V1_41_00

- Data Link (L2)

Identifying HS and PWM Data Transmissions

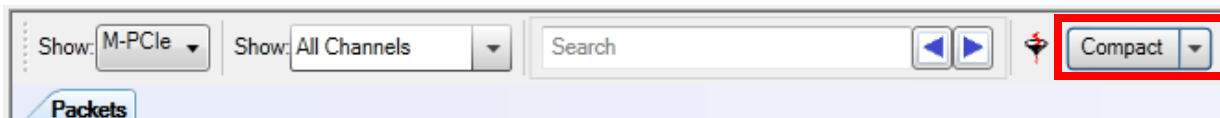
From the captured trace, you can identify the transmission in HS or PWM modes. The **Gear** column in the upper pane of the Protocol Viewer displays the burst mode and gear applicable for each packet in the captured trace.

Packets				
Sample Number	Direction	Gear	UniPro_V1_41_00 Packet	Packet Length
0	UFS-101	PWM G2	PACP_TEST_DATA_2	3072
1	UFS-101	PWM G2	PACP_TEST_DATA_3	4096
2	UFS-101	PWM G2	End of Burst	16
3	UFS-101	PWM G2	Start of Burst / Deskew	16

Packets				
Sample Number	Direction	Gear	UniPro_V1_41_00 Packet	Sequence Number
24	UFS-104	HS G1	TCO Data Frame	06
25	UFS-104	HS G1	TCO Data Frame	07
26	UFS-104	HS G1	TCO Data Frame	08
27	UFS-104	HS G1	TCO Data Frame	09

Compacting the Display of Training Sequence Packets

By default, the captured training sequence packets are listed individually in the upper pane of the Protocol Viewer. There are situations when there are numerous training sequence packets of a particular type, say TS1, exchanged continuously over a period of time. In such situations, the listing of packets in Protocol Viewer can become clogged with these training sequences thereby requiring you to scroll significantly to view other types of packets. To overcome this, you can use the **Compact** tool that compacts the display of training sequence packets into sets in Protocol Viewer. This tool is available in the Protocol Viewer toolbar as displayed in the screen below.

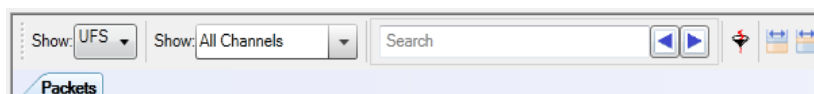


NOTE

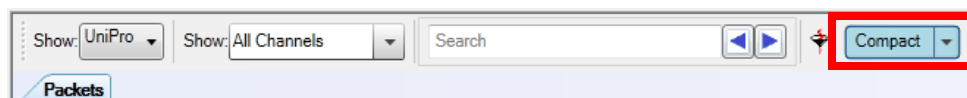
When using the U4431A module, the Compact Tool is available for the following protocols:

- UniPro
- SSIC
- M-PCIe

When you select a protocol such as UFS and CSI-3 from the **Show** listbox in the **Protocol Viewer**, the **Compact** button gets disabled as the Compact tool is not supported for this protocol.



However, when you select a protocol such as UniPro for which the Compact tool is supported, the **Compact** button is displayed as Enabled.



On enabling Compact, the training sequence packets of the same type, transmitted in the same direction in a time sequence are compacted into sets thereby reducing the packet entries in the Protocol Viewer listing. The compacted view also provides you a clearer view of the transitions such as TS1 to TS2 occurring in the exchange of training sequence packets.

Example - Compacted Display

The screens below display the packets in the Protocol Viewer before and after the Compact tool is used. Notice that all the packets are listed individually before compact is enabled. After enabling compact, the TS1 and TS2 packets are compacted as specific sets per direction and are listed as one entry per set in the Protocol Viewer. The number of packets that have been compacted into one entry/set is represented through the numeric value displayed with the entry/set.

Sample Number	Time	Direction	Direction	M-PCIE Packet
15	1.763 us		M-PCIE-101B	TS1
16	1.907 us		M-PCIE-101B	TS1
17	2.052 us		M-PCIE-101B	TS1
18	2.196 us		M-PCIE-101B	TS1
19	2.340 us		M-PCIE-101B	TS1
20	2.484 us		M-PCIE-101B	TS1
9	2.893 us	M-PCIE-101A		TS1
21	3.013 us		M-PCIE-101B	TS1
10	3.069 us	M-PCIE-101A		TS1
22	3.189 us		M-PCIE-101B	TS1
11	3.245 us	M-PCIE-101A		TS1

Before Enabling Compact

Sample Number	Time	Direction	Direction	M-PCIE Packet
4	256 ns	M-PCIE-101A		UpdateFC-Cpl
5	321 ns	M-PCIE-101A		SKP OS (4)
2	329 ns		M-PCIE-101B	UpdateFC-P
3	393 ns		M-PCIE-101B	UpdateFC-NP
4	457 ns		M-PCIE-101B	UpdateFC-Cpl
5	521 ns		M-PCIE-101B	SKP OS (3)
8	721 ns		M-PCIE-101B	TS1 (31)
9	2.893 us	M-PCIE-101A		TS1 (19)
39	6.186 us		M-PCIE-101B	TS2 (26)
28	6.274 us	M-PCIE-101A		TS2 (26)
66	10.897 us		M-PCIE-101B	TS1 (15)

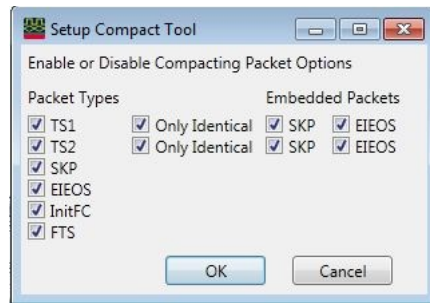
31 TS1 packets in the downstream direction compacted into one TS1 entry

After Enabling Compact

To configure the Compact setup

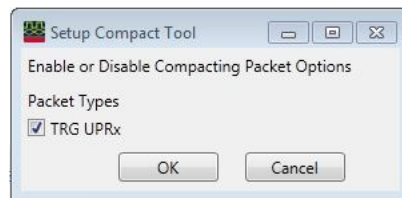
- 1 Click the drop-down arrow displayed with the **Compact** button and then select **Setup Compact**. The **Setup Compact Tool** dialog box is displayed.
- 2 Select the packet types that you want to compact into sets.
For **M-PCIE**,
 - You can compact TS1, TS2, SKP, EIEOS, InitFC, and FTS packet types.

- For TS1 and TS2 packets, you can select whether or not only identical or all TS1 and TS2 packets should be compacted. Selecting the **Only Identical** checkbox compacts only identical TS packets (that is, packets with same field values). This allows you to view when a field of a TS packet changes compared to the previous TS packet.
- You can also choose whether or not the SKP and EIEOS packets embedded within the TS1 and TS2 packets should be compacted with the applicable TS1/TS2 packets. If you do not select these check boxes, the SKP and EIEOS packets appear as individual packets after compact.

**M-PCIE**

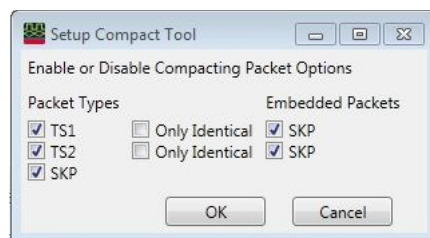
For **UniPro**,

- You can compact TRG UPRx packet types.

**UniPro**

For **SSIC**,

- You can compact TS1, TS2 and SKP packet types.
- For TS1 and TS2 packets, you can select whether or not only identical or all TS1 and TS2 packets should be compacted. Selecting the **Only Identical** checkbox compacts only identical TS packets (that is, packets with same field values). This allows you to view when a field of a TS packet changes compared to the previous TS packet.
- You can also choose whether or not the SKP packets embedded within the TS1 and TS2 packets should be compacted with the applicable TS1/TS2 packets. If you do not select these check boxes, the SKP packets appear as individual packets after compact.

**SSIC**

- 3 Click **OK**.

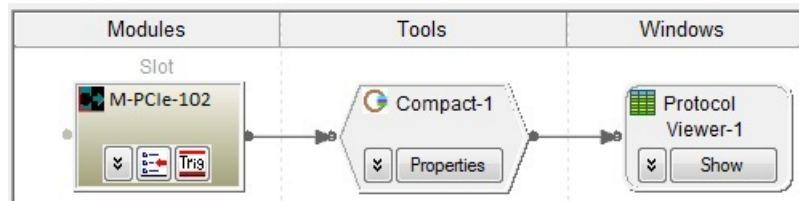
The compact setup is now configured as per your selections. When you enable compact, the packets will be compacted as per this configured setup. If the compact is already enabled, the changed setup configuration will be automatically reflected in the compacted packets display.

To enable the compact display

- 1 Click the drop-down arrow displayed with the **Compact** button and then select **Enable Compact**.

NOTE

When you enable the compact display for the first time, an instance of the Compact tool is added to your setup in the Overview window. Do not delete this tool's instance as this tool is used in the compact process.



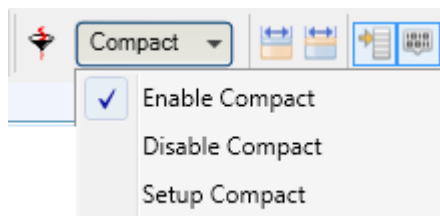
To turn off the compact display

- 1 Click the drop-down arrow displayed with the **Compact** button and then select **Disable Compact**. All packets are then listed individually.

NOTE

Clicking the Compact button flips the current display of packets from Compact to Not Compact and vice versa.

The Compact button's drop-down menu displays the current selection mode in terms of whether or not compact is currently enabled.



Viewing the Decoded Payload for a Packet

The **Payload** tab displays the decoded payload for the packet currently selected in the upper pane of the Protocol Viewer.

The following screen displays the payload of a UniPro TC0 Data Frame.

The screenshot shows the Keysight U4431A Protocol Analyzer interface. The top pane displays a list of packets. The bottom pane shows the decoded payload for the selected packet (Sample Number 1).

Sample Number	Direction	Gear	UniPro Packet	Sequence Number
0	CSI3-101	HS G1	Start of Burst /...	
1	CSI3-101	HS G1	TC0 Data Frame	01
2	CSI3-101	HS G1	TC0 Data Frame	01
3	CSI3-101	HS G1	TC0 Data Frame	01
4	CSI3-101	HS G1	TC0 Data Frame	01
5	CSI3-101	HS G1	End of Burst	
6	CSI3-101	HS G1	Start of Burst /...	
7	CSI3-101	HS G1	TC0 Data Frame	01
8	CSI3-101	HS G1	TC0 Data Frame	01

The bottom pane shows the decoded payload for the selected packet (Sample Number 1). The payload is displayed in hexadecimal and ASCII format. The word size is set to DWord, and the number of columns is 4. The endianness is set to Little Endian.

```

0000: 0000423B 000009A0 00000000 00000000  ..B;.....
0010: 09A00000 00000000 00000000 00000000  .....
0020: 09A00000 00000000 00000000 00000000  .....
0030: 09A00000 00000000 00000000 00000000  .....
0040: 09A00000 00000000 00000000 00000000  .....
0050: 09A00000 00000000 00000000 00000000  .....
0060: 09A00000 00000000 00000000 00000000  .....
0070: 09A00000 00000000 00000000 00000000  .....
  
```

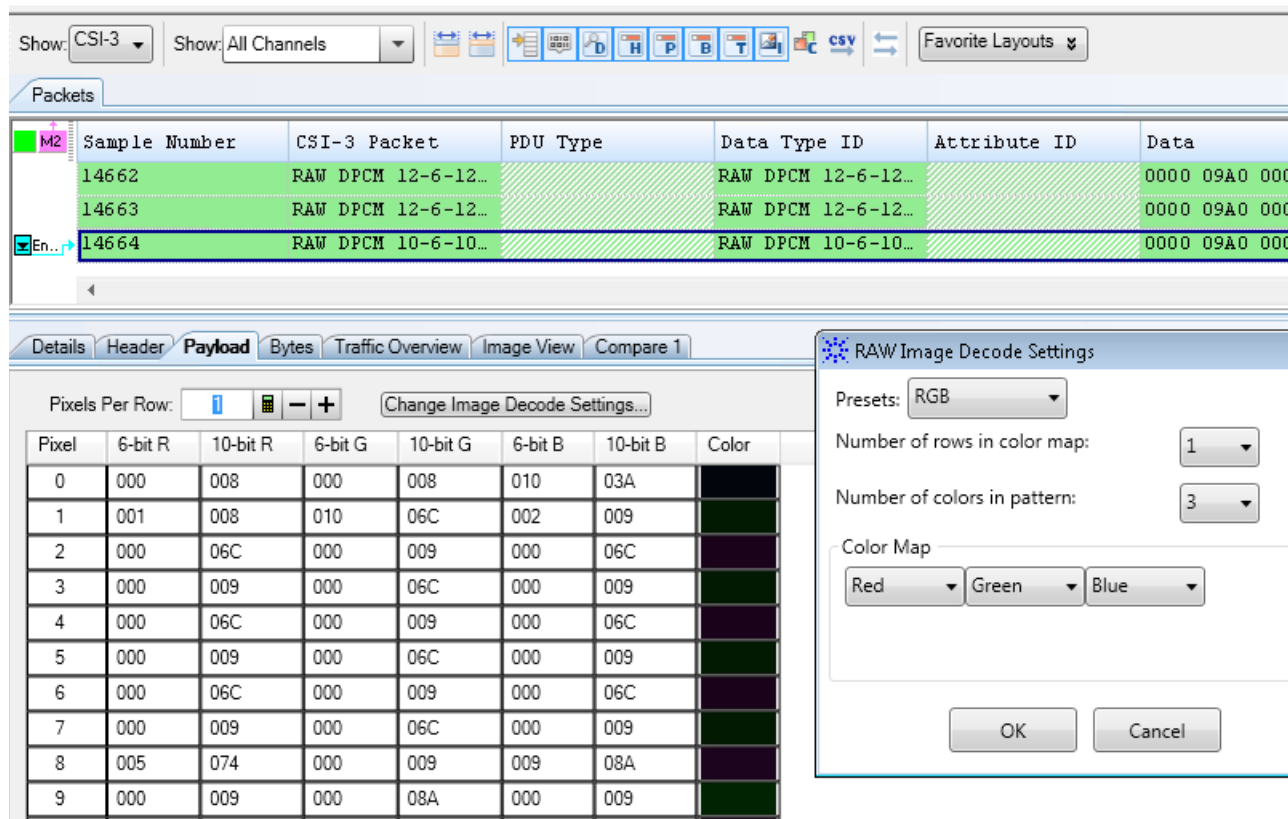
For CSI-3 image packets, the Payload tab displays the image's pixel bytes data. The payload of an RGB format CSI-3 image packet is shown below.

Sample Number	CSI-3 Packet	PDU Type	Data Type ID
0	Frame Start		Frame Start
1	Embedded 8-bit n...		Embedded 8-bit n...
2	Embedded 8-bit n...		Embedded 8-bit n...
3	RGB888		RGB888
4	Frame Start		Frame Start
5	RGB888		RGB888

Details Header Payload Bytes Traffic Overview Image View Compare 1				
Pixels Per Row: <input type="text" value="1"/>				
Pixel	Red	Green	Blue	Color
0	40	80	00	
1	A0	20	C0	
2	50	E0	60	
3	B0	30	D0	
4	C0	40	80	
5	60	A0	20	
6	FF	00	00	
7	FF	00	00	
8	FF	00	00	
9	FF	00	00	

Changing Decode Settings for CSI-3 RAW Image Payload Data

For CSI-3 RAW image formats, the Payload tab also provides the **Change Image Decode Settings** button. By default, RGB presets are used for decoding the payload of CSI-3 RAW image formats. However, you can use the **Change Image Decode Settings** button to select different decode settings for the image payload data. Clicking this button displays the **Raw Image Decode Settings** dialog box.



The screenshot shows the 'RAW Image Decode Settings' dialog box. The 'Presets' dropdown is set to 'RGB'. The 'Number of rows in color map' is set to '1' and the 'Number of colors in pattern' is set to '3'. The 'Color Map' section has three dropdowns: 'Red', 'Green', and 'Blue'. The table below shows the pixel data for 10 rows.

Pixel	6-bit R	10-bit R	6-bit G	10-bit G	6-bit B	10-bit B	Color
0	000	008	000	008	010	03A	
1	001	008	010	06C	002	009	
2	000	06C	000	009	000	06C	
3	000	009	000	06C	000	009	
4	000	06C	000	009	000	06C	
5	000	009	000	06C	000	009	
6	000	06C	000	009	000	06C	
7	000	009	000	06C	000	009	
8	005	074	000	009	009	08A	
9	000	009	000	08A	000	009	

For RAW formats, a number of preset options such as RGB, YMC, CMY, and Bayer decoding are available in the Raw Image Decode Settings dialog box. Based on the decode settings that you select in this dialog box, the image's payload data is recalculated in the Payload tab. The changed settings are also applicable while extracting an image in the **Image View** tab.

Viewing the Byte-wise Transmission of M-PHY Data

The **Bytes** tab in the lower pane of the Protocol Viewer provides a byte-wise view of the header and payload data of a packet.

The bytes data in this tab is spread across rows to display the bytes data for header followed by the bytes data for payload (if applicable).

When you select a packet listed in the upper pane of Protocol Viewer, its raw data spread across bytes used for its transmission is displayed in the Bytes tab.

NOTE

The data displayed in the Bytes tab does not represent the lane-wise view of bytes. This tab does not show the bytes spread across the lanes on which the bytes were transmitted.


If you want to view the lane-wise transmission of raw data, you use the Waveform Viewer display. In this Viewer, you can see the raw data spread across the lanes used for its transmission. For this data to be acquired and displayed in Waveform Viewer, ensure that the **Raw Data** checkbox is selected in the **Analyzer Setup** dialog box prior to data acquisition. To know more, refer to the topic "[Viewing the Lane-wise Transmission of Raw Data](#)" on page 71.

In the following figure, the data of a *DATA_OUT UFS* packet spread across the bytes used for its transmission is displayed. The red and blue highlights in this screen indicate the packet's header and payload data respectively.

The screenshot shows the 'Packets' pane with a list of packets. Packet 408 is highlighted in light blue and labeled 'DATA OUT'. Below it, packet 409 is highlighted in green and labeled 'READY TO XFER'. Packet 410 is highlighted in light blue and labeled 'DATA OUT'. Packet 411 is highlighted in red and labeled 'REJECT UPIU'. The 'Details' pane is open to the 'Bytes' tab, showing a table of bytes. The first 8 bytes (0-7) are highlighted with a red border, representing the header. The remaining bytes (8-16) are highlighted with a blue border, representing the payload.

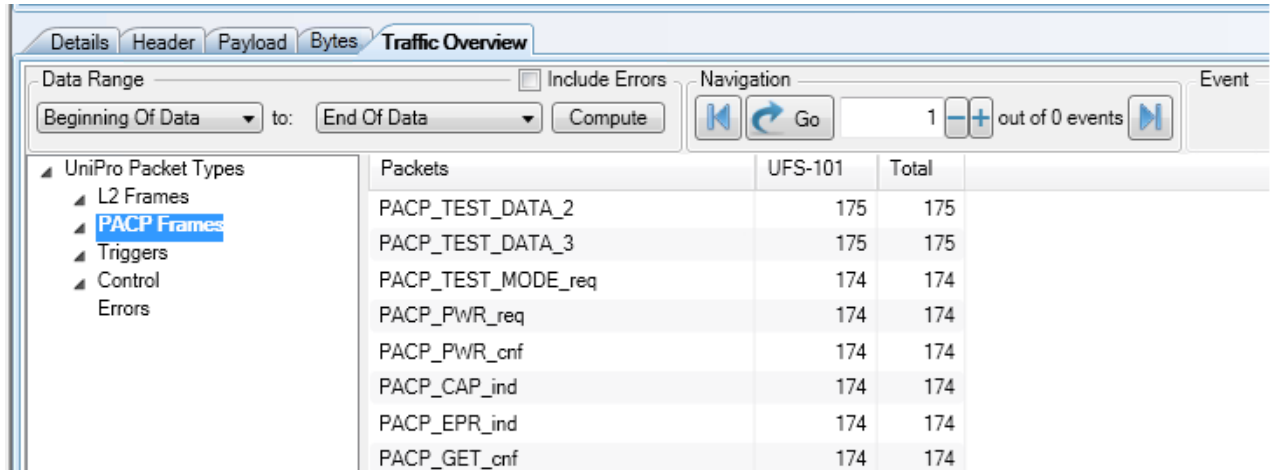
	Byte 0	Byte 1	Byte 2	Byte 3
1	02	00	00	00
2	00	00	00	00
3	00	00	00	20
4	00	00	00	00
5	00	00	00	20
6	00	00	00	00
7	00	00	00	00
8	00	00	00	00
9	01	00	03	02
10	05	04	07	06
11	0B	0A	0D	0C
12	0F	0E	00	00
13	02	01	14	03
14	06	05	00	00
15	00	00	00	00
16	00	00	00	00

NOTE

You can toggle the visibility of the Bytes tab using the **Toggle Byte Pane Visibility**  toolbar button displayed in the Protocol Viewer toolbar.

Viewing the Captured M-PHY Traffic Statistics

The Traffic Overview tab in the lower pane of the Protocol Viewer provides an overview of the M-PHY traffic listed in the upper pane. It provides a count of various types of captured M-PHY packets categorized on the basis of the type of packet. It also displays the count of M-PHY packet errors.



To view M-PHY traffic statistics

- 1 Click the **Traffic Overview** tab.
- 2 In the **Data Range** group box, specify the range of data (from the upper pane) for which you want to compute and display traffic statistics. You can also select markers set in the upper pane for defining the data range.
- 3 Select the **Include Errors** checkbox if you want a count of errored packets to be included and displayed in the traffic statistics.
- 4 Click **Compute**.
Traffic statistics are displayed for the selected data range. The left pane lists the packet types for the applicable protocol. Selecting a packet type from this list displays the total number of packets in the data range for that packet type.
- 5 Use the **Navigation** section in the Traffic Statistics tab to navigate through the packets of a particular type in the upper pane.
 - a Select a packet type from the right pane of Traffic Statistics.
 - b In the Navigation section text field, specify the packet occurrence to which you want to navigate in the upper pane.
 - c Click **Go**.

The specified packet occurrence is highlighted in the upper pane.

NOTE

The last packet type “**Errors**” listed in the left pane displays the count for the errored packets categorized on the basis of errors types. This error count is displayed only if you selected the **Include Errors** checkbox before computing traffic statistics.

Extracting Images from the Packet Data

You can use the **Image View** tab in the lower pane of the Protocol Viewer to display images constructed from the captured packet data. The Image View tab lists all the “Start of Frame” locations for images found in the captured data range specified by you. An image can be constructed from a selected “Start of Frame”.

NOTE

The **Image View** tab is available in the lower pane of the Protocol Viewer window only if the following two prerequisites are met

- the CSI-3+UniPro license is installed for the U4431A module.
- CSI-3 is selected as the protocol in the Show listbox at the top of the Protocol Viewer. For other protocols, the Image View is not applicable.

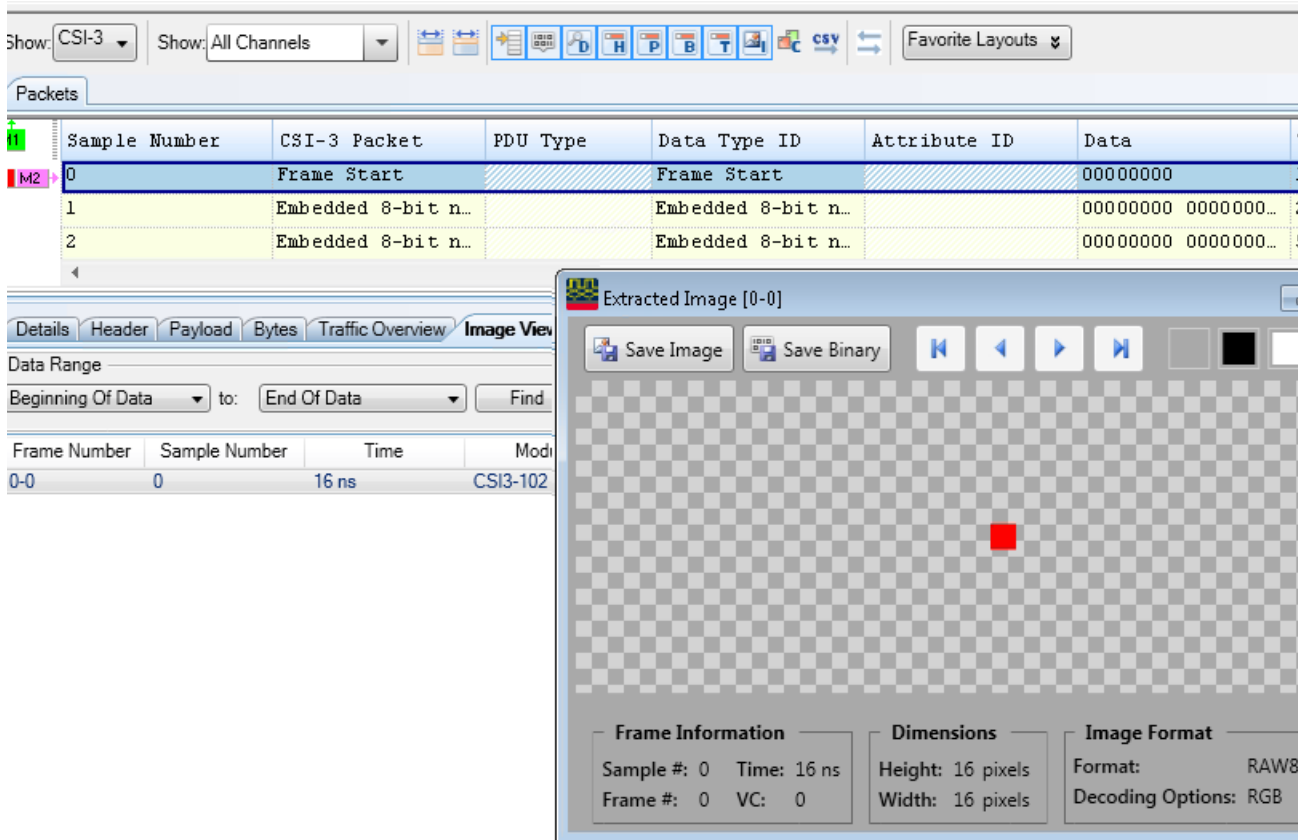
All data formats in the CSI-3 specifications are supported for image construction in the Image View tab.

To extract an image


- 1 Click the **Image View** tab.
- 2 In the **Data Range** fields, select the range of the captured data from which the Start of Frame locations are to be displayed.
- 3 From the **Display Time as** section, you can choose to display the Start of Frame locations with their absolute time or the time relative to their previous frames. The default selection is **Absolute**.
- 4 Click **Find**.
All the “Start of Frame” locations from the captured data as per the data range specified by you are listed in the Image View tab.
- 5 Select a start of frame from the displayed list and click the **Show Image** button to get an image constructed from the selected start of frame.


If you have the appropriate license, the image is constructed and displayed in the **Extracted Image** dialog box. The constructed image’s attributes such as width, height, format, and decoding options are also displayed.

A test image constructed from the start of frame 0 (sample number 0) is displayed in the following screen.



NOTE

In the **Extracted Image** dialog box, you can use the  buttons to construct and display an image from the first, next, previous or last Start of Image Frame locations currently listed in the Image View tab.

The Extracted Image dialog box also provides you four background patterns  for the displayed image.

Saving the Extracted Image


Once the extracted image is displayed in the Extracted Image dialog box, the following two options are available for saving this image:

- **Save Binary** - This button is displayed only if the extracted image is in a RAW format. Using this button, you can save the image's pixel bytes in a binary (.bin) file. This allows you to save the raw image payload data that was originally captured by the U4431A module. This raw image payload data excludes any modifications that are made as a result of the color information calculation and decoding the image.
- **Save Image** - This button is available for all image formats. It allows you to save the decoded image as a .bmp file. For an image with a RAW format, this button allows you to save the modified image that resulted from the color information calculations and decoding.

Changing the Decoding Options for the Extracted Image

By default, an image is decoded and displayed in the Extracted Image dialog box as per the image decoding options set in the **Payload** tab. If required, you can change the decoding options for the displayed image to redisplay it as per the changed decoding options.

To change the decoding options of an extracted image

- 1 Click the **Change the decoding options**  button displayed in the Extracted Image dialog box. The **Raw Image Decode Settings** dialog box is displayed.
- 2 Change the decoding options as needed. To know more, refer to the topic [“Changing Decode Settings for CSI-3 RAW Image Payload Data”](#) on page 61.

Exporting the list of “Start of Frames” locations to a CSV File

You can also export the list of Start of Frames locations to a CSV file by clicking the **Export list to CSV** button. On clicking this button, a CSV file is generated. The list of “start of frames” displayed for the specified range of captured data is exported to this CSV file. The timestamps of the start of frames list exported to this CSV file are absolute regardless of the time setting (**Absolute** or **Relative to previous frame**) that you have selected in the Image View tab.

The following screen displays a sample CSV file with the exported start of frames list.

1	Frame Nu	Sample N	Time (ns)	Module
2	0-0	0	0	CSI3-102
3	0-1	5748	1.48E+08	CSI3-102
4	0-2	8639	2.6E+08	CSI3-102

NOTE

If you want to export the protocol data displayed in the upper pane of the Protocol Viewer to a CSV file, you can use the **Export to CSV** toolbar button displayed at the top of Protocol Viewer.



The output CSV file has data matching the fields and their sequencing currently displayed in the upper pane of the Protocol Viewer.

	A	B	C	D	E	
1	Sample N	CSI-3 Packet	PDU Type	Data Type ID	Attribute	Data
2	0	Frame Start		Frame Start		0000 0000
3	1	Embedded 8-bit non Image Data		Embedded 8-bit non Image Data		0000 0000 000
4	2	Embedded 8-bit non Image Data		Embedded 8-bit non Image Data		0000 0000 000
5	3	RAW8		RAW8		0000 0000 000

Viewing Packet and Raw Signal Data using the Waveform Viewer

NOTE

The information about the Waveform Viewer in this topic is specific to viewing and analyzing M-PHY data. To get general information about the Waveform Viewer, its display, or how to use it, refer to the following topics in the Logic and Protocol Analyzer Online Help.

- **Reference > Windows > Waveform Display Window**

- **Analyzing the Captured Data > Analyzing Waveform Data**

You can view the captured M-PHY data (both packet data as well as raw signal data) as a digital waveform in the Waveform Viewer. The captured bus/signal data is displayed in a time based waveform as transmitted on the link. The Waveform view is useful in situations where you want to get an overview of the M-PHY link transitions.

NOTE

The display of raw data in the Waveform Viewer depends on whether or not you enabled the **Raw data capture** feature in the **Analyzer Setup** tab prior to data acquisition.

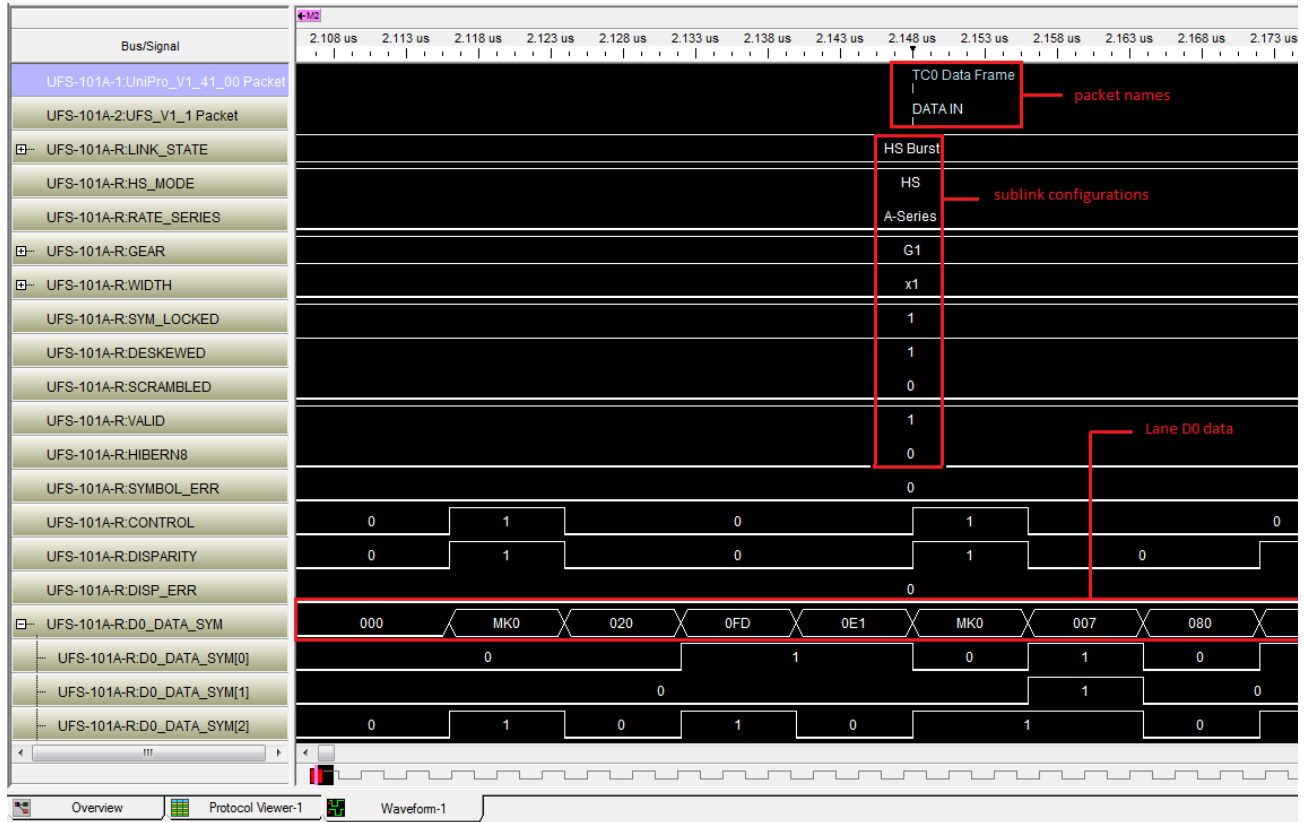
Understanding Waveform Viewer Display for M-PHY Data

A waveform display primarily displays the acquired M-PHY data as follows.

- The first row in the waveform area is the captured M-PHY/UniPro/SSIC packet name. In case of PCIe, each packet is displayed in the form of a horizontal colored bar. Each packet is identified by a separate color across the timeline and you can correlate the same color both in Waveform and Protocol viewer.
- The second row in the waveform area is the applicable UFS/CSI-3 packet name if the UFS/CSI-3 data is captured. For a bidirectional link, this is followed by the captured M-PHY/UniPro/SSIC packet name in the other direction.
- After the packet name(s), various link configuration settings as detected across the timeline are displayed such as:
 - the current state of link
 - burst mode
 - rate series
 - link width
 - whether or not data is deskewed (1 represents deskewed and 0 represents skewed)
 - whether or not data is scrambled (1 represents descrambling is enabled for scrambled data and 0 represents descrambling is disabled)
 - whether or not there is a symbol error (1 represents a symbol error in the data and 0 represents no symbol error)
 - whether or not there is a disparity error (1 represents a disparity error in the data and 0 represents no disparity error)
- The link configurations are followed by the captured lane-wise signal data values for a sublink. This includes symbol, 8b encoded, and 10b encoded data. For a bidirectional setup, the data for one sublink is followed by the data for the other sublink.

If needed, you can hide a specific information about the captured data from the waveform display by right-clicking on the information row on the left and selecting **Delete Row**.

In the following screen, the data for the **DATA IN** UFS packet (**TC0 Data Frame** UniPro packet) is displayed in the Waveform Viewer.



You can correlate the data displayed in the waveform with the relevant packet(s) in the Protocol Viewer. The following screen displays the correlated packet data for the signal data displayed in the above screen.

The screenshot displays the Protocol Viewer interface. At the top, there are filters for 'Show: UFS_V1_1' and 'Show: All Channels'. Below this is a toolbar with various icons. The main area is divided into two sections: 'Packets' and 'Details'.

Packets Section:

Sample Number	UFS_V1_1 Packet	Data	Time
4	QRESP (Success)		1.827 us
5	DATA IN	02030001 0607040...	2.148 us
6	CMD (FORMAT UNIT)		7.364 us

Details Section:

The 'Details' section is currently showing the 'DATA IN' packet. It has tabs for 'Details', 'Header', 'Payload', 'Bytes', 'Traffic Overview', and 'Compare 1'. Below the tabs, there are buttons for 'Show All Fields', 'Favorites...', and 'DATA IN'.

The 'Generated Fields' section is expanded, showing the following fields:

- Direction = UFS-101A
- Packet Length = 512
- Link Width = 1
- Gear = HS G1
- Rate Series = A-Series

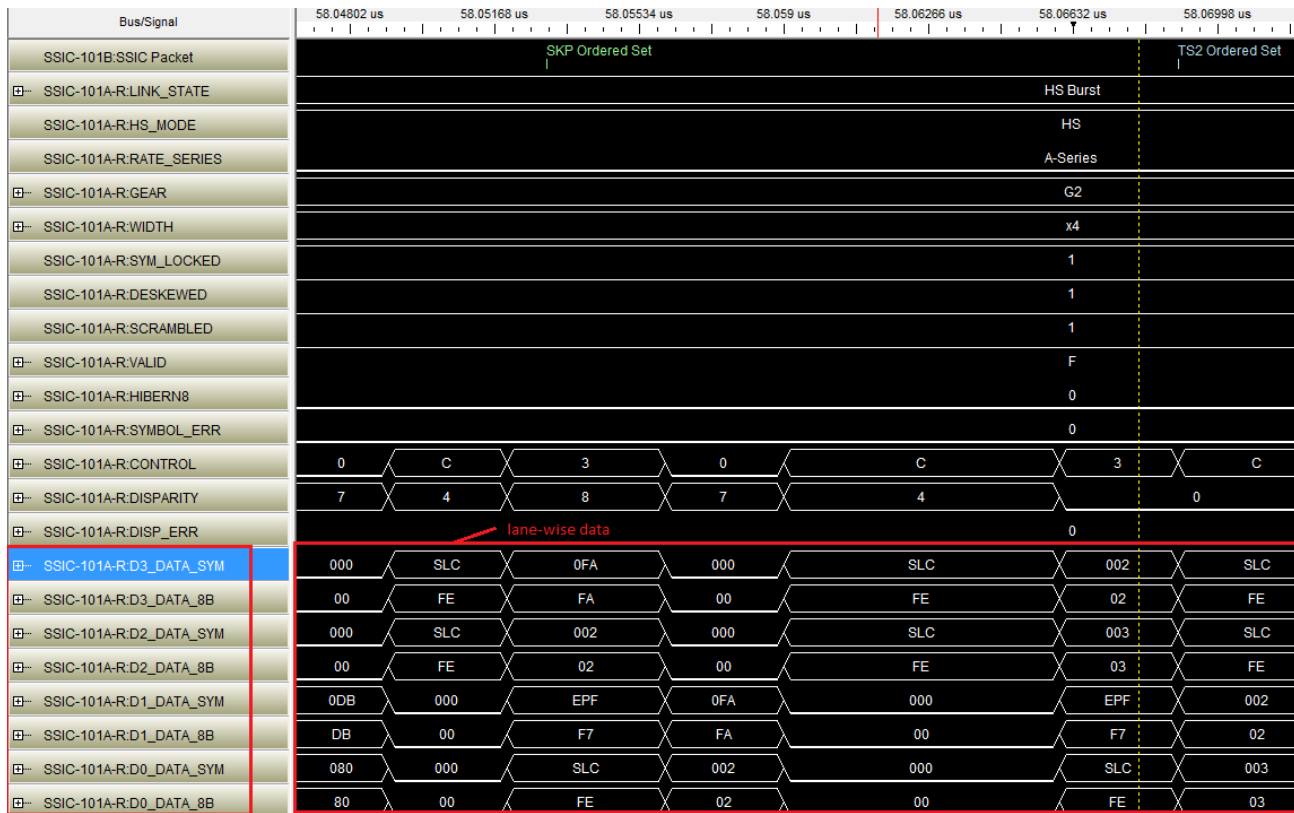
The 'UFS_V1_1' section is also expanded, showing the following fields:

- UTP
 - HD = 0 Hex
 - DD = 0 Hex
 - Transaction Type = DATA IN
 - Flags = 00 Hex
 - LUN = 00 Hex
 - Task Tag = 00 Hex
 - Reserved2 = 00 Hex
 - Reserved3 = 00 Hex
 - Reserved4 = 00 Hex
 - Reserved5 = 00 Hex
 - Total EHS Length = 00 Hex
 - Reserved6 = 00 Hex
 - Data Segment Length = 0020 Hex
 - Data Buffer Offset = 0000 0000 Hex
 - Data Buffer Count = 0000 0020 Hex
 - Reserved7 = 0000 0000 0000 0000 0000 0000 Hex
- Data
 - Data = 0100 0302 0504 0706 0B0A 0D0C 0F0E 0000 0201 1403 0605 0000 0000 0000 0000
 - Data Padding

Viewing the Lane-wise Transmission of Raw Data

In the Waveform Viewer, you can view raw data organized as per the data lanes on which it was transmitted.

For instance, in the following screen, the raw data for SSIC packets is displayed across the four lanes D0 to D3 used for its transmission. In this example, each row of data in the highlighted section of the Waveform Viewer represents the data transmitted on the lane indicated through the row's label.



Labels for data lanes

The lane-wise display of data can be skewed or deskewed based on the Raw Data Capture Mode that you selected in the Analyzer Setup tab prior to data acquisition.

NOTE

The display of raw data for each lane in the Waveform Viewer depends on whether or not you enabled the **Raw data capture** feature in the **Analyzer Setup** tab prior to data acquisition.

Viewing Deskewed and Descrambled Data

Deskewing the Acquired Data

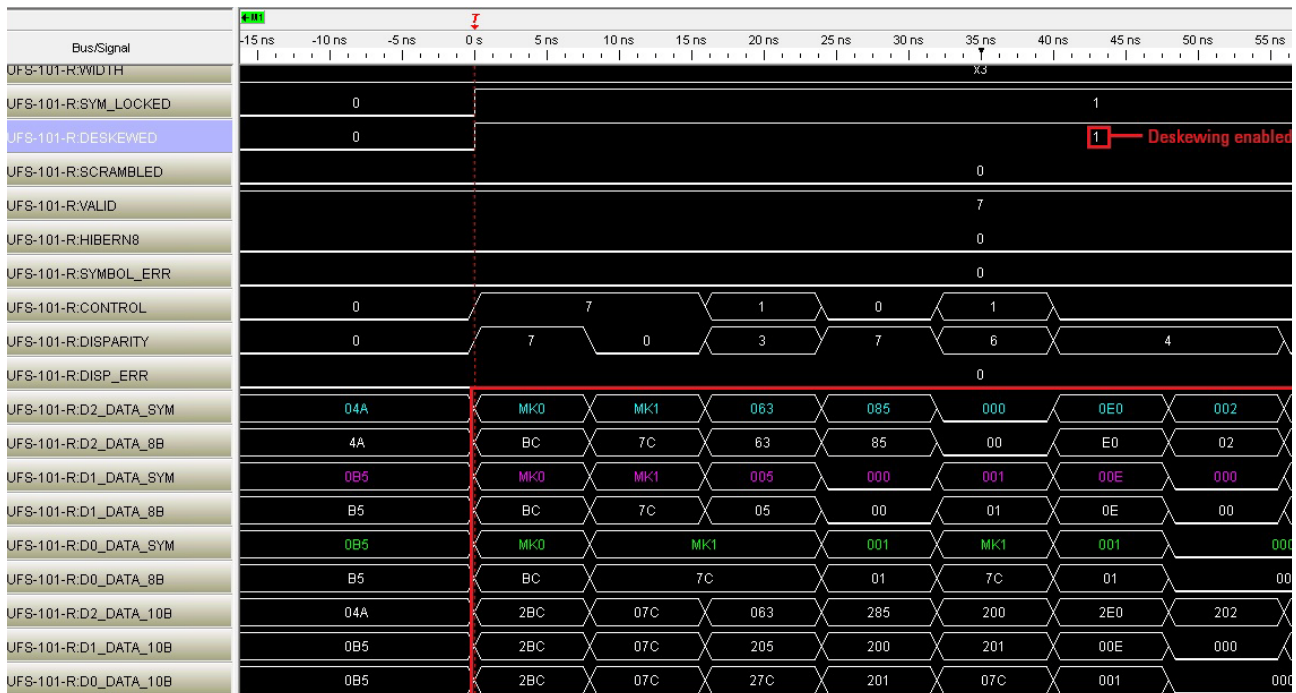
By default, the U4431A module skews the acquired data because the default selection for the **Raw Data Capture Mode** is **8b Desrambled and Deskewed Data** in case of SSIC and MPCle and **8b Deskewed Data** in case of UniPro/UFS/CSI-3.

To enable deskew for the data to be acquired

- 1 Access the **Analyzer Setup** tab of the Setup dialog box.
- 2 From the **Raw Data Capture Mode** listbox,
 - select the **8b Deskewed Data** option for UniPro/UFS/CSI-3.
 - select the **8b Desrambled and Deskewed Data** option for SSIC and MPCle.

To view deskewed data

- 1 Display the Waveform Viewer window.
- 2 Look for the **DESKEWED** row in the Waveform Viewer. Notice that, in the following example, the DESKEWED value is displayed as 1 indicating that the deskewing is enabled. The deskewed data at the start of a burst across the three lanes (D0, D1, and D2) is also highlighted in this example.



Descrambling the Scrambled Data

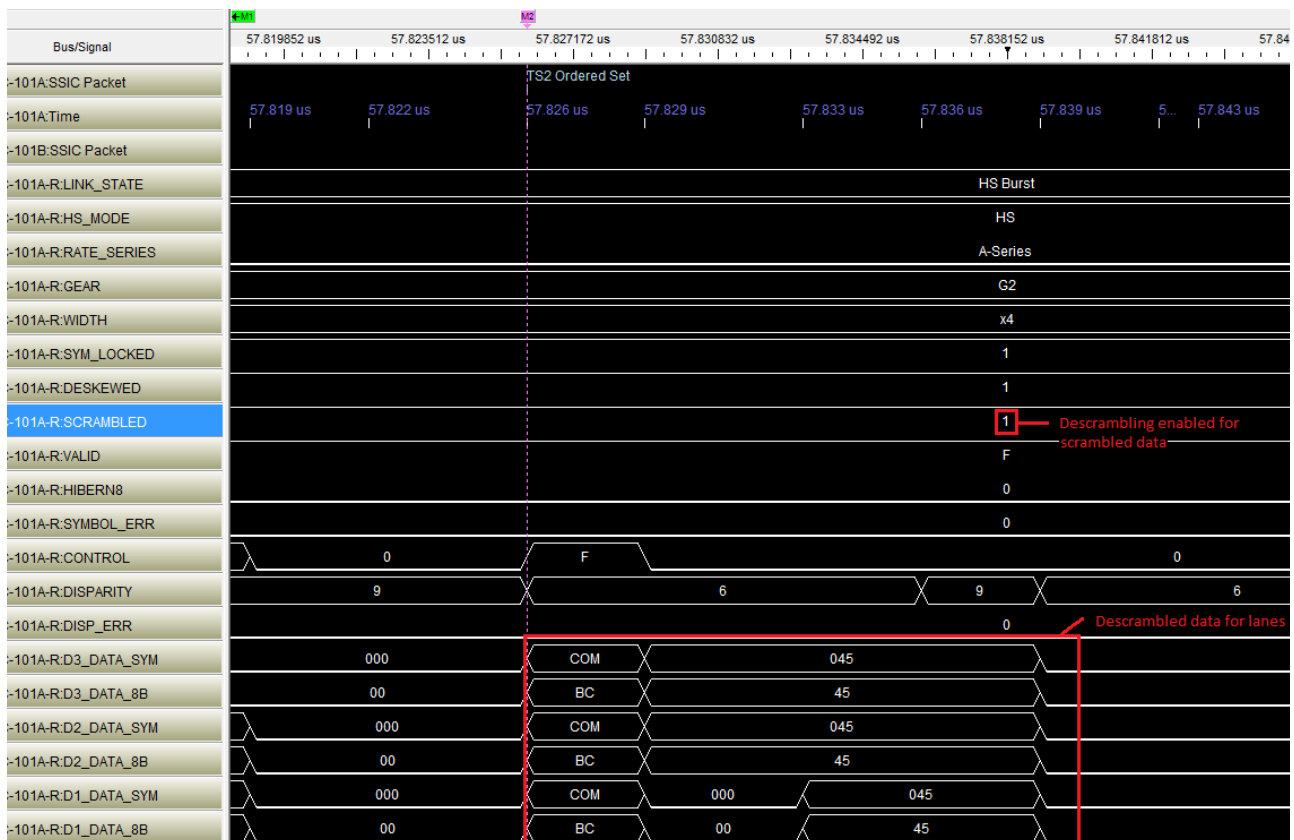
Descrambling is applicable only for SSIC and MPCle setups. By default, the U4431A module descrambles the scrambled data on the link because by default, the **Data on link is scrambled** checkbox is selected in the **Analyzer Setup** tab.

To enable descrambling for the scrambled data

- 1 Access the **Analyzer Setup** tab of the **Setup** dialog box.
- 2 Ensure that the following two options are selected:
 - the **Data on link is scrambled** checkbox is selected
 - the **8b Descrambled and Deskewed Data** option or the **8b Descrambled Data** option is selected from the **Raw Data Capture Mode** listbox.

To view descrambled data

- 1 Display Waveform Viewer.
- 2 Look for the **SCRAMBLED** row in the Waveform Viewer. Notice that, in the following example, the SCRAMBLED value is displayed as 1 indicating that the data on the link is scrambled for which descrambling has been enabled. The descrambled data across lanes is also highlighted in red in this example.

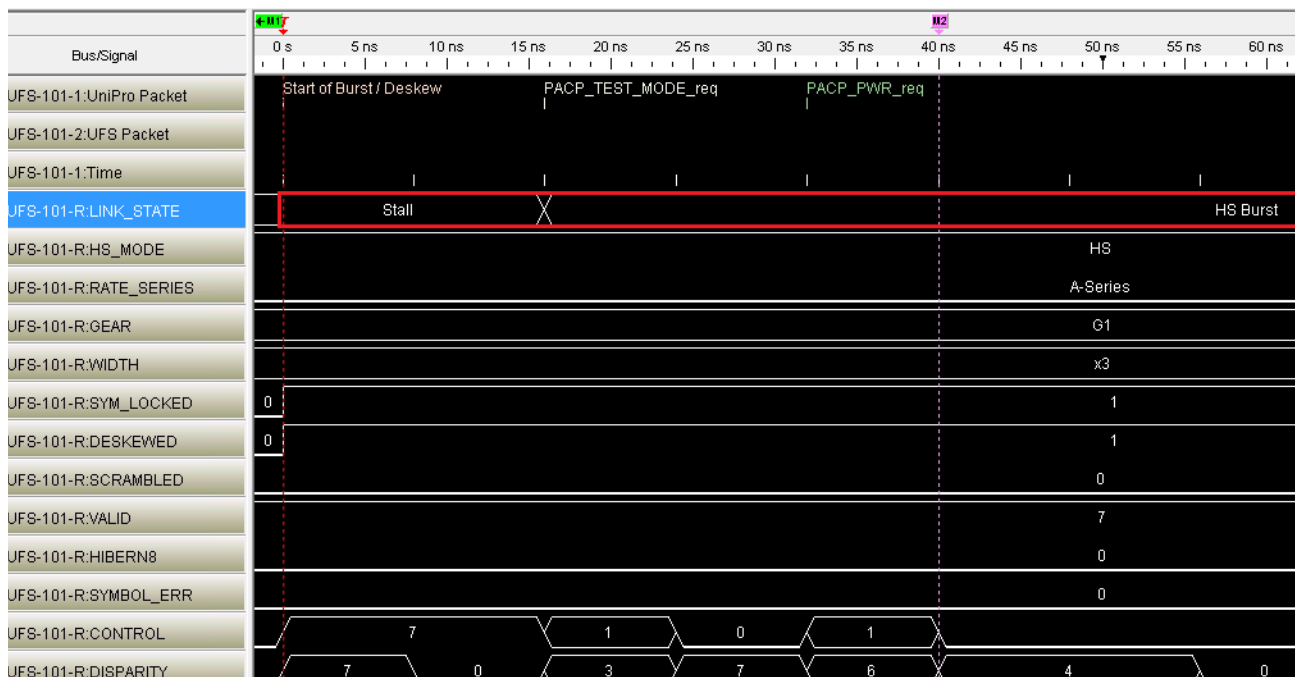


Tracking and Viewing Link States in Captured Data

In the Waveform Viewer, you can track and view the changes in the link's state of the acquired data. The **LINK_STATE** row in the Waveform Viewer displays the link's transitions from one state to another across the timeline of the acquired data.

In case of a bidirectional acquisition, the state of Transmit and Receive sublinks is displayed separately.

In the following screen, the row displaying the link state is highlighted.



Viewing Time Synchronized Protocol Data in Display Windows

For analysis or debugging, you may want to view the captured data in a display window along with its related protocol/signal data in another window such that the displays are time synchronized for ease of mapping and viewing related data. One such situation can be viewing UniPro protocol data in a Protocol Viewer and the related UFS protocol data in another Protocol Viewer. Or, viewing SSIC protocol data in a Protocol Viewer and its related signal data in a Waveform Viewer.

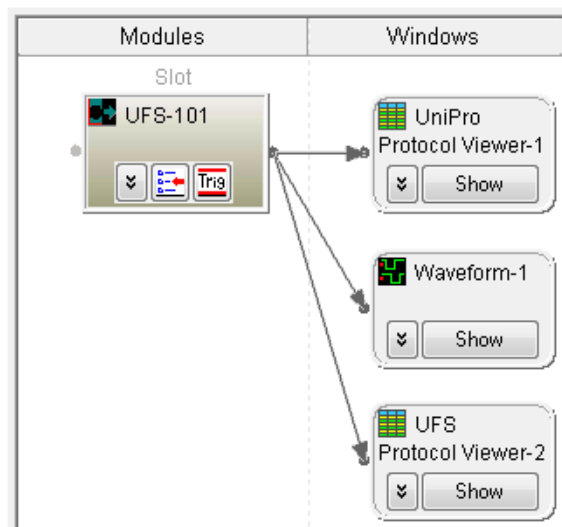
In such situations, you can use the Lockstep windows feature to ensure that when a Protocol Viewer window is scrolled, other lock stepped windows are automatically scrolled as well, such that the same time is centered in each lock stepped display window. This allows you to easily map and view correlated data in multiple display windows.

You can lockstep a Protocol Viewer window with display windows such as another Protocol Viewer, Waveform, Listing, or Compare.

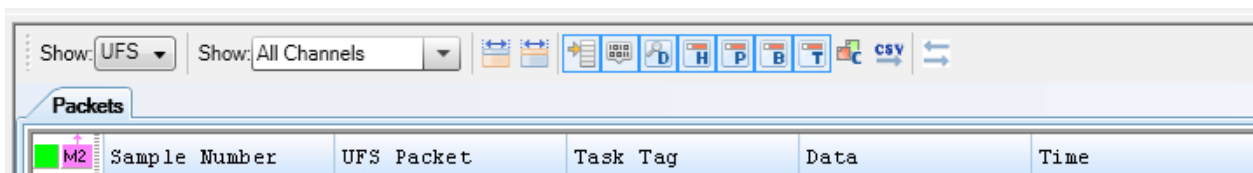
Some examples of using the lockstep feature with the data captured using U4431A are described below.


To lockstep UniPro protocol data with UFS protocol data

- 1 In the **Overview** window, ensure that you have added two instances of Protocol Viewers, one for the UniPro data and another for UFS data.

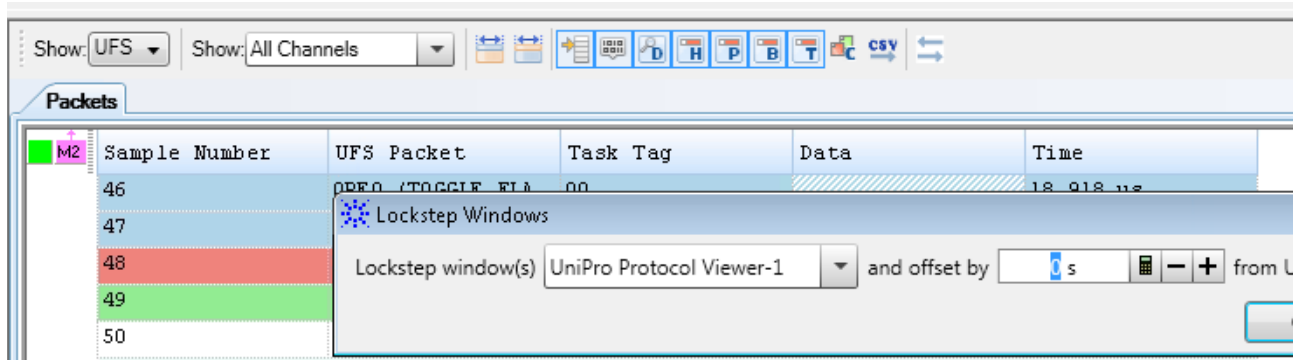


- 2 Ensure that each of the Protocol Viewer instance shows the data specific to the protocol for which it was created. Use the **Show** button in Protocol Viewer to show data specific to a protocol.



- 3 From the Protocol Viewer toolbar, click the  **Lockstep Windows** toolbar button.

In the **Lockstep Windows** dialog, select the other instance of Protocol Viewer with which you want to synchronize scrolling and specify any time offset from this window. For instance, from the UFS Protocol Viewer instance, select the UniPro Protocol Viewer instance.



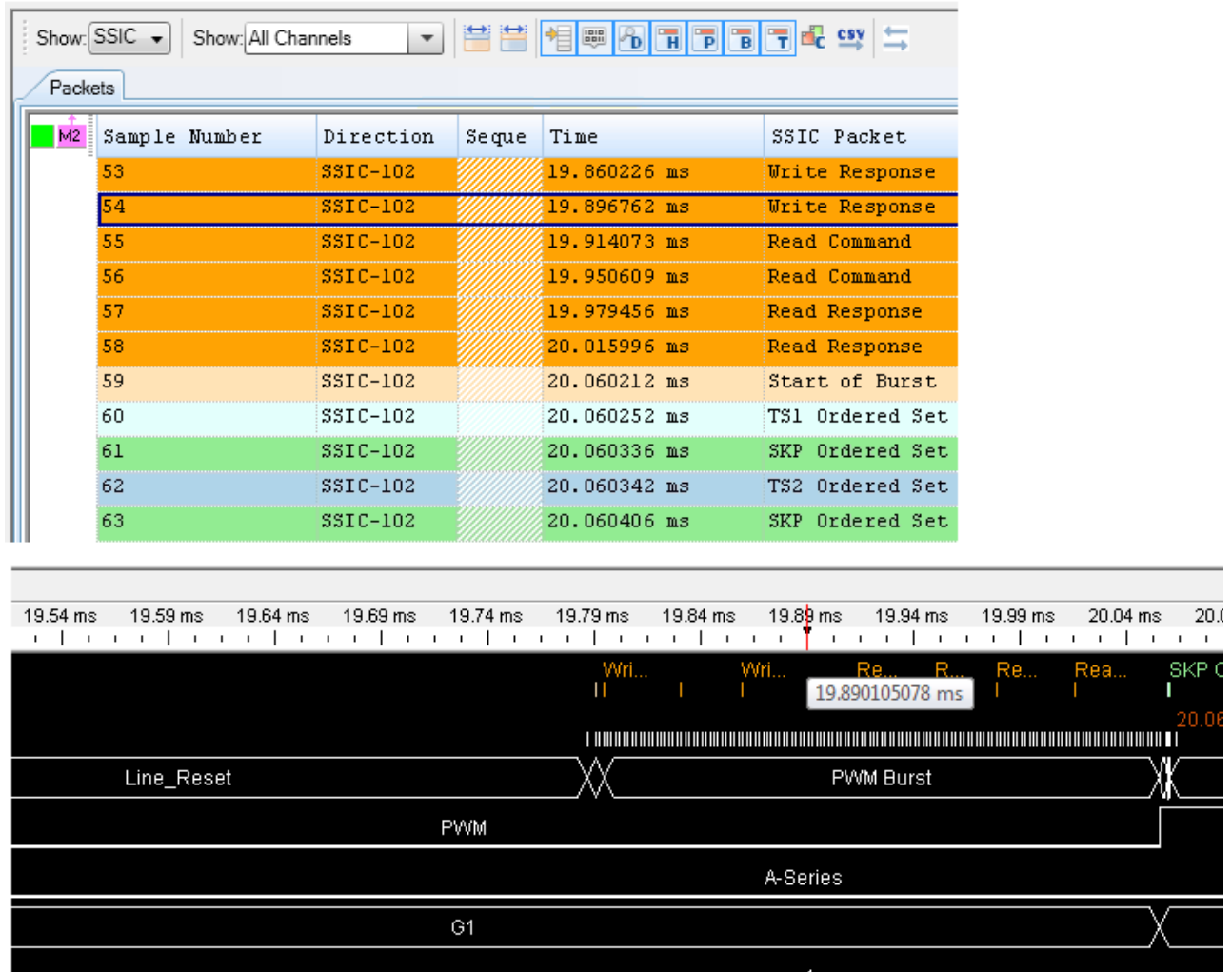
- 4 Click **OK** to close the **Lockstep Windows** dialog.

Once the windows are lockstepped, scrolling or moving to a specific marker in the lockstepped window automatically scrolls the other window to the appropriate location.

Sample Num	UFS Packet	Task Tag	Time	Data
5751	QREQ (NOP)	00	2.315009 ms	
5752	QREQ (READ DESC...	00	2.315330 ms	
5753	QREQ (READ ATTRI...	00	2.315650 ms	
5754	QREQ (WRITE ATTR...	00	2.315970 ms	
5755	QREQ (READ FLAG)	00	2.316291 ms	
5756	QREQ (SET FLAG)	00	2.316612 ms	
5757	QREQ (CLEAR FLAG)	00	2.316932 ms	
5758	QREQ (TOGGLE FLA...	00	2.317252 ms	
5759	QREQ (WRITE DESC...	00	2.317573 ms	

Sample Numbe	Direction	Gear	UniPro Packet	Time
5975	UFS-101	HS G1	TC0 Data Frame	2.314368 ms
5976	UFS-101	HS G1	TC0 Data Frame	2.314688 ms
5977	UFS-101	HS G1	TC0 Data Frame	2.315009 ms
5978	UFS-101	HS G1	TC0 Data Frame	2.315330 ms
5979	UFS-101	HS G1	TC0 Data Frame	2.315650 ms
5980	UFS-101	HS G1	TC0 Data Frame	2.315970 ms
5981	UFS-101	HS G1	TC0 Data Frame	2.316291 ms
5982	UFS-101	HS G1	TC0 Data Frame	2.316612 ms
5983	UFS-101	HS G1	TC0 Data Frame	2.316932 ms

Similarly, you can lockstep a Protocol Viewer window with a Waveform Viewer window to ensure that when you scroll in Waveform Viewer window, protocol data in Protocol Viewer is scrolled. The following screen shows a synchronized display of SSIC data in Protocol Viewer and Waveform Viewer windows using Lockstep feature.



6 Viewing LTSSM States and State Transitions

LTSSM Overview / 80
Configuring and Computing LTSSM States / 84
Viewing LTSSM States/Transitions Data / 86
Navigating Through the LTSSM Transitions/States occurrences / 89
Interpreting LTSSM States and Transition Results / 91

The U4431A M-PHY analyzer lets you view LTSSM states and state transitions as detected in the data captured in a trace. Using this data, you can test or debug the DUT's LTSSM functions.

This chapter describes how you can view these LTSSM states.

NOTE

The LTSSM feature is available only in SSIC and MPCle protocols.

NOTE

The number of SSIC states is less than MPCle states.

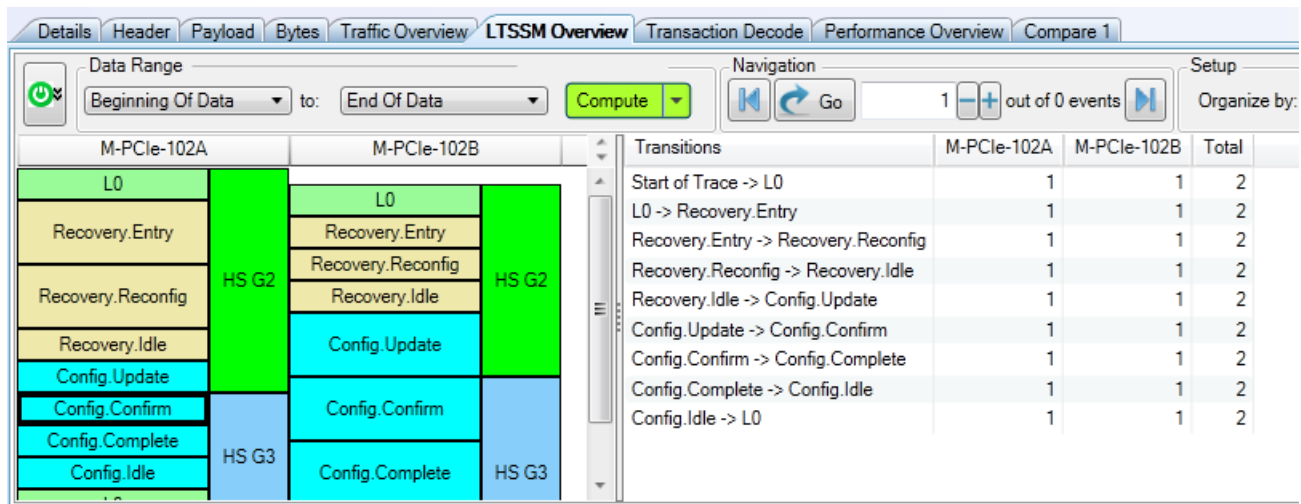
LTSSM Overview

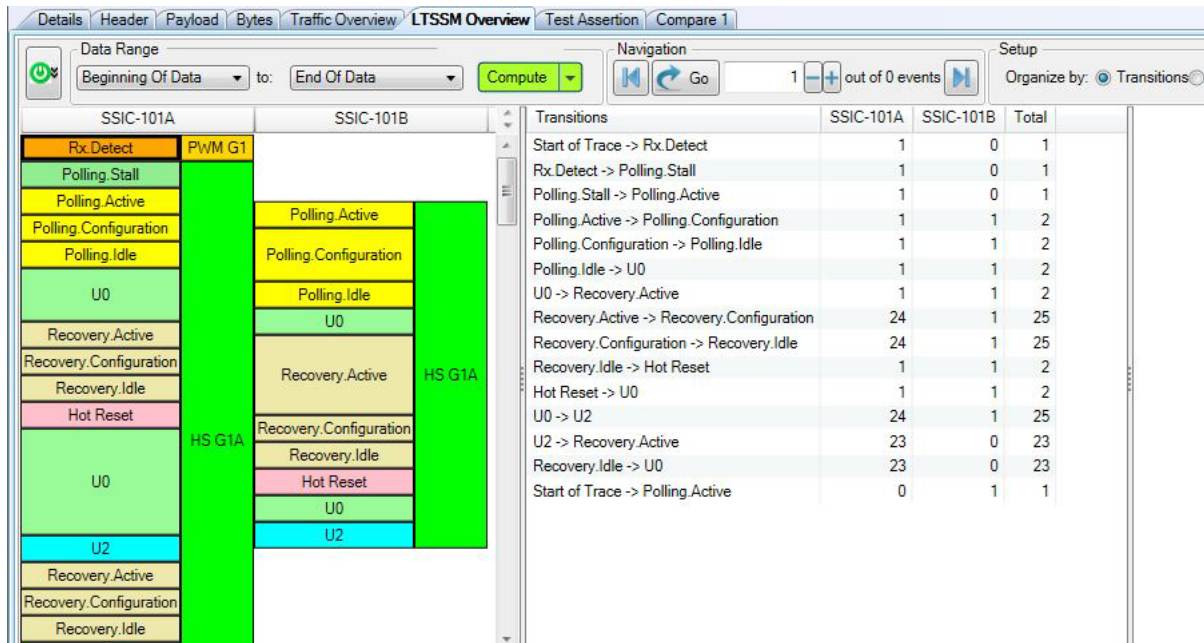
Link Training and Status State Machine (LTSSM) drives and controls the link initialization and training process for a device to enable the normal data exchange between the two devices over the link. LTSSM operates at the physical layer level and transits through various states and substates during link initialization, training, and management. During each of these states, appropriate physical layer packets (training sequences) are exchanged between the link partners to initialize, train, and manage the link.

To begin communication with a device, the link training process must complete successfully. This makes link training one of the most crucial process in testing and validating a DUT.

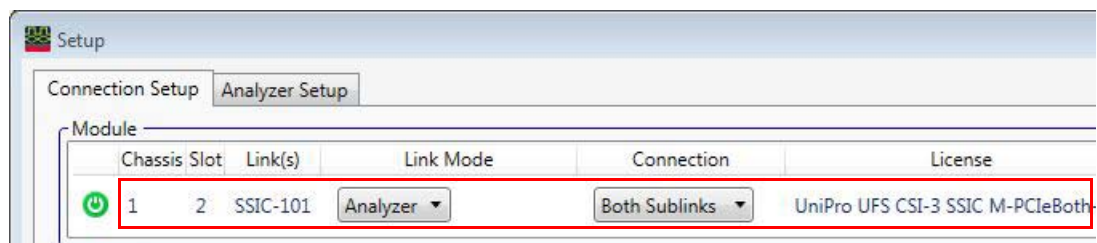
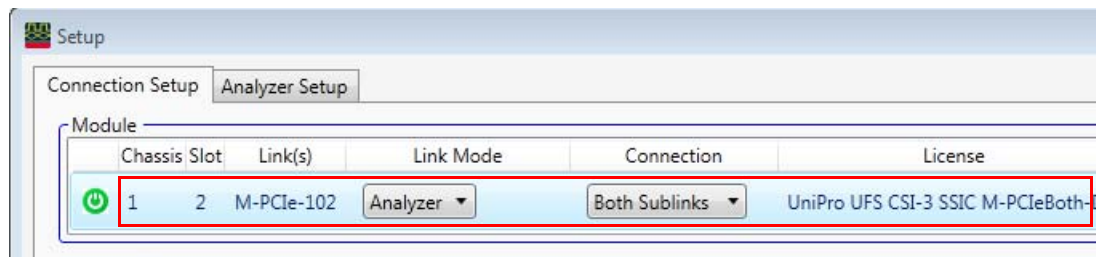
The **LTSSM Overview** pane in the Protocol Viewer window of the Logic Analyzer GUI helps you in verifying the link training process and finding out reasons for any failure in this process. This pane provides an overview of the link training process by displaying a sequential list of the LTSSM states and their transitions.

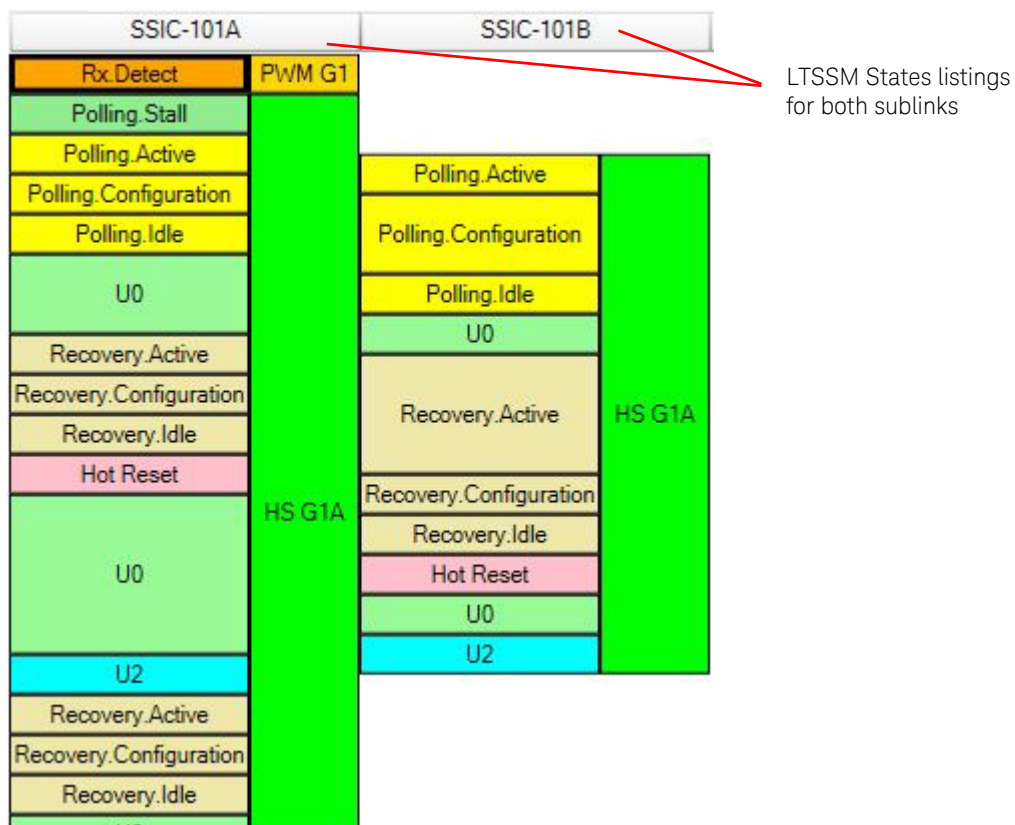
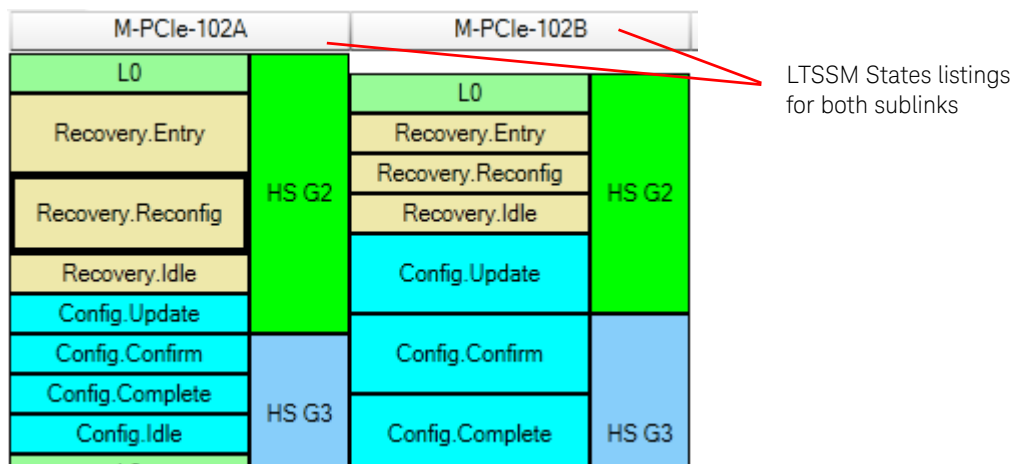
To display these states and transitions, the software analyzes a user-specified data range or the entire data captured in a trace and presents the list of transitions/states data, from that trace.





The LTSSM Overview pane can display LTSSM transition and state data, for one as well as both sub-links. However, this display depends on how you configured the connection of data capture (One Sublink, or Both Sublinks) in the Connection Setup tab of the Setup dialog box of the U4431A module. For instance, if you configured the data capture of both sublinks using U4431A, then LTSSM states are displayed for both links.





Using the LTSSM Overview pane, you can view the LTSSM transitions and states, during events such as:

- Link initialization and configuration to bring the link to an operational state.
- Link recovery or retraining in situations such as speed changes, power management, or recovering from a link error.
- Downgrading or upgrading the link speed in response to a link speed change request.

Prerequisites

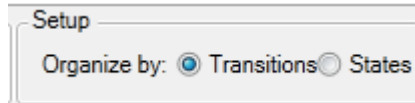
Before you can view LTSSM transitions and states, you need to ensure that:

- You have the appropriate node/server software license available and installed for the LTSSM Overview feature.
- You have set up the U4431A analyzer module and captured the data for the required link(s).
- You have configured the LTSSM setup to get the data display in the LTSSM overview pane according to your requirements. (Described in the next section)

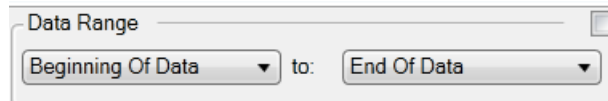
Configuring and Computing LTSSM States

To view LTSSM data in the LTSSM Overview pane, you need to configure the pane settings and initiate computation of the LTSSM data display based on these settings.

- 1 Access the captured data along with the U4431A setup details in the Logic Analyzer GUI.
- 2 Click the **Protocol Viewer** node connected to the U4431A module in the **Overview** window of the Logic Analyzer GUI.
- 3 Click the **LTSSM Overview** tab in the lower pane of the Protocol Viewer window.
- 4 Configure the LTSSM setup to get a display of LTSSM States/Transition as per your requirements.
 - a To configure how the computed LTSSM data should be organized in the LTSSM Overview pane, select the appropriate **Organize By** mode - By Transitions, or By States from the **Setup** groupbox.
 - **Transitions** displays the LTSSM states transitions in the results for navigating through the occurrences of the state transition events in the analyzed data.
 - **States** displays the LTSSM states in the results for navigating through the occurrences of the state entry events in the analyzed data.



- b In the **Data Range** groupbox, specify the start and end range for the captured data in the trace for which you want to display the LTSSM data. Only the specified range of data is analyzed to detect the LTSSM states. The default selections in the Data Range group box ensure that the LTSSM data is displayed for the entire trace. However, you can set markers in the packets listing in the upper pane and then specify the data range using these markers so that LTSSM data is displayed only for that specific range of packets.



- 5 Click the **Compute** button displayed with the **Data Range** fields. There are following two options available as a drop-down menu with this Compute button.
 - **Compute All** allows you to compute traffic overview statistics, decoded transactions, offline performance summary, and LTSSM data (in their respective tabs) for the captured packets by a single click of this button.
 - **Compute This** allows you to compute only LTSSM data from the captured packets. When you click **Compute**, then also only LTSSM data is computed.

NOTE

If you want to compute LTSSM data in background while the U4431 module is capturing data, then select the **Compute All on Run** check box in the Protocol Viewer and click the **Run** toolbar button. This allows you to perform a compute for all the relevant tabs in Protocol Viewer automatically after the data capture is complete. It thereby helps you perform a faster compute than performing a compute for each tab individually after the data is captured.




The LTSSM data is computed and displayed for the specified data range and configured link direction. Refer to the next sections to view how the LTSSM states and transitions data is displayed.

NOTE

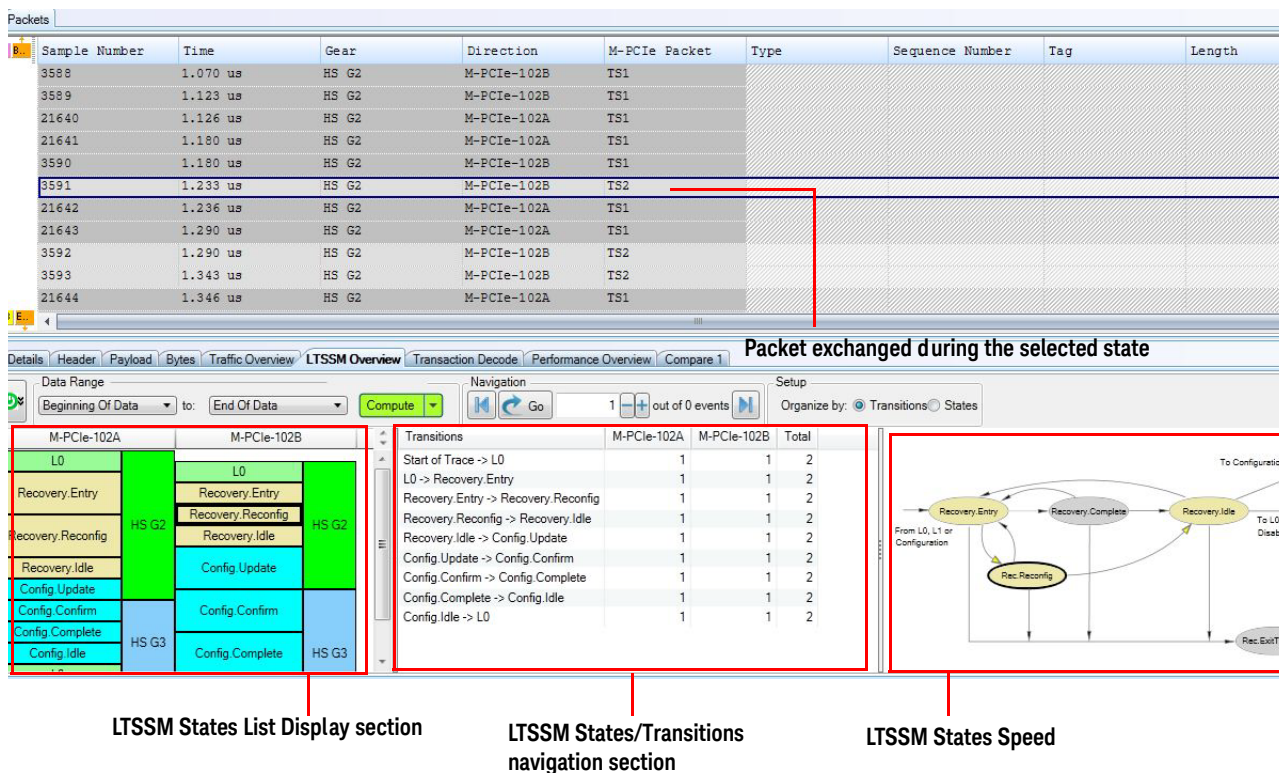
You need to recompute the LTSSM states display results if you want to change:

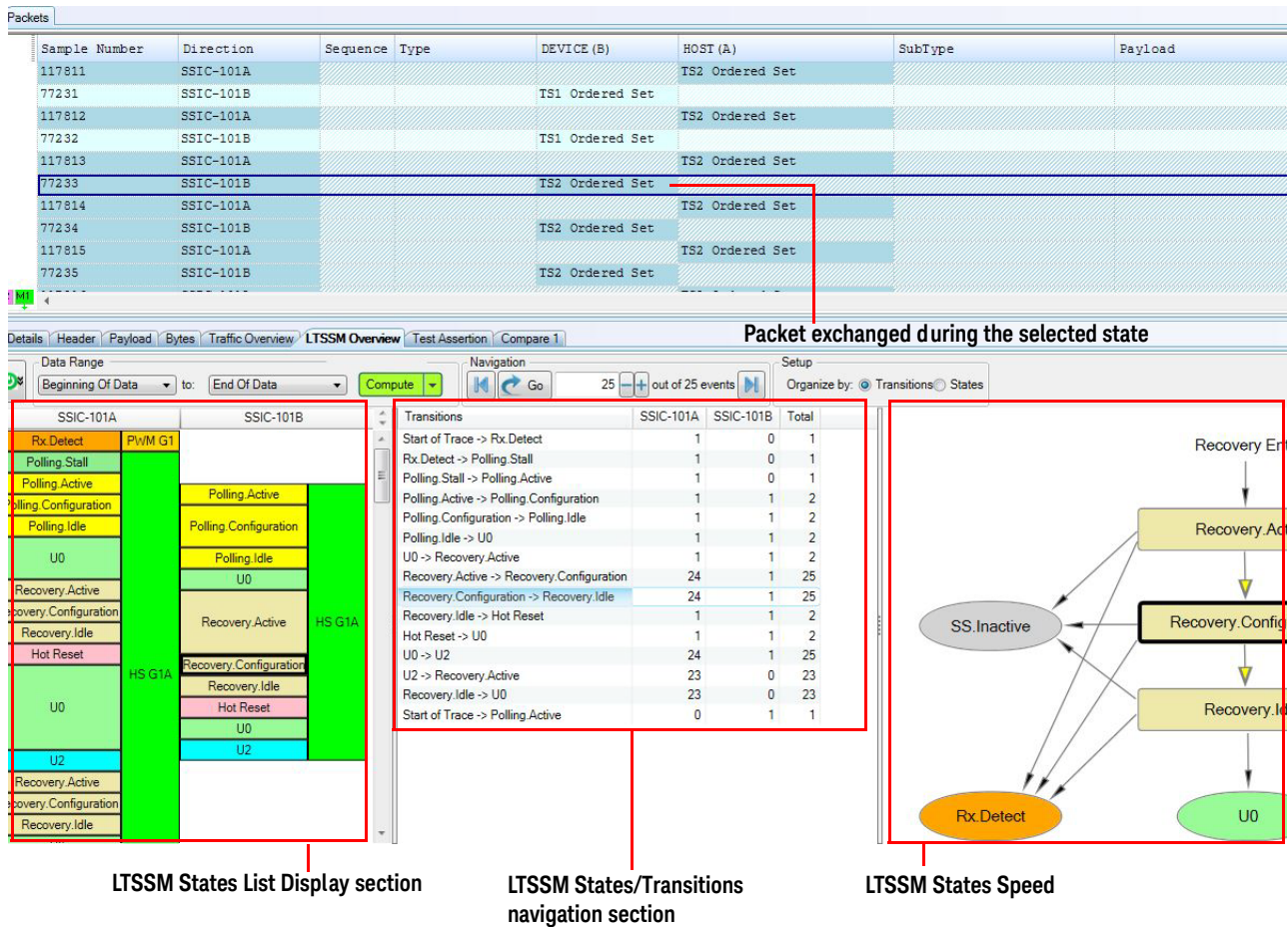
- the data range for which results are to be displayed.
- the LTSSM navigation mode for the navigation pane.
- the link direction for which results are to be displayed.

You can hide or display the LTSSM Overview pane using the  toolbar button in the upper pane of the Protocol Viewer window.

Viewing LTSSM States/Transitions Data

The screen below displays the results of a compute operation for LTSSM states followed by a description of these results.



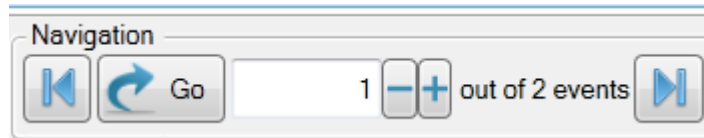


The LTSSM states results are described in the following sections, as highlighted in the pane in the above screen.

LTSSM State Results	Description
LTSSM States List Display section	<p>This section displays a list of the LTSSM states in the sequence in which these occurred in the trace or the specified part of the trace. The states are grouped and listed for a link direction. In the above screen, LTSSM states are displayed for both sublinks</p> <p>When you click a state in the list, the packet that is exchanged as the first packet for that state occurrence is highlighted in the upper pane of the Protocol Viewer. This provides you a quick start point for viewing and navigating through the packets exchanged during a particular state.</p> <p>Moving the mouse pointer to a state presents a tool tip with useful information about the state such as the time tag for the state and the packet exchanged at the state change.</p>
LTSSM States/Transitions Navigation section	<p>This section displays a list of the applicable LTSSM states or state transition names. The display of states or transitions in this section depends on whether you selected Transitions or States organization mode in the Setup groupbox. In the above screen, the display and organization in the navigation section is as per the <i>Transitions</i> mode.</p> <p>For each of these states/transitions, the section displays the number of events representing the number of occurrences of these states or transitions in the analyzed data.</p> <p>Using this section, you can easily navigate through these occurrences of LTSSM states or transitions and the packets exchanged during these occurrences.</p>
LTSSM States Speed section	<p>For each state in LTSSM States List Display section, the applicable link speed during the state is displayed. Clicking a speed in this list highlights the packet representing the transition to that speed in the upper pane of the Protocol Viewer. Clicking the gear on LTSSM States List Display section highlights states speed in blue text.</p>

Navigating Through the LTSSM Transitions/States occurrences

You can use the Navigation groupbox in the LTSSM Overview tab to navigate through LTSSM transitions/states occurrences.



- 1 To navigate through the occurrences of a state for both the directions, select the particular transition/state occurrence in the navigation section. To navigate through the occurrences of a state for a specific direction, select the number of events displayed for that direction in front of the transition/state name in the navigation section.
- 2 Either type the number of occurrence to which you want to navigate in the Navigation groupbox and click **Go** or click the **+** or **-** buttons in the groupbox to sequentially move to next or previous occurrence of the transition/state.



The specified occurrence of the selected state/transition is highlighted in the state list display and the associated packet is highlighted in the upper pane of the Protocol Viewer window. For instance, in the following screen, the first occurrence of the transition from Recovery.Reconfig to Recovery.Idle is highlighted in the states list along with the TS2 packet representing the beginning of the first occurrence of Recovery.Idle state.

Sample Number	Time	Gear	Direction	M-PCIe Packet	Type
3597	1.563 us	HS G2	M-PCIe-102B	TS2	
21648	1.566 us	HS G2	M-PCIe-102A	TS1	
3598	1.616 us	HS G2	M-PCIe-102B	TS2	
21649	1.620 us	HS G2	M-PCIe-102A	TS1	
3599	1.673 us	HS G2	M-PCIe-102B	SKP OS	
21650	1.676 us	HS G2	M-PCIe-102A	TS2	
3600	1.686 us	HS G2	M-PCIe-102B	TS2	

M-PCIe-102A		M-PCIe-102B		Transitions		M-PCIe-102A		M-PCIe-102B		Total	
L0		L0		Start of Trace -> L0		1		1		2	
Recovery.Entry		Recovery.Entry		L0 -> Recovery.Entry		1		1		2	
Recovery.Reconfig	HS G2	Recovery.Reconfig		Recovery.Entry -> Recovery.Reconfig		1		1		2	
Recovery.Reconfig	HS G2	Recovery.Idle	HS G2	Recovery.Reconfig -> Recovery.Idle		1		1		2	
Recovery.Idle		Recovery.Idle		Recovery.Idle -> Config.Update		1		1		2	
Config.Update		Config.Update		Config.Update -> Config.Confirm		1		1		2	
Config.Confirm		Config.Confirm		Config.Confirm -> Config.Complete		1		1		2	
Config.Confirm		Config.Confirm		Config.Complete -> Config.Idle		1		1		2	
				Config.Idle -> L0		1		1		2	

Sample Number	Direction	Sequence	Type	DEVICE (B)	HOST (A)
77221	SSIC-101B			TS1 Ordered Set	
117801	SSIC-101A				TS1 Ordered Set
77222	SSIC-101B			TS1 Ordered Set	
117802	SSIC-101A				TS1 Ordered Set
77223	SSIC-101B			TS1 Ordered Set	
117803	SSIC-101A				TS2 Ordered Set
77224	SSIC-101B			TS1 Ordered Set	
117804	SSIC-101A				SKP Ordered Set
117805	SSIC-101A				TS2 Ordered Set
77225	SSIC-101B			TS1 Ordered Set	

Transitions	SSIC-101A	SSIC-101B	Total
Start of Trace -> Rx.Detect	1	0	1
Rx.Detect -> Polling.Stall	1	0	1
Polling.Stall -> Polling.Active	1	0	1
Polling.Active -> Polling.Configuration	1	1	2
Polling.Configuration -> Polling.Idle	1	1	2
Polling.Idle -> U0	1	1	2
U0 -> Recovery.Active	1	1	2
Recovery.Active -> Recovery.Configuration	24	1	25
Recovery.Configuration -> Recovery.Idle	24	1	25
Recovery.Idle -> Hot Reset	1	1	2
Hot Reset -> U0	1	1	2
U0 -> U2	24	1	25
U2 -> Recovery.Active	23	0	23

- Click  toolbar button to navigate to the first packet of the first occurrence of the selected state/transition.
- Click  toolbar button to navigate to the packet representing the transition to the last occurrence of the selected state/transition.

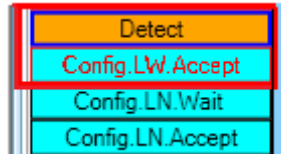
NOTE

You can also double-click a state, or transition entry to quickly navigate to its associated packet in the upper pane.

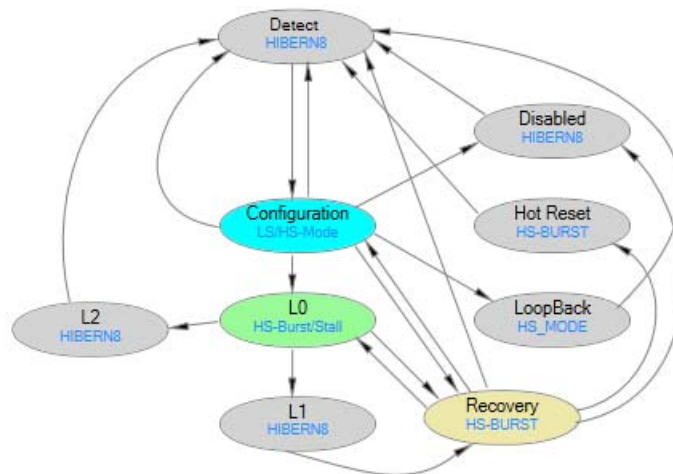
Interpreting LTSSM States and Transition Results

Following are some important points about interpreting the states and transitions displayed in the LTSSM Overview tab:

- **Errored LTSSM states** - If an LTSSM state is listed in red colored text, it indicates an erroneous state transition. Following is an example of an erroneous state transition from Detect to Config.LW.Accept. There should have been a transition to the Polling state after Detect.



- The following are the speeds in the speed column of the states listing.
 - HS
 - LS
 - PWM
- The pane displays LTSSM states as detected from the trace. If some events are not represented in the captured data in the trace, then these events will not be part of the LTSSM state transitions list in the LTSSM Overview pane.
- The LTSSM state diagram highlights which states have been entered in a given trace. This diagrammatic representation can help you quickly assess whether or not the states occurred as expected and required.



7 Computing and Viewing Decoded Transactions

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The U4431A Analyzer module can decode and display transactions from the captured MPCle and SSIC data. This chapter describes how you can compute and view these decoded transactions.

Transaction Decoding - Overview

The **Transaction Decode** tab in the Protocol Viewer window allows you to compute and view transactions decoded from the captured MPCle or SSIC traffic. The decoding and display of transactions is done as per the relevant storage protocol specifications to help you easily correlate the decoded data to the protocol specifications and evaluate DUT's compliance to these specifications.

Types of Protocols Supported

Decoding of MPCle and SSIC transactions is supported.

Transaction Decode Tab

You use the **Transaction Decode** tab displayed in the lower pane of the Protocol Viewer to compute and view decoded transactions.

Dir	Timestamp	Transaction Type	Requestor ID
I-PCle-101A	224 ns	Config Read Type 0	000:00:0
I-PCle-101A	449 ns	Config Read Type 0	000:00:0
I-PCle-101A	673 ns	Config Read Type 0	000:00:0
I-PCle-101A	898 ns	Config Read Type 0	000:00:0

Transactions / Directions->	M-PCle-101A	M-
Config Read Type 0	457	
Config Read Type 0 missing Completion	53	
Config Write Type 0	6	
Config Write Type 0 missing Completion	2	
Completion with Data	0	

This tab consists of two panes. The left pane displays a list of transactions. The right pane displays the number of occurrences of these transactions.

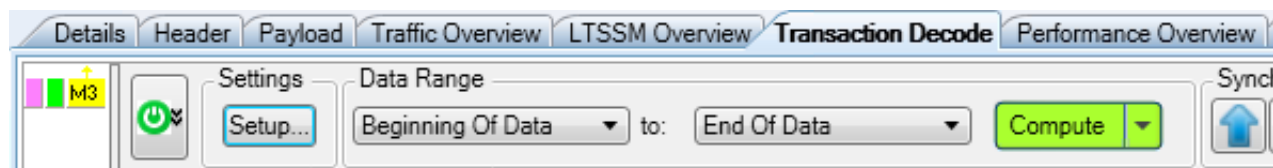
Configuring and Computing Decoded Transactions

Before you Start

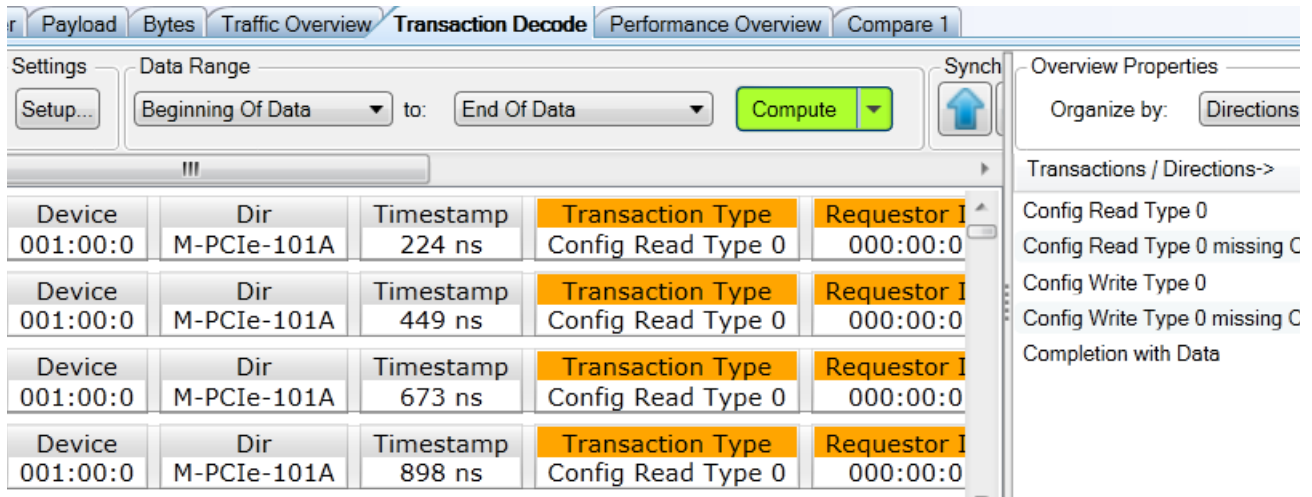
- You should have purchased and installed the U4431 *Transaction Decoder* license for computing decoded transactions.
On purchasing the license, you receive an entitlement certificate. Follow the instructions in this certificate to install the license.
- Ensure that the data in the required direction(s) is already captured and available in the Logic and Protocol Analyzer GUI for transaction decoding. You may save the captured data in a Logic Analyzer configuration (.ala) file and access this data offline for transaction decoding.
- It is recommended that you capture the device's complete boot process data so that the required device setup details are available for decoding transactions accurately.

Computing Transactions from the Captured Data

- 1 Click the **Transaction Decode** tab in the Protocol Viewer window.
- 2 In the **Data Range** groupbox, specify the start and end points of the captured data for which you want to compute decoded transactions. Only the specified range of data is analyzed to compute transactions. Following options are available for setting the data range.
 - **Beginning and End of data** - This data range selection ensures that transactions are computed for the entire trace.
 - **Trigger** - Selecting Trigger in the data range ensures that transactions are computed from the point where the U4431 module's trigger condition was met.
 - **Markers** - Selecting markers in the data range ensures that transactions are computed for the specific portion of MPCle traffic defined by markers. Refer to "[Defining Markers for Setting the Computation Range](#)" on page 96 to know more.



- 3 Click the **Compute** button displayed with the Data Range fields. The **Compute This** and **Compute All** are the two options available with this button.
 - **Compute All** allows you to compute traffic overview statistics (see [page 64](#)), decoded transactions, and offline performance summary (see [page 156](#)) for the captured packets by a single click of this button.
 - **Compute This** allows you to compute only the decoded transactions from the captured packets. When you click **Compute**, then also only the decoded transactions are computed.
 Transactions are computed and displayed for the specified data range.

**NOTE**

You need to recompute the decoded transaction results if you want to change:

- the data range for which transactions are to be displayed.
- the storage protocol for which transactions are to be displayed.
- the device setup such as base address, size, or the number of queues.

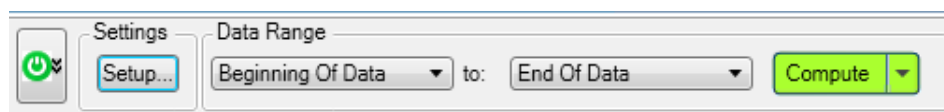
Defining Markers for Setting the Computation Range

If the captured traffic is too large and you want to view decoded transactions from a specific portion of this traffic, then you can limit the computation range by defining start and end markers in the traffic.

To define markers

- 1 From the upper pane of the Protocol Viewer, right-click the row in the captured traffic that should act as the starting point for transaction decode.
- 2 Select **Place Marker** from the displayed context menu and then select an existing marker or click **New Marker** to define a new marker at this point.

Once markers are defined, these are available for selection in the **Data Range** group box of the **Transaction Decode** tab.



Defining / Verifying the Device Setup

While computing decoded transactions, the Logic and Protocol Analyzer software automatically discovers the required device details such as device ID, type, and base address from the captured data. You can view this device related data in the **Transaction Decode Setup** dialog box.

NOTE

At times, the required device details are not available in the captured data and therefore cannot be autodiscovered from the captured data. In such situations, it becomes mandatory for you to specify these details. To avoid such a situation, you can ensure that device details are available by capturing the device's complete boot process.

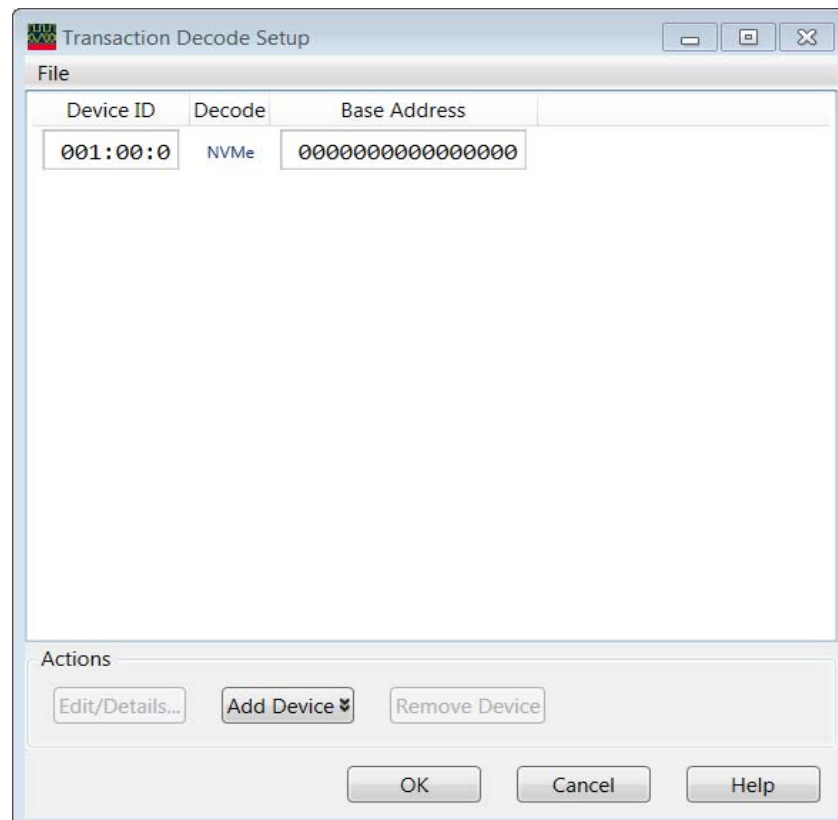
It is recommended that you verify and modify device details (if needed) for accurate transaction decode computation.

You use the **Transaction Decode Setup** dialog box to define/modify/verify the information about the device being tested. The types of information displayed in this dialog box differ based on the storage protocol decode applicable for the device.

To verify/modify device details

- 1 Click **Setup** in the **Transaction Decode** tab of the Protocol Viewer.

The **Transaction Decode Setup** dialog box is displayed with the device details autodiscovered from the captured data after you have computed the decoded transactions. In the following screen, the setup is displayed for PCIe data.



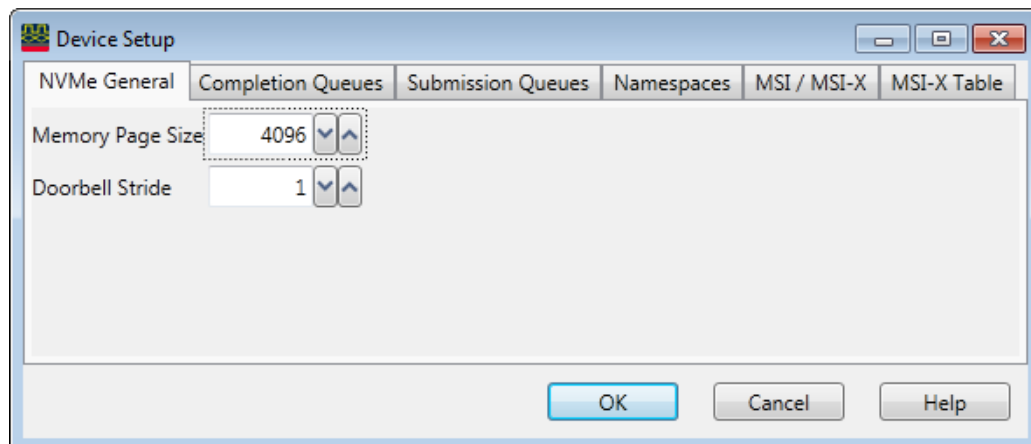
- 2 Modify **Device ID** and **Base Address**, if needed.
- 3 You can also add a new device details by clicking **Add Device** and then selecting the appropriate device type applicable for the DUT.
- 4 To delete the setup details of an existing device, select the row of the device and click **Remove Device**.

- 5 Double-click the device row to view/edit its details. Alternatively, select the device row and click **Edit Details...**

The **Device Setup** dialog box is displayed. The tabs and fields in this dialog box differ based on the storage protocol decode applicable for the device.

To view/edit details of an NVMe device

All device details such as its submission and completion queues and namespaces are autodiscovered while computing decoded transactions. If needed, you can add, remove, or edit these details of an NVMe device in the Device Setup dialog box.

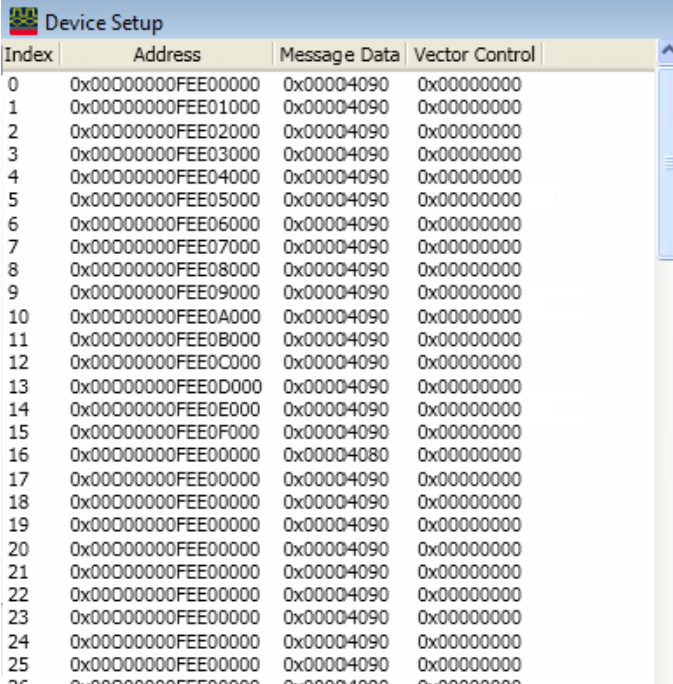


- 1 The **Memory Page Size** field indicates the size of the physical memory page configured by the host software. The Memory Page Size value is a part of the controller configurations that the host can set and modify. This value is used to set the size of PRP entries.
- 2 The **Doorbell Stride** field indicates the number of bytes to be used in memory space to separate doorbell registers. This value is a part of the controller capabilities.
- 3 Click the **Completion Queue** tab. The details of Admin and I/O completion queue(s) autodiscovered by the software from the captured data are displayed. If these are not autodiscovered, you can add a new queue by clicking **Add Queue** and specifying its ID, size, base address, and the unique MSI-X vector that the controller allocated to this queue to respond back.

Queues/Addresses	Queue ID	Size	Address	Int Vector
Admin Queue	0	127		0
Address: 127 0000000078F81000				
I/O Queue 1	1	1023		0
PRP 1: 1023 0000000078FB0000				
I/O Queue 2	2	1023		1
PRP 1: 1023 0000000078FD3000				

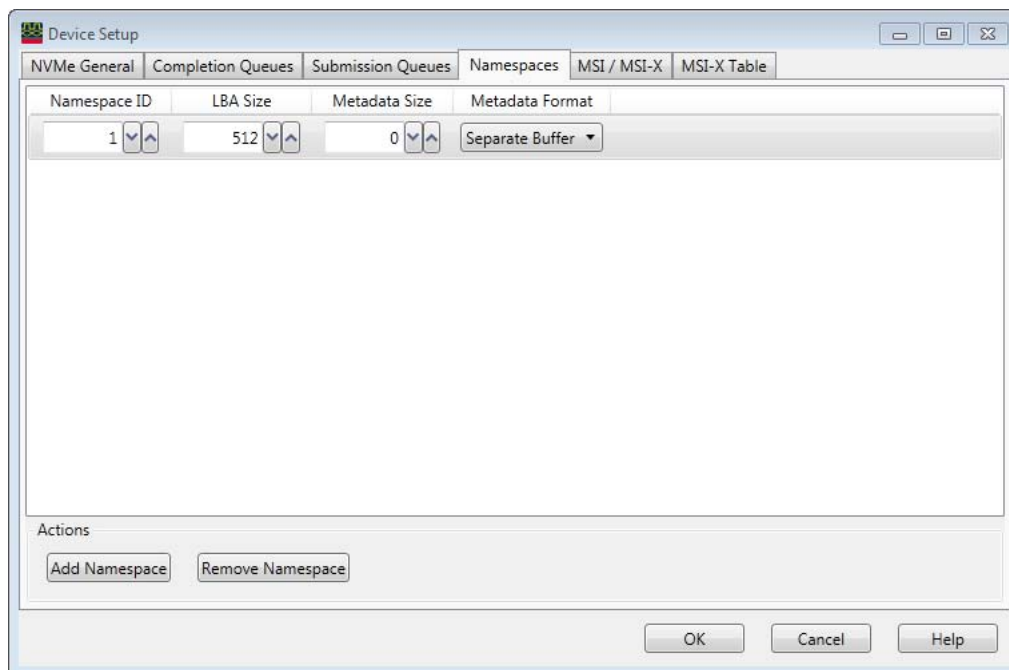
Actions

- 4 Click the **Submission Queue** tab. The details of Admin and I/O submission queue(s) autodiscovered by the software from the captured data are displayed. The completion queue associated with a submission queue is also displayed. A queue ID is used to display this association. If a queue's details are not autodiscovered, you can add a new queue by clicking **Add Queue** and specifying its ID, size, and base PRP address(es).
- 5 If a queue is physically non-contiguous, multiple PRP addresses are displayed for the queue representing multiple memory chunks. For a non-contiguous queue, you can add multiple PRP addresses by selecting a PRP address entry of the queue and clicking **Add Address**.



Index	Address	Message Data	Vector Control
0	0x00000000FEE00000	0x00004090	0x00000000
1	0x00000000FEE01000	0x00004090	0x00000000
2	0x00000000FEE02000	0x00004090	0x00000000
3	0x00000000FEE03000	0x00004090	0x00000000
4	0x00000000FEE04000	0x00004090	0x00000000
5	0x00000000FEE05000	0x00004090	0x00000000
6	0x00000000FEE06000	0x00004090	0x00000000
7	0x00000000FEE07000	0x00004090	0x00000000
8	0x00000000FEE08000	0x00004090	0x00000000
9	0x00000000FEE09000	0x00004090	0x00000000
10	0x00000000FEE0A000	0x00004090	0x00000000
11	0x00000000FEE0B000	0x00004090	0x00000000
12	0x00000000FEE0C000	0x00004090	0x00000000
13	0x00000000FEE0D000	0x00004090	0x00000000
14	0x00000000FEE0E000	0x00004090	0x00000000
15	0x00000000FEE0F000	0x00004090	0x00000000
16	0x00000000FEE00000	0x00004080	0x00000000
17	0x00000000FEE00000	0x00004090	0x00000000
18	0x00000000FEE00000	0x00004090	0x00000000
19	0x00000000FEE00000	0x00004090	0x00000000
20	0x00000000FEE00000	0x00004090	0x00000000
21	0x00000000FEE00000	0x00004090	0x00000000
22	0x00000000FEE00000	0x00004090	0x00000000
23	0x00000000FEE00000	0x00004090	0x00000000
24	0x00000000FEE00000	0x00004090	0x00000000
25	0x00000000FEE00000	0x00004090	0x00000000

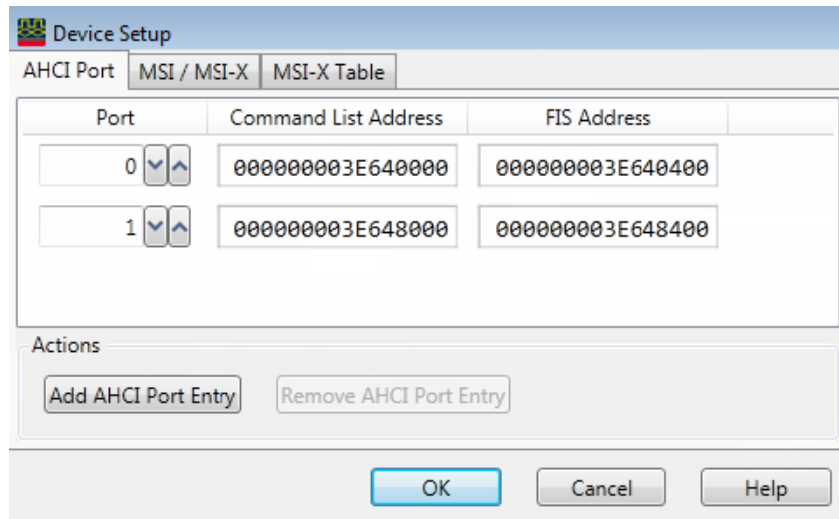
- 6 Click the **Namespaces** tab. The details of the controller's namespace(s) autodiscovered by the software from the captured data are displayed. If these are not autodiscovered, you can add a new namespace by clicking **Add Namespace** and specifying the following details.
- **LBA Size** - The Logical Block Address (LBA) data size (in bytes) that the namespace supports.
 - **Metadata Size** - The number of metadata bytes provided per LBA.
 - **Metadata Transfer Mode** - The metadata may be transferred either as part of the LBA by creating an extended LBA or as a separate contiguous buffer of data. When this field is set to **End of data LBA**, it indicates that the metadata is transferred at the end of the data LBA. When this field is set to **Separate Buffer**, it indicates that all the metadata for a command is transferred as a separate contiguous buffer of data.



- 7 Click the **MSI / MSI-X** tab to view/modify the MSI / MSI-X capabilities that were discovered for the Name device.
- 8 Click the **MSI-X** tab to view/add/modify the MSI-X table entries that were discovered for the Name device.
- 9 Click **OK** to confirm and apply these settings.

To view/edit details of an AHCI device

An AHCI device's details such as the ports that it supports are autodiscovered while computing decoded transactions. If needed, you can add, remove, or edit these details of an AHCI device in the Device Setup dialog box.



- 1 In the **AHCI Port** tab, click the **Add AHCI Port Entry** button if you want to add an AHCI port that the device supports. This tab displays the autodiscovered ports of the AHCI device.
- 2 In the **Port** field, view/modify the port number.
- 3 In the **Command List Address** field, view/modify the base physical address of the Command List of the selected AHCI port. A command list refers to the list of commands that the HBA fetches from the command list base address to execute. The base address of the command list is as per the *PxFB* and *PxFBU* port registers.
- 4 In the **FIS Address** field, view/modify the base address of the FISes (Frame Information Structure) received for the selected AHCI port. An FIS refers to the frame of information exchanged between the host and device. The base address of the FIS is as per the *PxFB* and *PxFBU* port registers.
- 5 Click the **MSI / MSI-X** tab to view/modify the MSI / MSI-X capabilities that were discovered for the AHCI device.
- 6 Click the **MSI-X** tab to view/add/modify the MSI-X table entries that were discovered for the AHCI device.
- 7 Click **OK** to confirm and apply these settings.

To view/add details of an SSIC device

When computing decoded SSIC transactions, you:

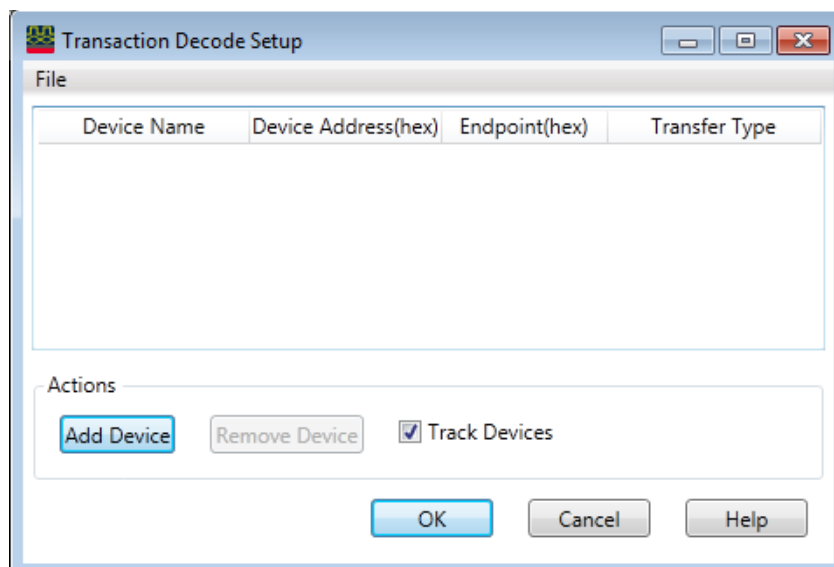
- either manually add details for the SSIC device(s) associated with the captured data.
- or configure the software to automatically discover the SSIC device details from the captured data.

In both these cases, you use the **Transaction Decode Setup** dialog box to add/autodiscover SSIC device details. The SSIC transactions are decoded as per the added / autodiscovered SSIC device details.

If the required device details are not available in the captured data, then the software will not be able to autodiscover the device details from the captured data. In such a situation, it becomes mandatory for you to specify these details for the accurate transaction decode.

- 1 Click **Setup** in the **Transaction Decode** tab of the Protocol Viewer.

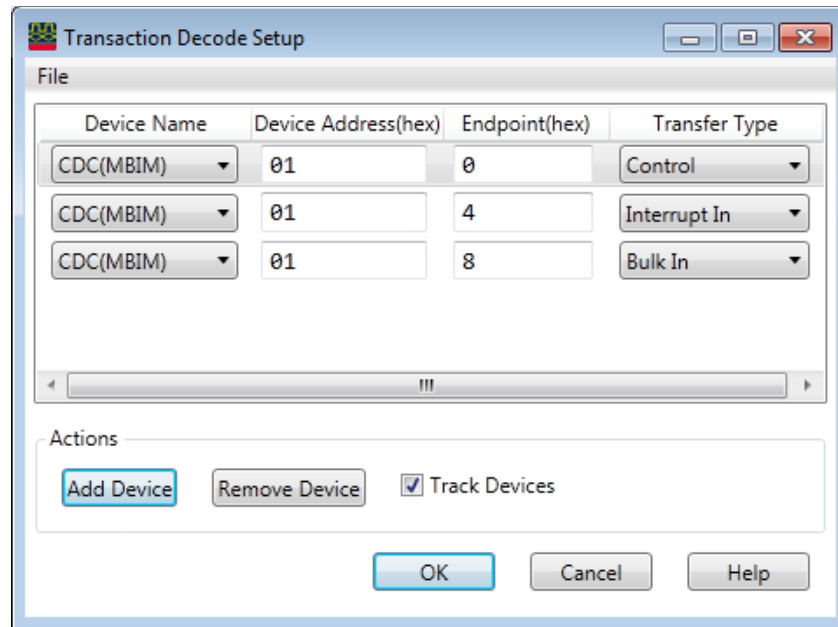
The **Transaction Decode Setup** dialog box is displayed. In the following screen, the setup for SSIC devices is displayed before the transactions are computed.



- 2 To manually add a device:
 - a Click **Add Device**.
 - b Select a **Device Name**. For SSIC devices, the software supports **CDC** device class and **ACM**, **NCM**, and **MBIM** subclasses.
 - c Specify the address and endpoint used by the device.
 - d From the **Transfer Type** listbox, select the transfer/endpoint type based on the endpoint usage. The software supports the following transfer/endpoint types:
 - **Control** – For initial configuration of the device and command and status operations
 - **Interrupt In or Interrupt Out** – For event notifications
 - **Isochronous** – For raw or formatted data communication with guarantee of bandwidth or minimum latency
 - **Bulk In and Bulk Out** – For large bursty data with error-free delivery, but with no guarantee of bandwidth.
 - e Click **OK** and then compute the transactions. The added device details will be used for such a compute operation.

Or
- 3 To configure the software to autodiscover SSIC device details from the captured data:
 - a Select the **Track Devices** checkbox in the Transaction Decode Setup dialog box.
 - b Click **OK** to close this dialog box.
 - c Click **Compute** in the **Transaction Decode** tab. When you click Compute, the software tracks the SSIC devices from the trace, populates these device details in the Transaction Decode Setup and computes the decoded transactions using these details.
 - d Open the **Transaction Decode Setup** dialog box again to view the autodiscovered device details after the computer operation.

The following screen displays three SSIC endpoints autodiscovered from the captured data.



To delete the setup details of an existing device, select the row of the device and click **Remove Device**.

See Also

[“Viewing SSIC Transactions”](#) on page 134

Saving the Device Setup Details

You can save the device, its queues, and namespace details (specified using the Transaction Decode Setup dialog box) in a Transaction decode (.tdprop) file. Once saved, you can open this file later in the Transaction Decode Setup dialog box to quickly access and set up the device details for a transaction decode computation.

To save device details

- 1 Click **Setup** in the **Transaction Decode** tab of the Protocol Viewer.
The **Transaction Decode Setup** dialog box is displayed. Modify the device details as needed.
- 2 Click **File > Save As...**
- 3 Specify a name for the Transaction Decode setup file and click **Save**.

To open a previously saved setup file for transaction computation

- 1 Click **Setup** in the **Transaction Decode** tab.
The **Transaction Decode Setup** dialog box is displayed.
- 2 Click **File > Open**.
- 3 In the **Open** dialog box, select the Transaction decode (.tdprop) file that contains the device setup details.
- 4 Click **Open**.

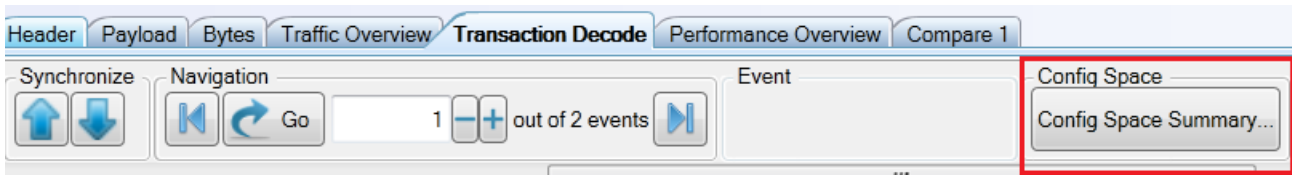
You can edit a saved .tdprop file by opening it in an XML Editor or the Transaction Decode Setup dialog box.

Viewing the Configuration Space of a DUT

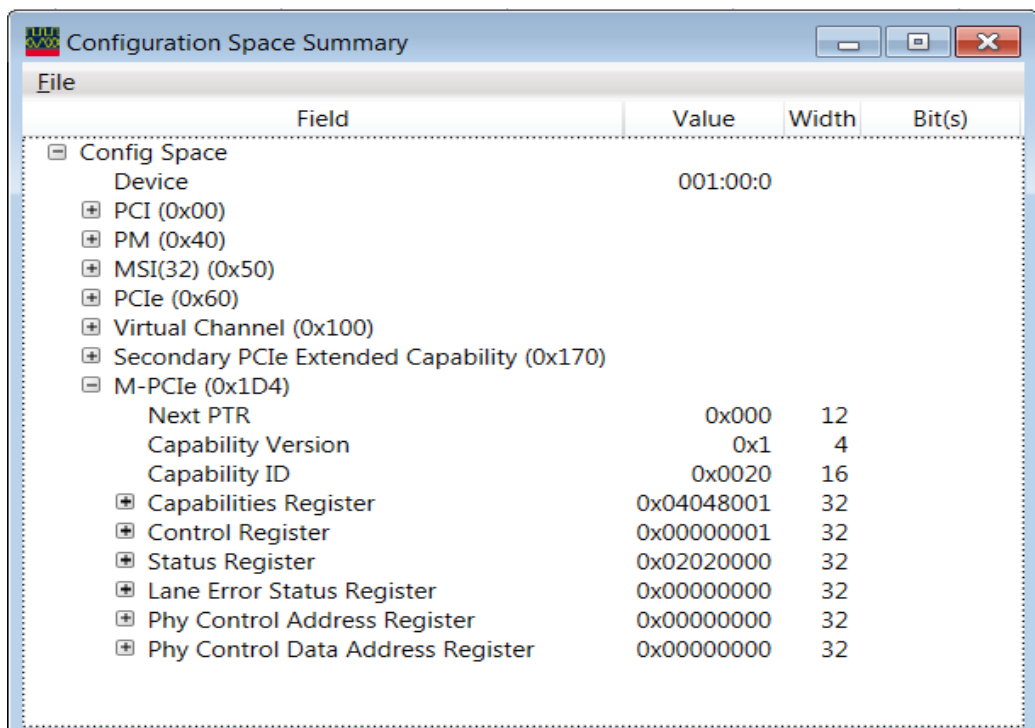
While computing MPCle decoded transactions, the Logic and Protocol Analyzer software automatically discovers and reads details from various MPCle configuration registers in the DUT's configuration space. It displays register values for DUT's capabilities such as PCI, PCIe, MPCle, Power Management, MSI, Virtual Channel, and Secondary PCIe Extended capabilities.

To view the configuration space of a DUT

- 1 Compute decoded transactions in the **Transaction Decode** tab.
- 2 Click the **Config Space Summary** button displayed in the top panel of the Transaction Decode tab.



The configuration space summary of the DUT is displayed.



Viewing the Attribute Summary of a DUT

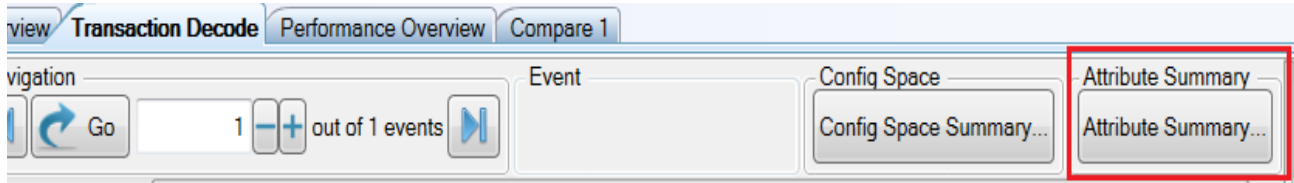
While computing decoded transactions, if the Logic and Protocol Analyzer software detects that the captured data includes PWM G1 data when RRAP packets are transmitted, it displays a button named Attribute Summary button in the Transaction Decode tab. You can use this button to display details related the attributes discovered from the decode of RRAP packets found in the captured data.

NOTE

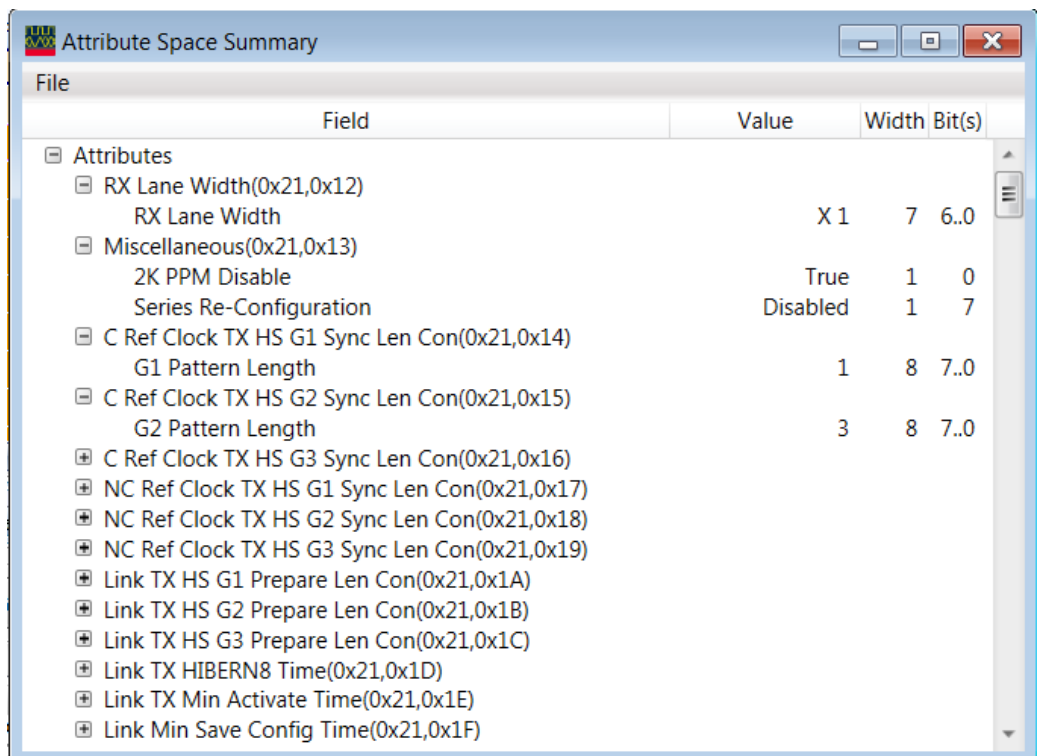
This button is not displayed if the captured data does not include RRAP packets.

To view the attribute space summary of a DUT

- 1 Compute decoded transactions in the **Transaction Decode** tab.
- 2 Click the **Attribute Summary** button displayed in the top panel of the Transaction Decode tab.



The attribute space summary of the DUT is displayed.



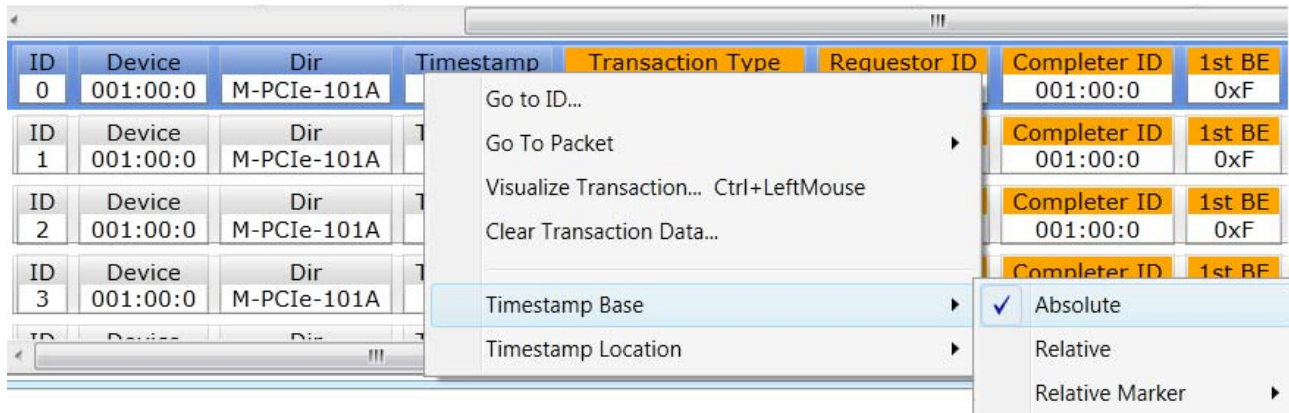
Configuring Timestamps Displayed in the Computed Transactions

Each computed transaction has a timestamp displayed for it. You can configure:

- where you want the timestamp field to be located for the computed transactions.
- the base to be used for the calculation of timestamps of transactions.

To configure timestamp settings

- 1 Right-click the **Timestamp** field displayed for a computed transaction.
- 2 Select **Timestamp Base** from the context-menu.



- 3 From the displayed submenu, you can select:
 - **Absolute** - Timestamp for a transaction is calculated on the basis of the absolute time of the first PCIe packet applicable for that transaction. This is the default selection.
 - **Relative** - Timestamp for a transaction is calculated relative to the time of the first packet applicable for the previous transaction in the list.
 - **Relative Marker** - Timestamps are calculated relative to the marker that you select from the displayed submenu.

Changing the timestamp base recalculates the displayed timestamps based on the selected base.
- 4 Right-click the **Timestamp** field displayed for a computed transaction and then select **Timestamp Location** from the context-menu.
- 5 By default, timestamps are displayed in the front of transactions data. If required, you can move the display of timestamps to the back of transactions data.

Saving the Computed Transaction Data

Once you computed the transaction data, you can save it in a logic analyzer .ala configuration file. The transaction data is saved along with the captured PCIe traffic, device setup details, and any other configurations that you made in the Protocol Viewer window.

NOTE

You do not need the Transaction Decoder software license to view the saved transaction data. However, if you want to recompute transactions from the saved trace, then you need the Transaction Decoder software license.

To save the computed transaction data

- 1 Click **File > Save as**.
- 2 In the **Save As** dialog box, specify the name of the file.
- 3 Ensure that the **Standard Configuration (*.ala)** option is selected as the file type and **All Data and Setup** is selected in the **File Options** group box.
- 4 Click **Save**.

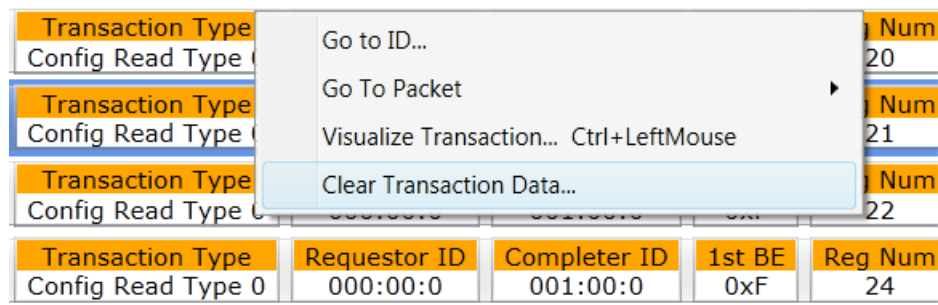
To access and view previously saved transaction data

- 1 Click **File** > **Open**.
- 2 In the **Open** dialog box, navigate to the **Standard Configuration (*.ala)** file in which you saved the transaction data.
- 3 Click **Open**.

Clearing the Computed Transaction Data

You can clear the computed transaction data displayed in the Transaction Decode tab.

- 1 Right-click a transaction displayed in the Transaction Decode tab.
- 2 Click **Clear Transaction Data...** from the right-click context menu.



- 3 Click **Yes** to confirm.

The computed transactions display is cleared.

If the transactions data was previously saved in an .ala file, then reloading the .ala file redisplay the transactions data after it has been cleared. If the transactions data was not previously saved, you can specify the data range and then click **Compute** again to redisplay the transactions data.

Interpreting and Navigating Through the Transaction Decode Results

Transactions are decoded and computed from a set of captured packets that contain the storage protocol information associated with that transaction.

Decoded transactions are displayed in the *Transaction Decode* tab in an order based on the timestamp of the first packet associated with a transaction. One or many packets may be associated with a transaction.

Transaction Details Pane

In the left pane of the Transaction Decode tab, transactions with their details are displayed. In this pane, a transaction is uniquely identified by a Transaction ID.

The details displayed for transactions may vary based on their type and the applicable storage protocol. For instance, moving the mouse pointer to a field of a decoded MPCle transaction presents a tool tip with information about that field.

ID	Device	Dir	Timestamp	Transaction Type	Requestor ID	Completer ID	1st BE	Reg Num	Reg Offset	Cap Type	Device Capabilities	Payload
7636	001:00:0	M-PCIE-101A	2.238387 ms	Config Read Type 0	000:00:0	001:00:0	0xF	25	0x0064	PCIE	0x00000040	0x00000040
7637	001:00:0	M-PCIE-101A	2.238611 ms	Config Read Type 0	000:00:0	001:00:0	0xF	24	0x0060	PCIE	0x00000040	0x00000040
7638	001:00:0	M-PCIE-101A	2.238835 ms	Config Read Type 0	000:00:0	001:00:0	0xF	25	0x0064	PCIE	0x00000040	0x00000040
7639	001:00:0	M-PCIE-101A	2.239060 ms	Config Read Type 0	000:00:0	001:00:0	0xF	26	0x0068	PCIE	0x00000040	0x00000040
7640	001:00:0	M-PCIE-101A	2.239284 ms	Config Read Type 0	000:00:0	001:00:0	0xF	27	0x006C	PCIE	0x00000040	0x00000040
7641	001:00:0	M-PCIE-101A	2.239508 ms	Config Read Type 0	000:00:0	001:00:0	0xF	28	0x0070	PCIE	0x00000040	0x00000040

Different colors are used to clearly indicate transactions of different types. Errored transactions are displayed in red.

Dir	Timestamp	Transaction Type	Requestor ID	Completer ID
M-PCIE-101A	494.486 us	Config Read Type 0 missing Completion	000:00:0	001:00:0
M-PCIE-101B	494.494 us	Completion with Data	0x00010020	
M-PCIE-101A	494.711 us	Config Read Type 0 missing Completion	000:00:0	001:00:0
M-PCIE-101A	494.935 us	Config Read Type 0 missing Completion	000:00:0	001:00:0
M-PCIE-101A	495.160 us	Config Read Type 0 missing Completion	000:00:0	001:00:0

Unsuccessful Completion Response

NOTE

If you are displaying data captured from multiple U4431A modules in a single Protocol Viewer instance, then transactions from these multiple modules will be interleaved based on the timestamp of the first packet in the transaction.

To display transactions for each module separately, you can add a Protocol Viewer instance to each module in the Overview pane of the Logic and Protocol Analyzer GUI.

Transaction Overview Pane

The right pane of the Transaction Decode tab displays statistics for the computed transactions. This pane lists the transaction types applicable for the computed transaction data and the number of events/occurrences for each transaction type in the computed data.

The Overview pane for the SSIC transactions is described in the ["Viewing SSIC Transactions"](#) on page 134.

Overview Properties			
Organize by: Directions			
Transactions / Directions->	M-PCle-101A	M-PCle-101B	Total
Config Write Type 0 missing Completion	2	0	2
Config Write Type 0	6	0	6
Config Read Type 0 missing Completion	53	0	53
Config Read Type 0	457	0	457
Completion with Data	0	29	29

In this pane, you can organize the number of transaction occurrences/events on the basis of the organization types described in the following table.

Organized by	Description
Direction	Transaction occurrences are organized on the basis of the Uplink and Downlink directions in the captured data. If the captured data is unidirectional, then this view displays occurrences for one direction only.
Configuration Space	Selecting this option displays the number of occurrences of only MPCle config space transactions organized on the basis of reads and writes to MPCle configuration registers in the DUT's configuration space.

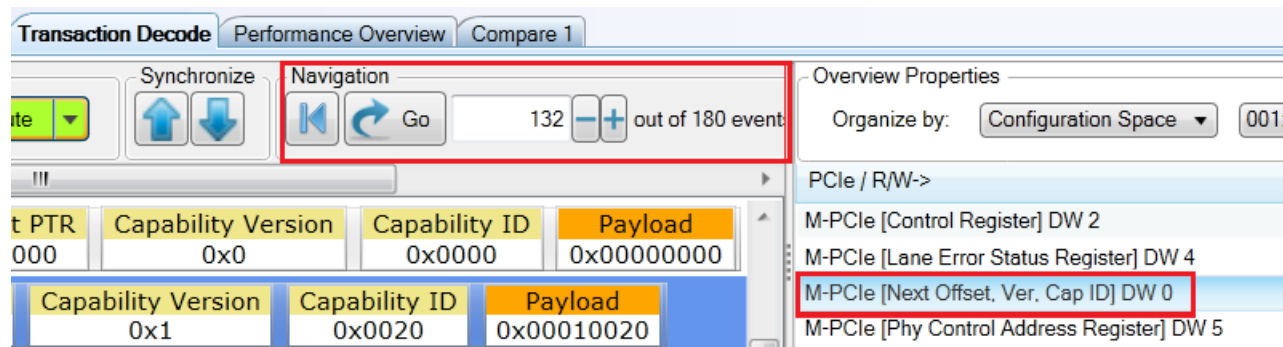
Overview Properties			
Organize by: Configuration Space 001:00:0 Device ID			
PCle / R/W->	Read	Write	Total
M-PCle [Capabilities Register] DW 1	180	0	180
M-PCle [Control Register] DW 2	180	0	180
M-PCle [Lane Error Status Register] DW 4	180	0	180
M-PCle [Next Offset, Ver, Cap ID] DW 0	180	0	180
M-PCle [Phy Control Address Register] DW 5	180	0	180
M-PCle [Phy Control Data Register] DW 6	180	0	180
M-PCle [Status Register] DW 3	180	0	180
MSI(32) [Message Addr (31:0)] DW 1	360	0	360
MSI(32) [Message Control, Next Cap, Cap ID] DW 0	539	0	539
MSI(32) [Message Data] DW 2	360	0	360




Navigating Through Transactions

You can easily navigate through the occurrences of a particular type of transaction in the computed transaction data.

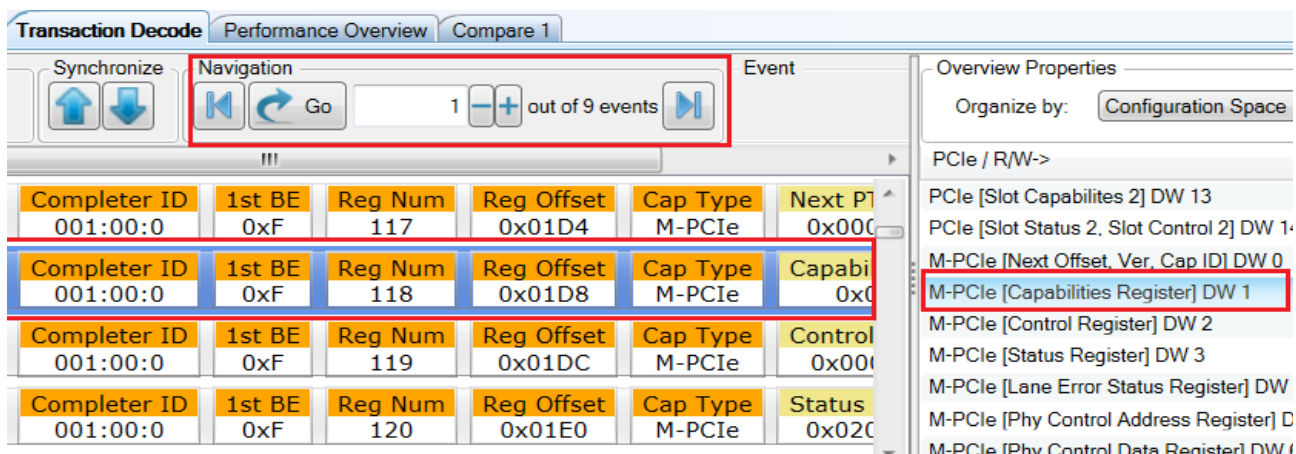
- 1 From the Transaction Overview pane on the lower-right, select a transaction type whose occurrences you want to navigate and view.

The navigation bar in the Transaction Decode tab now displays the total number of occurrences found for the selected transaction type.




- 2 To view the first occurrence of that transaction type in the computed data, click  button in the navigation bar. Click  to go to the last occurrence of the selected transaction type.
- 3 To sequentially move through the occurrences of a transaction type, use the  buttons in the navigation bar.
- 4 To go to a specific occurrence of a transaction type, specify the event/occurrence number in the text box displayed in the navigation bar and click **Go**.

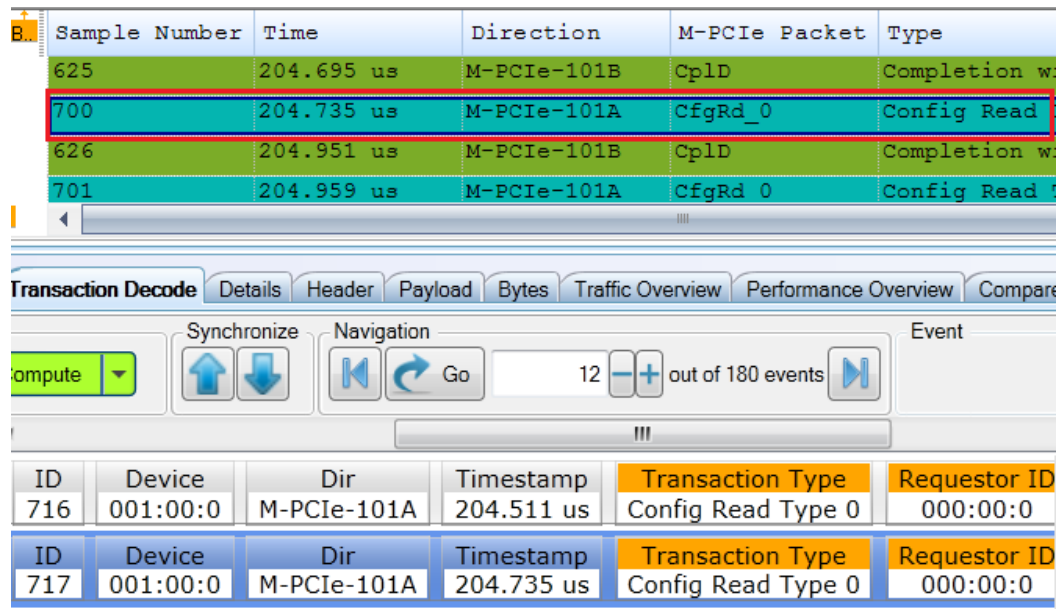
You can also double-click a particular occurrence number in the right pane to navigate to the first transaction mapped to that occurrence number. For instance, in the following screen, the first occurrence of a Config Read transaction to the *MPCIe Capabilities Register* is highlighted on double-clicking its occurrence number in the right pane.



Navigating Between Transactions and their Associated Packets

From a transaction listed in the Transaction Decode tab, you can quickly navigate to the packet(s) exchanged for that particular transaction. To accomplish this, you need to double-click a transaction in the Transaction Decode tab. Doing so, highlights the packet exchanged as the first packet for that transaction in the upper pane of the Protocol Viewer. You can also use the **Synchronize**  button in the Transaction Decode tab to synchronize the display of packets data with the selected transaction.


In the following screen, double-clicking the transaction for Config Read to the Capabilities Register highlights the memory read packet for this transaction.



Sample Number	Time	Direction	M-PCIe Packet	Type
625	204.695 us	M-PCIe-101B	CplD	Completion w
700	204.735 us	M-PCIe-101A	CfgRd_0	Config Read
626	204.951 us	M-PCIe-101B	CplD	Completion w
701	204.959 us	M-PCIe-101A	CfgRd_0	Config Read

ID	Device	Dir	Timestamp	Transaction Type	Requestor ID
716	001:00:0	M-PCIe-101A	204.511 us	Config Read Type 0	000:00:0
717	001:00:0	M-PCIe-101A	204.735 us	Config Read Type 0	000:00:0

NOTE

You can also quickly navigate from a packet displayed in the upper pane to its applicable transaction in the Transaction Decode tab. To accomplish this, you select the packet and then click the **Synchronize**  button displayed in the Transaction Decode tab. This synchronizes the display of transactions with the selected packet.

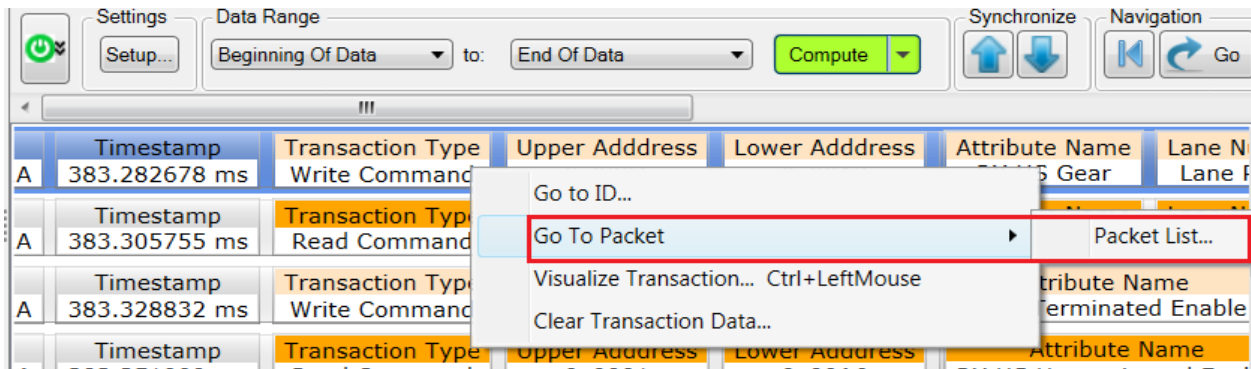
Navigating to a specific packet of a transaction

A transaction may have multiple packets associated with it.

For SSIC transactions, the associated packets are listed nested within the applicable transaction. For MPCle, you can directly navigate to any packet of a transaction. To do this,

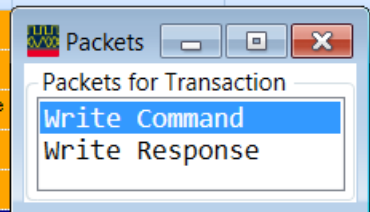
- 1 Right-click a transaction.
- 2 Select **Go To Packet > Packet List**.

A list of packets applicable for that transaction are displayed in a sequential order.



- 3 Select a packet from this list. The packet that you selected gets highlighted in the upper pane of the Protocol Viewer.

Sample Number	Time	Direction	M-PCIe Packet	Type	Sequence
16489	383.221356 ms	M-PCIe-101B	Read Response		
16799	383.236523 ms	M-PCIe-101A	Write Command		
16490	383.244433 ms	M-PCIe-101B	Write Response		
16800	383.259600 ms	M-PCIe-101A	Read Command		
16491	383.267510 ms	M-PCIe-101B	Read Response		
16801	383.282678 ms	M-PCIe-101A	Write Command		
16492	383.290587 ms	M-PCIe-101B	Write Response		
16802	383.305755 ms	M-PCIe-101A	Read Command		
16493	383.313664 ms	M-PCIe-101B	Read Response		
16803	383.328832 ms	M-PCIe-101A	Write Command		
16494	383.336741 ms	M-PCIe-101B	Write Response		



Visualizing a Transaction Set (Super Transaction)

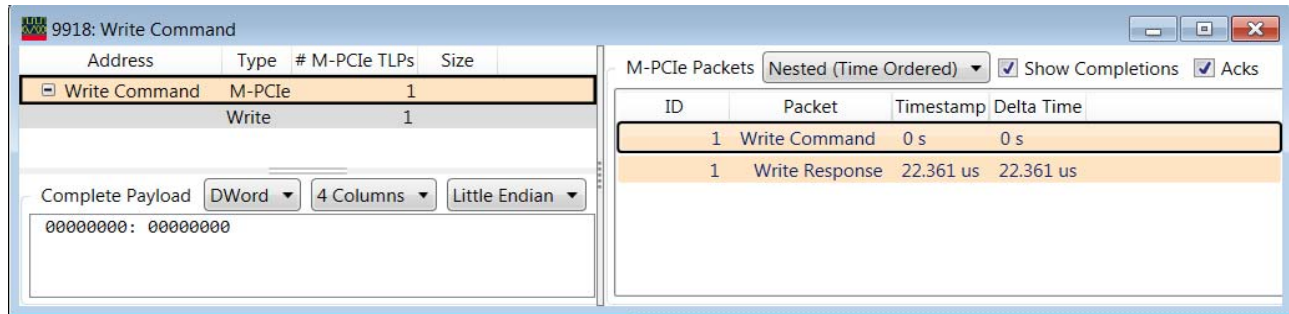
This topic is NOT applicable to the SSIC transactions decode. To get information relevant to SSIC transactions, refer to ["Viewing SSIC Transactions"](#) on page 134.

At times, you may want to visualize a decoded transaction as a super transaction with all its related transactions forming a complete set. This provides a visual sequential flow of transactions that were related to a particular transaction.

To visualize a transaction set

- 1 Right-click a decoded transaction.
- 2 Select **Visualize Transaction...** from the context menu.

For instance, in the following screen, the *M-PCIe* transaction has been visualized as a complete set of transactions.



In the above screen, the transaction set for the *M-PCIe* Write transaction is displayed: It begins with the Write command followed by a response to this Write command.

For each of these related transactions in a set, you can view:

- the associated raw payload in the lower pane.
- the applicable *M-PCIe* packet(s) in the right pane.

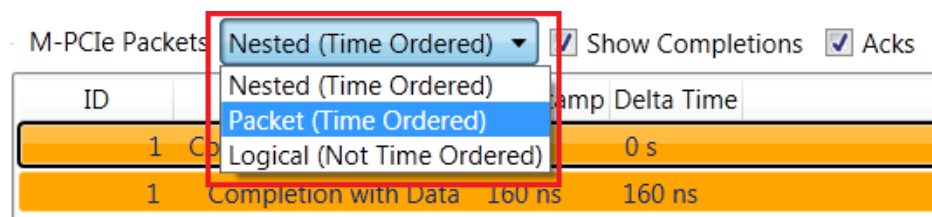
NOTE

Clicking a super transaction displays the combined payload for the complete set of transactions.

M-PCIe Packets applicable for a Transaction Set

In the right pane of the Visualize Transaction dialog box, by default, *M-PCIe* packets are listed as per the *Nested (Time Ordered)* option. This means packets are listed in a time based sequence with the completion packets nested within the applicable Read packets. If required, you can change this display to:

- list the packets in a logical sequence instead of time based sequence – *Logical (Not Time Ordered)* option.
- list the packets without nesting the completions within the read packets – *Packets (Time Ordered)* option.



For each of the listed packets, the following time related fields are displayed:

- **Timestamp** – This field is relative to the timestamp of the first packet displayed for the transaction set. (The first packet timestamp is taken as 0s.).
- **Delta Time** – This field displays the difference between the timestamp of the currently selected packet and the previous packet in the list of packets for the transaction set.

M-PCIe Packets Nested (Time Ordered) ▾ ☒ Show Completions ☒ Acks

ID	Packet	Timestamp	Delta Time
1	Config Read Type 0	0 s	0 s
1	Completion with Data	160 ns	160 ns

You can enable or disable the display of Completions and Acks packets from the packets list in the right pane using the **Show Completions** and **Acks** checkboxes respectively.

Viewing NVMe Transactions

Previous help topics described how you can configure and compute transactions in the Transaction Decode tab. This topic is specific to NVMe transactions and provides examples of how you can interpret NVMe transactions that are computed and displayed in the Transaction Decode tab.

The decoded NVMe transactions provide you a sequential view of the communication cycle between the host software and NVMe controller for various requests placed by the host software in queues. You can check how the NVMe controller responds to and completes these requests. At the administrative management level, you can verify how the NVMe controller handles admin requests such as queue management requests.

For NVMe, the Transaction Decode tab displays transactions for:

- NVMe controller initialization such as Admin queue configuration
- NVMe Admin and I/O commands submission and their responses (completions).
- MSI-X interrupts initiations by controller
- NVMe I/O submission and completion queues management

Viewing Transactions for NVMe Controller Initialization

The following screen displays a set of transactions for NVMe controller initialization. The AQA, ASQ, and ACQ registers are modified to define the Admin Submission Queue and its corresponding Admin Completion Queue. The transactions display the Admin queue attributes in terms of size (128 entries) and the base address (64-bit physical address) to be used for Admin submission queue and completion queue.

ID	Device	Transaction Type	Requestor ID	Completer ID	ACQS	ASQS	Timestamp
589	003:00:0	NVMe AQA (W)	000:00:0	000:00:0	127	127	64.189070798 s
590	003:00:0	NVMe ASQ (W)	000:00:0	000:00:0		ASQB 0x000000023ED40000	64.189070878 s
591	003:00:0	NVMe ACQ (W)	000:00:0	000:00:0		ACQB 0x000000023ED42000	64.189071038 s

Base address of completion queue

Base address of submission queue

Queue size

Viewing Admin Command Transactions

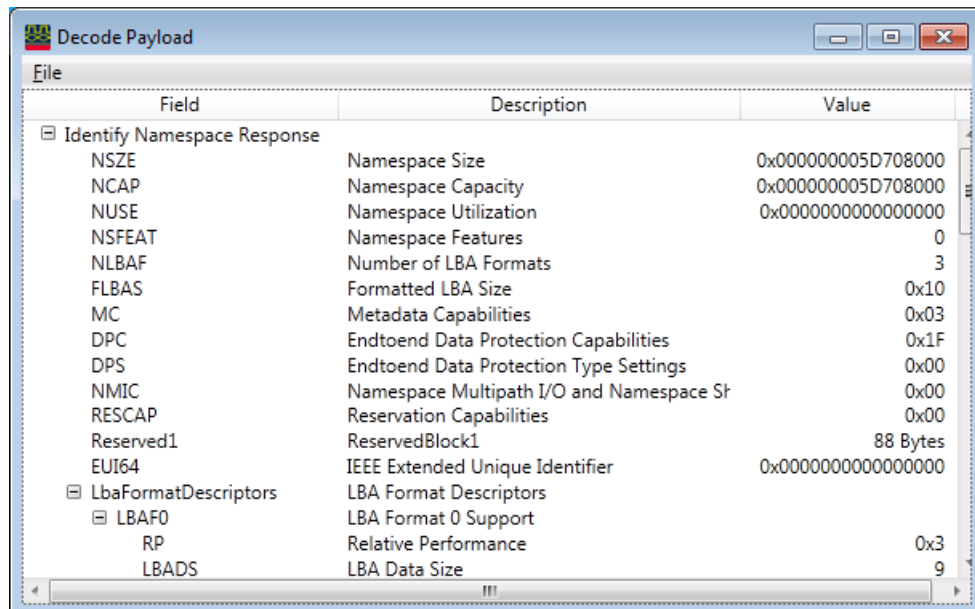
The following screen displays transactions related to the *Identify* command submission and completion to return capabilities and status of a specific namespace.

ID	Device	Transaction Type		Requestor ID	Completer ID	SQT	Timestamp			
112	003:00:0	NVMe Admin Sub Queue Tail DB (W)		000:00:0	000:00:0	2	64.225827568 s			
ID	Device	Transaction Type	Requestor ID	Completer ID	CID	PSDT	FUSE	OPC	NSID	MPTR
113	003:00:0	NVMe Identify	003:00:0	000:00:0	1	0	Normal	Identify	1	0x000000000
ID	Device	Transaction Type		Requestor ID	Completer ID	Cmd Spc	SQID	SQHD	DNR	M
114	003:00:0	NVMe Admin Comp Queue Entry		003:00:0	000:00:0	0	0	2	0	0

Host writes to NVMe Admin submission queue to request namespace capabilities

Controller returns the namespace data structure in multiple memory writes to PRP entries

You can further view the namespace capabilities returned by the Identify command by right-clicking the NVMe Identify transaction and selecting **Decode Payload**.



Field	Description	Value
<input checked="" type="checkbox"/> Identify Namespace Response		
NSZE	Namespace Size	0x000000005D708000
NCAP	Namespace Capacity	0x000000005D708000
NUSE	Namespace Utilization	0x0000000000000000
NSFEAT	Namespace Features	0
NLBAF	Number of LBA Formats	3
FLBAS	Formatted LBA Size	0x10
MC	Metadata Capabilities	0x03
DPC	Endtoend Data Protection Capabilities	0x1F
DPS	Endtoend Data Protection Type Settings	0x00
NMIC	Namespace Multipath I/O and Namespace S	0x00
RESCAP	Reservation Capabilities	0x00
Reserved1	ReservedBlock1	88 Bytes
EUI64	IEEE Extended Unique Identifier	0x0000000000000000
<input checked="" type="checkbox"/> LbaFormatDescriptors	LBA Format Descriptors	
<input checked="" type="checkbox"/> LBAF0	LBA Format 0 Support	
RP	Relative Performance	0x3
LBADS	LBA Data Size	9

You can also visualize how the data returned by the Identify command is stored in specific PRP entries by right-clicking the transaction and selecting **Visualize Transaction...** (see [page 121](#) for details).

607: NVMe Identify

Address	Type	#M-PCIeTLPs	Size
NVMe Identify	NVMe	1	4096
0x00000000D0C01000	SQDB	1	
0x0000000023ED40040	SQ	1	
0x0000000023ED4F000	PRP 1	32	4096
0x0000000023ED42010	CQ	1	
0x00000000FEE00000	MSI-X	1	
0x00000000D0C01004	CQDB	1	

M-PCIe Packets: **Nested (Time Ordered)** ☒ Show Completions ☒ Acks

ID	Packet	Timestamp	Delta Time
PRP 1.1	Memory Write 64b	19.286 us	18.802 us
PRP 1.2	Memory Write 64b	19.362 us	76 ns
PRP 1.3	Memory Write 64b	19.438 us	76 ns
PRP 1.4	Memory Write 64b	19.514 us	76 ns
PRP 1.5	Memory Write 64b	19.590 us	76 ns
PRP 1.6	Memory Write 64b	19.666 us	76 ns
PRP 1.7	Memory Write 64b	19.742 us	76 ns
PRP 1.8	Memory Write 64b	19.818 us	76 ns
PRP 1.9	Memory Write 64b	19.894 us	76 ns
PRP 1.10	Memory Write 64b	19.970 us	76 ns
PRP 1.11	Memory Write 64b	20.046 us	76 ns
PRP 1.12	Memory Write 64b	20.122 us	76 ns
PRP 1.13	Memory Write 64b	20.198 us	76 ns
PRP 1.14	Memory Write 64b	20.274 us	76 ns
PRP 1.15	Memory Write 64b	20.350 us	76 ns
PRP 1.16	Memory Write 64b	20.426 us	76 ns

PRP Payload: **DWord** **4 Columns** **Little Endian**

```

00000000: 5D708000 00000000 5D708000 00000000
00000010: 00000000 00000000 03100300 0000001F
00000020: 00000000 00000000 00000000 00000000
00000030: 00000000 00000000 00000000 00000000
00000040: 00000000 00000000 00000000 00000000
00000050: 00000000 00000000 00000000 00000000
00000060: 00000000 00000000 00000000 00000000
00000070: 00000000 00000000 00000000 00000000
00000080: 03090000 03090008 000C0000 000C0008
00000090: 00000000 00000000 00000000 00000000
000000A0: 00000000 00000000 00000000 00000000
  
```

Viewing NVMe I/O Command Transactions

The following screen displays an *NVMe Read* transaction to read data from the starting LBA specified in the Read command.

The screenshot displays the 'Transaction Decode' tab of the Keysight U4431A MIPI M-PHY Protocol Analyzer. The top section shows a list of transactions with columns: Sample Num, Time, PCI-Express Pack, Direction, and Address, Register Number. A red box highlights a sequence of transactions starting from Sample Num 789 (MemRd_32, PCIe-102:Up, Address=78FA 0000) through Sample Num 793 (MemWr_32, PCIe-102:Up, Address=78F8 46E0). Below this, the 'Transaction Decode' panel shows a detailed view of a transaction. The 'Data Range' section includes 'Begin Of Paired Data' and 'End Of Paired Data' buttons. The 'Navigation' section has 'Go To' and 'Visualize PRP...' buttons. The 'Transaction Decode' table shows columns: Device, Transaction Type, Requestor ID, Completer ID, CID, PSDT, FUSE, and OPC. A red box highlights the 'Memory Read 32b (Gen1,2)' transaction, which is the first transaction in the sequence. The 'Transaction Decode' table also shows the 'Memory Write 32b (Gen1,2)' transaction, which is the last transaction in the sequence.

Sample Num	Time	PCI-Express Pack	Direction	Address, Register Number
789	12.862613273 s	MemRd_32	PCIe-102:Up	Address=78FA 0000
1247	12.862613426 s	CplID	PCIe-102:Down	
790	12.862675357 s	MemWr_32	PCIe-102:Up	Address=78F8 4560
791	12.862675393 s	MemWr_32	PCIe-102:Up	Address=78F8 45E0
792	12.862675431 s	MemWr_32	PCIe-102:Up	Address=78F8 4660
793	12.862675467 s	MemWr_32	PCIe-102:Up	Address=78F8 46E0

Device	Transaction Type	Requestor ID	Completer ID	SQT	Time
001:00:0 Q:1	NVMe I/O Sub Queue Tail DB (W)	000:00:0	000:00:0	1	12.862
001:00:0 Q:1	NVMe Read	001:00:0	000:00:0		
001:00:0 Q:1	NVMe I/O Comp Queue Entry	001:00:0	000:00:0		
001:00:0	MSI-X Interrupt	001:00:0	000:00:0		
001:00:0 Q:1	NVMe I/O Comp Queue Tail DB (W)	000:00:0	000:00:0	1	12.8

For this transaction, the controller:

- first performs a *Memory Read* to read the request entry from the base address of submission queue 1.
- then fetches the requested data from system memory
- then performs multiple *Memory Writes* to write this data to the applicable PRP entries for data transfer to host.

In the transaction following the NVMe Read transaction, the controller updates the completion queue 1 with the status of the Read command completion.

Viewing a Complete Set of Transactions for a Command Submission and Completion

The following screen displays a complete set of NVMe transactions between the host and controller. This set of five transactions represents the steps involved in the *NVMe Write* command submission and completion process. 2

ID	Device	Transaction Type		Requestor ID	Completer ID	SQT	Timestamp		
999	001:00:0 Q:3	NVMe I/O Sub Queue Tail DB (W)		000:00:0	000:00:0	12	14.912247991 s		
ID	Device	Transaction Type	Requestor ID	Completer ID	CID	PSDT	FUSE	OPC	NSID
1000	001:00:0 Q:3	NVMe Write	001:00:0	000:00:0	12	0	Normal	Write	1
ID	Device	Transaction Type	Requestor ID	Completer ID	Cmd Spc		SQID	SQHD	
1001	001:00:0 Q:3	NVMe I/O Comp Queue Entry	001:00:0	000:00:0	0		3	12	
ID	Device	Transaction Type	Requestor ID	Completer ID	1st BE	Address	Payload		
1002	001:00:0 Q:3	MSI-X Interrupt	001:00:0	000:00:0	0xF	0xFEE04000	0x000040A0		
ID	Device	Transaction Type		Requestor ID	Completer ID	CQH	Timestamp		
1003	001:00:0 Q:3	NVMe I/O Comp Queue Tail DB (W)		000:00:0	000:00:0	12	14.912765426 s		

The following list describes the set of five transactions displayed above.

- 1 The first transaction indicates to the controller that a command is submitted for processing.
- 2 The second transaction represents the NVMe Write command that the host created in the submission queue. The controller reads this command from the submission queue for execution and executes the command.
- 3 The third transaction represents the command completion entry that the controller writes to the completion queue.
- 4 The fourth transaction represents the MSI-X interrupt generated by the controller to indicate that a completion entry has been added to the completion queue.
- 5 The fifth transaction indicates to the controller that the host has processed the completion entry that the controller added to the completion queue.

NOTE

You can also visualize a complete set of related transactions (as a super transaction) by right-clicking the transaction and selecting **Visualize Transaction....** See [page 113](#) for details on this feature. The following is an example of how the set of transactions for an *NVMe Read* command has been sequentially visualized.

Viewing Decoded Payload for NVMe Commands

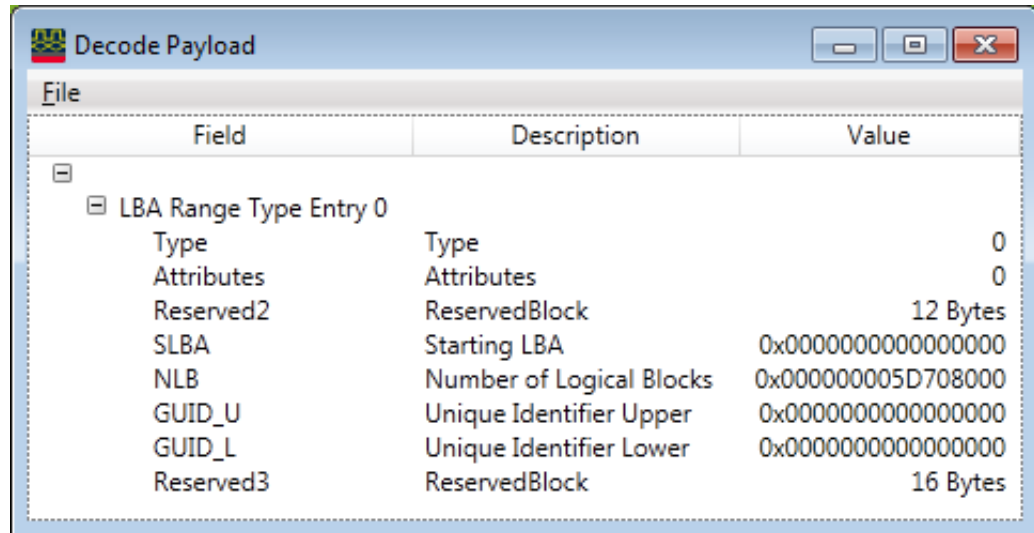
For NVMe commands that retrieve information from NVMe registers in a specific data structure format, you can view the returned data structure in the same format as defined in the NVMe protocol specifications. For instance, the *Identify* command or the *Get Features* command returns information in a specific data structure format defined in NVMe specifications. The Transaction Decode tab displays the decoded payload for such commands to present the data structure for such commands as per the defined format.

To view the decoded payload of an NVMe command

- 1 Right-click the transaction displayed for the command in the Transaction Decode tab.
- 2 Select **Decode Payload...**

The **Decode Payload** dialog box is displayed with the payload fields matching the fields specified in NVMe specifications.

The following screen displays the payload details of the *NVMe Get Features* command. The feature for which the information is retrieved is *LBA Range Type*. The displayed payload is as per the data structure format defined for LBA Range Type in NVMe specifications.



Field	Description	Value
[-] LBA Range Type Entry 0		
Type	Type	0
Attributes	Attributes	0
Reserved2	ReservedBlock	12 Bytes
SLBA	Starting LBA	0x0000000000000000
NLB	Number of Logical Blocks	0x000000005D708000
GUID_U	Unique Identifier Upper	0x0000000000000000
GUID_L	Unique Identifier Lower	0x0000000000000000
Reserved3	ReservedBlock	16 Bytes

Viewing Decoded PRPs for NVMe Commands

Physical Region Page (PRP) entries are used in read/write transactions to indicate the physical memory locations in system memory. These memory locations are used by controller for data transfers to and from system memory. For a Read request, the PRP entry indicates the memory location where the controller should transfer the data read from system memory. For a Write request, the PRP entry indicates the memory location from where the controller has to gather the data to be written to the system memory.

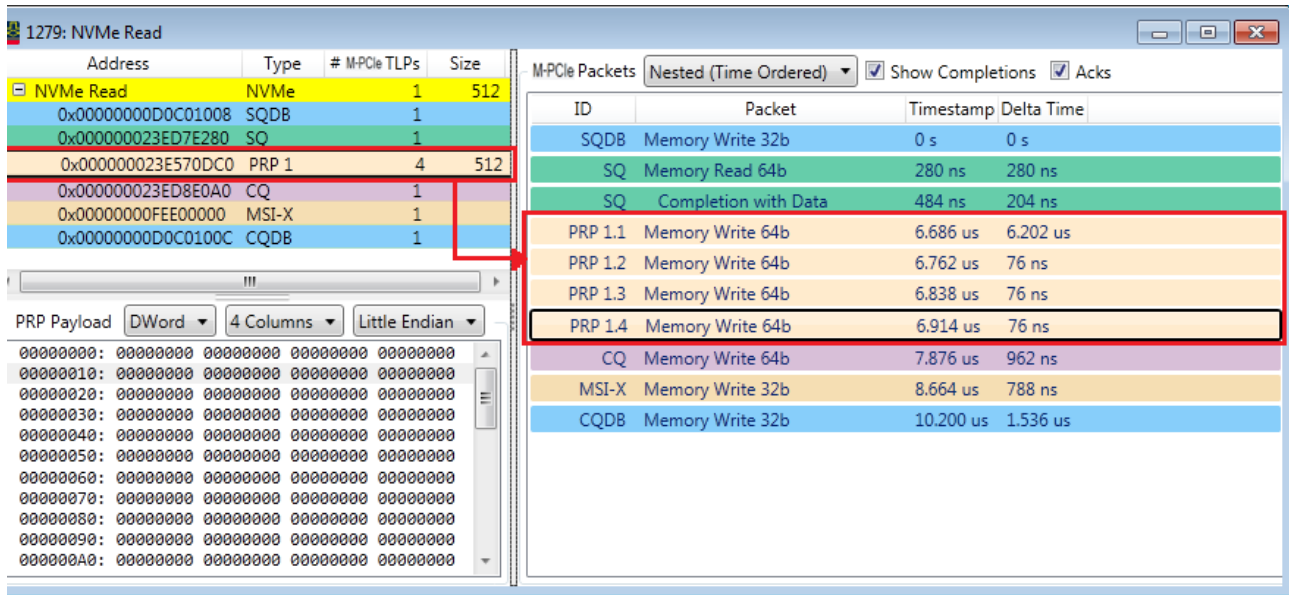
These addresses can be directly a memory location or a pointer to a location that provides a set of addresses of contiguous memory to perform large read/write operations.

In the Transaction Decode tab, you can view the PRP entries associated with a command that utilizes PRP entries such as an NVMe Read or Write command.

To view the decoded PRPs of an NVMe command

- 1 Right-click the transaction displayed for the command in the Transaction Decode tab.
- 2 Select **Visualize Transaction...**

A dialog box is displayed with PRPs and other related transactions associated with the command.



In this dialog box, you can find the following PRP related information.

Component displayed	Description
PRPs applicable for the NVMe command	<p>The displayed PRPs include:</p> <ul style="list-style-type: none"> a PRP entry representing an actual physical memory page. a PRP representing a pointer to a page that defines a PRP List. For such a PRP, a set of PRP entries in a single page of contiguous memory are also displayed underneath. multiple PRP lists for commands that require multiple PRP Lists for larger read/writes. The last PRP entry in the first list points to the next PRP list used.
Raw payload for each PRP entry	Clicking a PRP entry in the left displays the raw payload for that PRP entry in the lower pane of the dialog box.
Packets associated with each PRP entry	Clicking a PRP entry in the left displays a list of its associated MPCle packets in the right pane of the dialog box. You can click a packet from this list to navigate directly to that specific packet in the upper pane of Protocol Viewer. This can help you view the complete address range associated with a PRP entry.

Viewing Decoded MSI-X Table

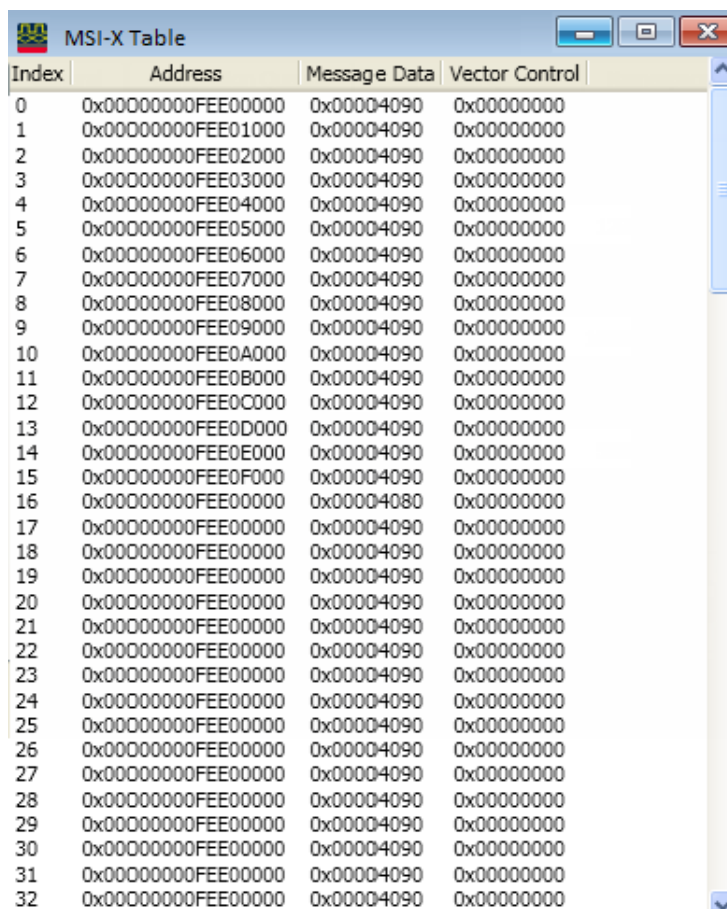
From the decoded **MSI-X Table** transaction, you can view the index entries of the MSI-X table in a decoded readable format.

To view index entries of the MSI-X table

- In the Transaction Decode tab, navigate to the MSI-X Table transaction by double-clicking the **MSI-X Table** transaction type from the right pane.
- Right-click the highlighted **MSI-X Table** transaction in the left pane of the Transaction Decode tab and then select **Decode MSI-X Table**.

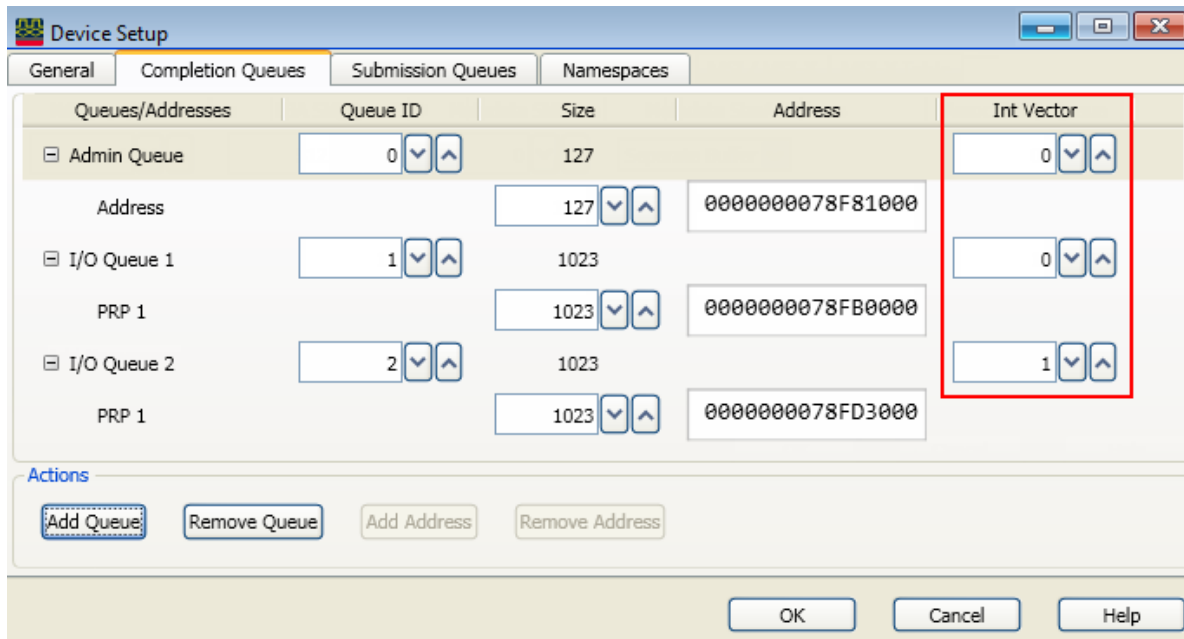
ID	Device	Transaction Type	Requestor ID	Completer ID	1st BE	Address	Payload	Time
572	003:00:0	MSI-X Table	000:00:0	000:00:0	0xF	0xD0C02000	0xFEE00000	63.6800
Go To								
Decode MSI-X Table...								
ID	Device	Transaction Type	Requestor ID	Completer ID	1st BE	Address	Payload	Time
573	___:__:__	Memory Write 32b	000:00:0	000:00:0	0xF	0xD0C02800	0xFEE00000	

The decoded MSI-X Table is displayed.



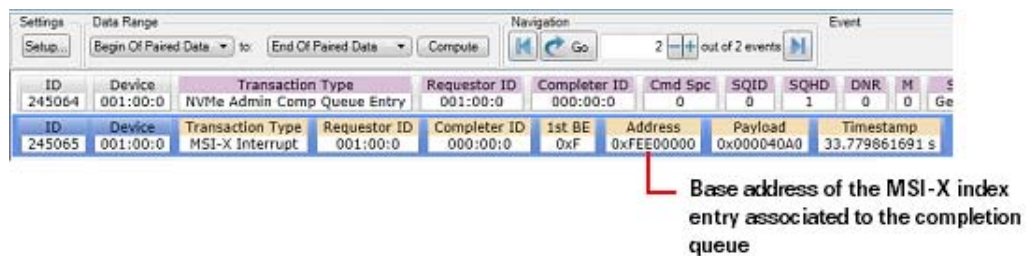
Index	Address	Message Data	Vector Control
0	0x00000000FEE00000	0x00004090	0x00000000
1	0x00000000FEE01000	0x00004090	0x00000000
2	0x00000000FEE02000	0x00004090	0x00000000
3	0x00000000FEE03000	0x00004090	0x00000000
4	0x00000000FEE04000	0x00004090	0x00000000
5	0x00000000FEE05000	0x00004090	0x00000000
6	0x00000000FEE06000	0x00004090	0x00000000
7	0x00000000FEE07000	0x00004090	0x00000000
8	0x00000000FEE08000	0x00004090	0x00000000
9	0x00000000FEE09000	0x00004090	0x00000000
10	0x00000000FEE0A000	0x00004090	0x00000000
11	0x00000000FEE0B000	0x00004090	0x00000000
12	0x00000000FEE0C000	0x00004090	0x00000000
13	0x00000000FEE0D000	0x00004090	0x00000000
14	0x00000000FEE0E000	0x00004090	0x00000000
15	0x00000000FEE0F000	0x00004090	0x00000000
16	0x00000000FEE00000	0x00004080	0x00000000
17	0x00000000FEE00000	0x00004090	0x00000000
18	0x00000000FEE00000	0x00004090	0x00000000
19	0x00000000FEE00000	0x00004090	0x00000000
20	0x00000000FEE00000	0x00004090	0x00000000
21	0x00000000FEE00000	0x00004090	0x00000000
22	0x00000000FEE00000	0x00004090	0x00000000
23	0x00000000FEE00000	0x00004090	0x00000000
24	0x00000000FEE00000	0x00004090	0x00000000
25	0x00000000FEE00000	0x00004090	0x00000000
26	0x00000000FEE00000	0x00004090	0x00000000
27	0x00000000FEE00000	0x00004090	0x00000000
28	0x00000000FEE00000	0x00004090	0x00000000
29	0x00000000FEE00000	0x00004090	0x00000000
30	0x00000000FEE00000	0x00004090	0x00000000
31	0x00000000FEE00000	0x00004090	0x00000000
32	0x00000000FEE00000	0x00004090	0x00000000

An MSI-X index entry is associated to a completion queue at the time of creation of the queue. You can view details about the MSI-X index entry associated to a completion queue in the **Completion Queues** tab of the **Setup** dialog box.



Each MSI-X index entry in the MSI-X table has a base address associated to it. This is the address at which the controller writes the MSI-X interrupt for the associated completion queue.

The following screen displays one such MSI-X Interrupt transaction for the completion queue 1.



Viewing AHCI Transactions

This topic is specific to AHCI transactions and provides examples of how you can interpret AHCI transactions that are computed and displayed in the **Transaction Decode** tab. To get information on how you can configure and compute AHCI transactions in the Transaction Decode tab, refer to the following previous topics in this chapter.

- “Transaction Decoding - Overview” on page 94
- “Configuring and Computing Decoded Transactions” on page 95
- “Defining / Verifying the Device Setup” on page 96
- “Interpreting and Navigating Through the Transaction Decode Results” on page 109

For AHCI, the Transaction Decode tab displays transactions for:

- PCIe Config space registers
- Generic host control registers
- Port specific registers
- SATA commands

The following sections provide examples of these decoded AHCI transactions.

PCIe Configuration Space Registers Transactions - Examples

These transactions are PCIe Config Read and Write requests to various configurations registers in the DUT's config space. You can view the PCI Header and PCI Capabilities for an AHCI device in these transactions. Some examples of the registers that these transactions map to are:

- PCI Header registers such as *Identifier (Device ID)*, *Class Codes*, *AHCI Base Address <BAR5>*, and *Capabilities Pointer (CAP)*.
- PCI Power Management Capability registers such as *PMCAP*
- Message Signaled Interrupt Capability registers such as *MSICAP*

Transaction Type	Requestor ID	Completer ID	1st BE	Reg Num	Reg Offset	Cap Type	Base Address 5	Payload
type 0	000:03:2	006:00:0	0xF	9	0x0024	PCI	0xD1C10000	0xD1C10000

Transaction Type	Requestor ID	Completer ID	1st BE	Reg Num	Reg Offset	Cap Type	Class Code Base Class	Sub Class
type 0	000:03:2	006:00:0	0xF	2	0x0008	PCI	Mass Storage Controller	SATA Controller/AHCI 1.0

Field	Value
Power Management Capabilities	0x0003
PME Support	0x00
D2 Support	0x0
D1 Support	0x0
Aux Current	0x0
Device Specific Initialization (DSI)	0x0
PME Clock	0x0
Version	0x3

Transaction Type	Requestor ID	Completer ID	1st BE	Reg Num	Reg Offset	Cap Type	Power Management Capabilities	Next P
type 0	000:03:2	006:00:0	0xF	30	0x0078	PM	0x0003	0x80

Transaction Type	Requestor ID	Completer ID	1st BE	Reg Num	Reg Offset	Cap Type	Message Control Capabilities	Payload
type 0	000:03:2	006:00:0	0xC	20	0x0050	MSI(32)	0x0000	0x00007805

NOTE

The decoded transactions are a mix of MPCle Config, generic host control, port-specific and SATA commands transactions. If you are looking for specific MPCle config space transactions, you may want to use the Overview pane on the right. In this pane, you can choose to organize the registers listing by **Configuration Space**. Doing so, displays the list of MPCle configuration space registers in this pane. You can then double-click the desired register from this list to navigate to the transaction for this register in the left pane.

MPCle config space registers are not displayed in the Overview pane if you have organized this pane by **Directions** or **Ports**.

Overview Properties			
Organize by:		Configuration Space	003:00:0
		Device ID	
PCIe / R/W->	Read	Write	Total
PCI [Status, Command] DW 1	48	37	85
PCI [BIST, HT, LT, CLS] DW 3	385	4	389
PCI [Max Lat, Min Gnt, Intr Pin, Intr Line] DW 15	14	19	33
PCIe [Dev Status, Dev Control] DW 2	7	4	11
PCIe [Link Status, Link Control] DW 4	9	3	12
PM [PMC, Next Cap, Cap ID] DW 0	9	0	9
PM [Data Reg, PMCSR_BSE, PMCSR] DW 1	9	2	11
MSI(32) [Message Control, Next Cap, Cap ID] DW 0	8	3	11
PCI [Dev ID, Vend ID] DW 0	783	1	784
PCI [Class Code, Rev ID] DW 2	53	0	53
PCI [Cap. Pointer] DW 13	13	0	13
PCI [BAR 0] DW 4	20	9	29
PCI [BAR 1] DW 5	20	9	29
PCI [BAR 2] DW 6	20	9	29
PCI [BAR 3] DW 7	20	9	29
PCI [BAR 4] DW 8	20	9	29

Generic Host Control Registers Transactions - Examples

Generic host control registers such as *AHCI CAP (Host Capabilities)* and *GHC (Global Host Control)* are global HBA registers that are applicable for the entire HBA. The Transaction Decode tab displays decoded transactions for these registers. The following screen shows some such transactions for generic host control registers.

III													
Timestamp	Transaction Type	MJR	MNR	Timestamp									
.091 us	AHCI VS (R)	0x0001	0x0200	59.221945343 s									
Timestamp	Transaction Type	S64A	SNCQ	SSNTF	SMPS	SSS	SALP	SAL	SCLO	SAM	SPM	FBSS	P
645 ns	AHCI CAP (R)	1	1	1	0	1	0	1	1	0	1	0	0
Timestamp	Transaction Type	APST	NVMP	BOH	Timestamp								
635 ns	AHCI CAP2 (R)	0	0	0	59.221946623 s								
Timestamp	Transaction Type	AE	MRS	IE	HR	Timestamp							
625 ns	AHCI GHC (R)	1	0	0	0	59.221947248 s							
Timestamp	Transaction Type	AE	MRS	IE	HR	Timestamp							
871 ns	AHCI GHC (W)	0	0	0	1	59.221948119 s							
Timestamp	Transaction Type	AE	MRS	IE	HR	Timestamp							
84 ns	AHCI GHC (R)	0	0	0	0	59.221948203 s							

Port Specific Registers Transactions - Examples

AHCI port specific registers such as *PcCLB*, *PxIS*, *PxSACT* and *PxCMD* are applicable for each port supported by an AHCI device. The read and write transactions to these port registers are also displayed in the Transaction Decode tab. The following screen displays some examples of such ports register accesses.

Overview Properties			
Organize by:	Ports	006:00:0	Device ID for which ports are displayed
AHCI / Ports->	0	1	Total
AHCI PxSACT (R)	0	1795	1795
AHCI PxCI (R)	205	1802	2007
AHCI PxCI (W)	0	605	605
AHCI PxIS (R)	206	606	812
AHCI PxIS (W)	1	602	603
AHCI PxSERR (R)	205	601	806
AHCI PxTFD (R)	205	613	818
AHCI PxSACT (W)	0	587	587
AHCI PxCLB (W)	1	1	2
AHCI PxCLBU (W)	1	1	2
AHCI PxFB (W)	1	1	2
AHCI PxFBU (W)	1	1	2
AHCI PxIE (R)	1	5	6
AHCI PxCMD (R)	308	15	323

NOTE

The decoded transactions are a mix of PCIe Config, generic host control, port-specific and SATA commands transactions. If you are looking for a particular type of port specific transactions, you may want to use the Overview pane on the right. In this pane, you can choose to organize the registers listing by **Ports**. Doing so, displays the list of port registers in this pane and organize the transaction occurrences by ports. You can then double-click the desired register from this list to navigate to the transaction for this register in the left pane.

Port registers are not displayed in the Overview pane if you have organized this pane by **Configuration Space**.

Overview Properties			
Organize by:	Ports	006:00:0	Device ID for which ports are displayed
AHCI / Ports->	0	1	Total
AHCI PxSACT (R)	0	1795	1795
AHCI PxCI (R)	205	1802	2007
AHCI PxCI (W)	0	605	605
AHCI PxIS (R)	206	606	812
AHCI PxIS (W)	1	602	603
AHCI PxSERR (R)	205	601	806
AHCI PxTFD (R)	205	613	818
AHCI PxSACT (W)	0	587	587
AHCI PxCLB (W)	1	1	2
AHCI PxCLBU (W)	1	1	2
AHCI PxFB (W)	1	1	2
AHCI PxFBU (W)	1	1	2
AHCI PxIE (R)	1	5	6
AHCI PxCMD (R)	308	15	323

AHCI ports supported by the device

SATA Commands Transactions - Examples

The decoded AHCI transactions also include transactions for SATA Commands such as *Identify Device*, *Set Features*, *Read FPDMA Queued*, *Write FPDMA Queued*, and *NOP*.

The following screens display some examples of decoded SATA Commands transactions.

Transaction Type	Port	Command Header	Address	Features	Command	C	R	R1	R2	PM Port	FIS
AHCI ATA Command SetFeature (R)	1	4	0x13E64ED00	Enable write cache	SET FEATURES	1	0	0	0	0x0	F
Transaction Type	Port	Command Header	Address	Features	Command	C	R	R1	R2	PM Port	FIS
AHCI ATA Command RDMA_NCQ (R)	1	28	0x13E64D800	0x00	READ FPDMA QUEUED	1	0	0	0	0x0	0
Transaction Type	Port	Command Header	Address	Features	Command	C	R	R1	R2	PM Port	FIS Type
AHCI ATA Command Identify (R)	1	1	0x13E64D800	0x00	IDENTIFY DEVICE	1	0	0	0	0x0	0x27
Transaction Type	Port	Command Header	Address	Features	Command	C	R	R1	R2	PM Port	FIS
AHCI ATA Command WRMA_NCQ (R)	1	16	0x13E64D800	0x08	WRITE FPDMA QUEUED	1	0	0	0	0x0	F

Visualizing a Complete Set of AHCI Transactions

You can view each AHCI transaction displayed in the Transaction Decode tab as a super transaction to visualize its complete set of related transactions. To visualize the transaction set for an AHCI transaction, right-click the transaction and select **Visualize Transaction....**

NOTE

For a SATA command transaction such as Read FPDMA Queued that has a number of related transactions, the Visualize Transaction feature is particularly useful.

For PCIe config space or AHCI (generic/port) register transactions, the Visualize Transaction feature displays the read/write access transaction to the register.

Example 1 - Visualize the transaction set for a NOP command transaction

The screenshot displays the 'Transaction Decode' tab with a transaction set for '545806: AHCI ATA Command NOP (R)'. The main table lists the following transactions:

Address	Type	# M-PCIe TLPs
0x00000000D1C101B8	PxCI	1
0x000000013E648000	CmdList	1
0x000000013E648500	AHCI ATA Command NOP (R)	1

Below the table, the 'PxCI Payload' is shown as '00000000: 00000001'. The 'M-PCIe Packets' table on the right shows the following packets:

ID	Packet	Timestamp	De
PxCI	Memory Write 32b	0 s	0
CmdList	Memory Read 64b	412 ns	4
CmdList	Completion with Data	715 ns	3
CFIS	Memory Read 64b	1.128 us	4
CFIS	Completion with Data	1.424 us	2

In the above example, the following transactions are a part of the *ATA Command NOP* super transaction.

- 1 The first transaction is a memory write to the PxCI (Port x Command Issue) register to indicate that a command has been built in memory for a command slot.
- 2 The second transaction is a memory read for the added command list entry.
- 3 The third transaction is a memory read of the NOP command from the command table.

Example 2 - Visualize the transaction set for a Write FPDMA Queued command transaction

555537: AHCI ATA Command WRMA_NCQ (R)

Address	Type	# M-PCIe TLPs
0x0000000D1C101B8	PxCI	1
0x000000013E648200	CmdList	1
0x000000013E64D800	AHCI ATA Command WRMA_NCQ (R)	1
0x000000013E648400	DMA Setup FIS	1
0x000000013E64D880	Physical Region Descriptor Table	1
0x000000013CAEA000	PRDT entry 0	15
0x000000013E648458	Set Device Bits FIS	1

Complete Payload: DWord, 4 Columns, Little Endian

```

00000000: 52545352 0009001E 00000000 00000000
00000010: 00001000 00001000 00010030 00040001
00000020: FFFFFFFF FFFFFFFF FFFFFFFF FFFFFFFF
00000030: 0212F1A4 00000000 FFFF0001 00000000
00000040: 00000028 004000E0 04000000 00000000
00000050: 00000070 00400030 C171AE60 00000000
00000060: 00000000 00000000 00000000 00000000
00000070: 0212F14B 00000000 0212F1A4 00000000
00000080: FFFFFFFF 00000000 00000000 00000008
00000090: 0054004E 00530046 00000000 00000000
000000A0: 00000000 00000000 00000000 00000000
000000B0: 00000000 00000000 00000000 00000000

```

ID	Packet	Timestamp	Delta
PxCI	Memory Write 32b	0 s	0 s
CmdList	Memory Read 64b	386 ns	386 ns
CmdList	Completion with Data	630 ns	244 ns
CFIS	Memory Read 64b	1.038 us	408 ns
CFIS	Completion with Data	1.292 us	254 ns
DSFIS	Memory Write 64b	88.364 us	87.07 us
PRDT	Memory Read 64b	88.476 us	112 ns
PRDT	Completion with Data	88.741 us	265 ns
PRD 0.1	Memory Read 64b	89.204 us	463 ns
PRD 0.1	Completion with Data	89.446 us	242 ns
PRD 0.1	Completion with Data	89.742 us	296 ns
PRD 0.1	Completion with Data	90.046 us	304 ns
PRD 0.2	Memory Read 64b	89.252 us	-794 ns
PRD 0.2	Completion with Data	90.632 us	1.38 us
PRD 0.3	Memory Read 64b	89.300 us	-1.33 us
PRD 0.3	Completion with Data	91.184 us	1.88 us

In the above example, the following transactions are a part of the *ATA Command Write FPDMA Queued* super transaction.

- 1 The first transaction is a memory write to the PxCI (Port x Command Issue) register to indicate that a command has been built in memory for a command slot.
- 2 The second transaction is a memory read for the added command list entry.
- 3 The third transaction is a memory read of the Write FPDMA Queued command from the command table.
- 4 The fourth transactions is a DMA Setup FIS (DSFIS) memory write to transfer data for the Write FPDMA Queued command in the appropriate slot.
- 5 The fifth transaction is a PRD Table read transaction. This transaction further expands to display the PRDT entry used for reading the transferred data.
- 6 The last transaction in the set is a memory write for updating the Set Device Bits FIS. This indicates the completion of the Write FPDMA Queued command.

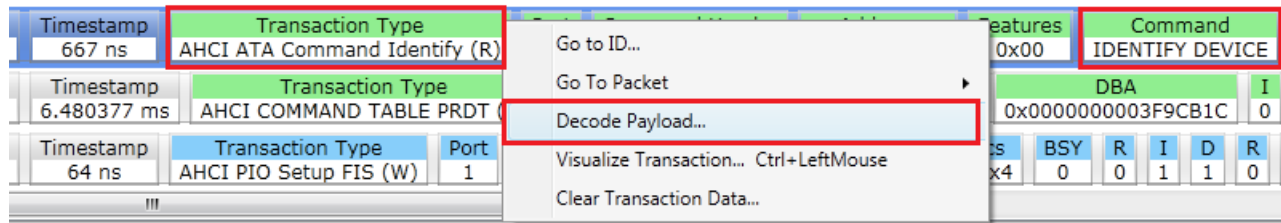
For more information on visualize transaction, refer to ["Visualizing a Transaction Set \(Super Transaction\)"](#) on page 113.

Viewing Decoded Payload for AHCI Commands

For *AHCI ATA IDENTIFY DEVICE* and *IDENTIFY (PACKET) DEVICE* commands that retrieve information in a specific data structure format, you can view the returned data structure in the same format as defined in the Serial ATA specifications. The Transaction Decode tab displays the decoded payload (all 256 words) for these commands to present their data structure as per the defined format.

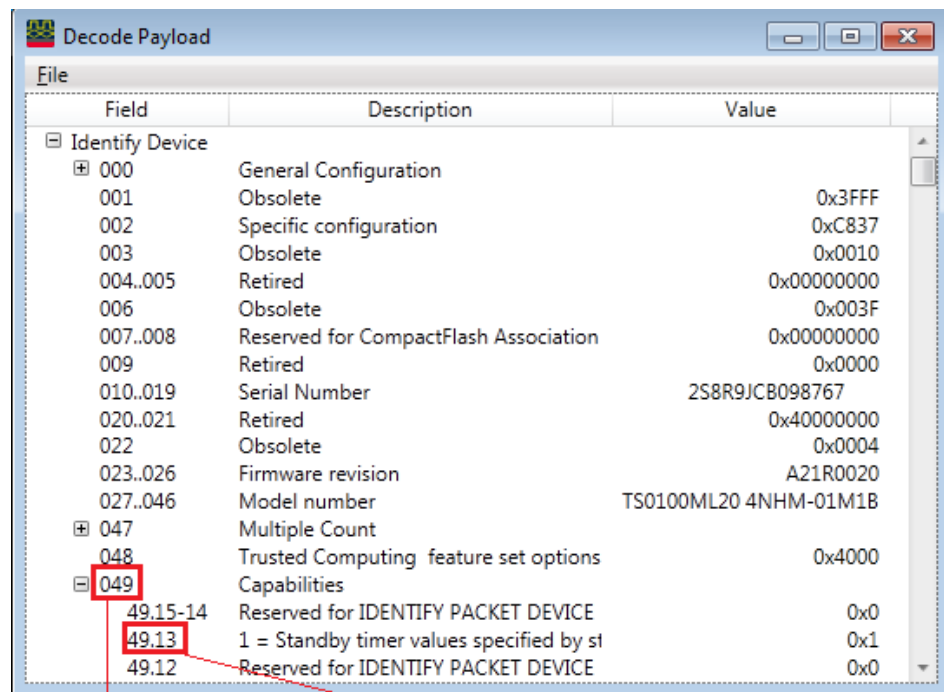
To view the decoded payload of an AHCI ATA IDENTIFY DEVICE command

- 1 Right-click an AHCI ATA IDENTIFY DEVICE transaction in the Transaction Decode tab.
- 2 Select **Decode Payload...**



The **Decode Payload** dialog box is displayed with the payload fields matching the information structure specified for the IDENTIFY DEVICE command in Serial ATA specifications.

The following screen displays the payload details of the *IDENTIFY DEVICE* command.



Maps to a word within the IDENTIFY DEVICE command as per the Serial ATA specifications. 49th word in this case.

Maps to a bit within a word. 13th bit of the 49th word in this case.

Viewing PRDT Entries for an ATA Command

Physical Region Descriptor Table (PRDT) is a table of scatter/gather list. It has zero to many entries. Each entry has a base address and byte counts in system memory where the device reads or writes the actual data transfer if the ATA command is a data transfer command.

In the Transaction Decode tab, you can view the PRDT entries associated to an ATA command that utilizes PRDT entries such as an *AHCI ATA Identify command* or *AHCI Read FPDMA Queued command*.

To view the decoded PRDT entries of an ATA command

- 1 Right-click the transaction displayed for the command in the Transaction Decode tab.
- 2 Select **Visualize Transaction...**

A dialog box is displayed with applicable PRDT transactions and other related transactions for the ATA command. In the following example, the three transactions for reading the PRD table for the Read FPDMA Queued command are highlighted. Clicking one of these PRDT transactions displays the raw payload for that PRDT Read transaction in the lower pane.

561940: AHCI ATA Command RDMA_NCQ (R)

Address	Type	# M-PCIe TLPs
0x00000000D1C101B8	PxCI	1
0x0000000013E648320	CmdList	1
0x0000000013E64D800	AHCI ATA Command RDMANCQ (R)	1
0x0000000013E648400	DMA Setup FIS	1
0x0000000013E64D880	Physical Region Descriptor Table	1
0x0000000013E64D980	Physical Region Descriptor Table	1
0x0000000013E64DA00	Physical Region Descriptor Table	1
0x0000000013E648458	Set Device Bits FIS	1

3 transactions for reading the PRD Table

Physical Region Descriptor Table Payload: DWord, 4 Columns, Little Endian

```

00000000: 784E4000 00000000 00000000 000000FF
00000010: 782E5000 00000000 00000000 000000FF
00000020: 785E6000 00000000 00000000 000000FF
00000030: 783E7000 00000000 00000000 000000FF
00000040: 77F19000 00000000 00000000 000000FF
00000050: 788E9000 00000000 00000000 000000FF
00000060: 78CEA000 00000000 00000000 000000FF
00000070: 78DEB000 00000000 00000000 000000FF
00000080: 786EC000 00000000 00000000 000000FF
00000090: 7841A000 00000000 00000000 000000FF
000000A0: 788EE000 00000000 00000000 000000FF
  
```

ID	Packet	Timestamp	Delta Time
PRDT	Memory Read 64b	69.033 us	112 ns
PRDT	Completion with Data	69.493 us	460 ns
PRDT	Completion with Data	69.789 us	296 ns
PRD 0.1	Memory Write 32b	71.140 us	1.351 us
PRD 0.2	Memory Write 32b	71.572 us	432 ns
PRD 0.3	Memory Write 32b	72.004 us	432 ns
PRD 0.4	Memory Write 32b	72.430 us	426 ns
PRD 0.5	Memory Write 32b	72.862 us	432 ns
PRD 0.6	Memory Write 32b	73.285 us	423 ns
PRD 0.7	Memory Write 32b	73.717 us	432 ns
PRD 0.8	Memory Write 32b	74.165 us	448 ns
PRD 0.9	Memory Write 32b	74.586 us	421 ns
PRD 0.10	Memory Write 32b	75.018 us	432 ns
PRD 0.11	Memory Write 32b	75.462 us	444 ns
PRD 0.12	Memory Write 32b	75.878 us	416 ns
PRD 0.13	Memory Write 32b	76.342 us	464 ns
PRD 0.14	Memory Write 32b	76.726 us	384 ns

- 3 Expand each of the displayed PRDT read transactions in the left to view the PRDT entries used for the data transfer. In the following example, the 16 PRDT entries applicable for the first PRDT read transaction are highlighted. With each PRDT entry, its base address, bytes count, and the number of MPCle TLPs utilized for data transfer are also displayed.

561940: AHCI ATA Command RDMA_NCQ (R)

Address	Type	# M-PCIe TLPs	Size
0x00000000D1C10188	PxCI	1	
0x0000000013E648320	CmdList	1	
0x0000000013E64D800	AHCI ATA Command RDMA_NCQ	1	
0x0000000013E648400	DMA Setup FIS	1	
0x0000000013E64D880	Physical Region Descriptor Table	1	
0x00000000784E4000	PRDT entry 0	32	4096
0x00000000782E5000	PRDT entry 1	32	4096
0x00000000785E6000	PRDT entry 2	32	4096
0x00000000783E7000	PRDT entry 3	32	4096
0x0000000077F19000	PRDT entry 4	32	4096
0x00000000788E9000	PRDT entry 5	32	4096
0x0000000078CEA000	PRDT entry 6	32	4096
0x0000000078DEB000	PRDT entry 7	32	4096
0x00000000786EC000	PRDT entry 8	32	4096
0x000000007841A000	PRDT entry 9	32	4096
0x00000000788EE000	PRDT entry 10	32	4096
0x00000000787EF000	PRDT entry 11	32	4096
0x00000000789F0000	PRDT entry 12	32	4096
0x0000000078FF1000	PRDT entry 13	32	4096
0x0000000078DF2000	PRDT entry 14	32	4096
0x00000000781F3000	PRDT entry 15	32	4096
0x0000000013E64D980	Physical Region Descriptor Table	1	
0x00000000784E4000	PRDT entry 16	32	4096

M-PCIe Packets: Nested (Time Ordered) | Show Completions | Ac

ID	Packet	Timestamp	Delta Time
PRDT	Memory Read 64b	69.033 us	112 ns
PRDT	Completion with Data	69.493 us	460 ns
PRDT	Completion with Data	69.789 us	296 ns
PRD 0.1	Memory Write 32b	71.140 us	1.351 us
PRD 0.2	Memory Write 32b	71.572 us	432 ns
PRD 0.3	Memory Write 32b	72.004 us	432 ns
PRD 0.4	Memory Write 32b	72.430 us	426 ns
PRD 0.5	Memory Write 32b	72.862 us	432 ns
PRD 0.6	Memory Write 32b	73.285 us	423 ns
PRD 0.7	Memory Write 32b	73.717 us	432 ns
PRD 0.8	Memory Write 32b	74.165 us	448 ns
PRD 0.9	Memory Write 32b	74.586 us	421 ns
PRD 0.10	Memory Write 32b	75.018 us	444 ns
PRD 0.11	Memory Write 32b	75.462 us	444 ns
PRD 0.12	Memory Write 32b	75.878 us	416 ns
PRD 0.13	Memory Write 32b	76.342 us	464 ns

4 Clicking a PRDT entry:

- displays the raw payload for the data transfer for the memory pointed to by that PRDT entry.
- highlights the first MPCle packet utilized for data transfer for the memory pointed to by that PRDT entry.

561940: AHCI ATA Command RDMA_NCQ (R)

Address	Type	# M-PCIe TLPs	Size
0x00000000D1C10188	PxCI	1	
0x0000000013E648320	CmdList	1	
0x0000000013E64D800	AHCI ATA Command RDMA_NCQ	1	
0x0000000013E648400	DMA Setup FIS	1	
0x0000000013E64D880	Physical Region Descriptor Table	1	
0x00000000784E4000	PRDT entry 0	32	4096
0x00000000782E5000	PRDT entry 1	32	4096
0x00000000785E6000	PRDT entry 2	32	4096
0x00000000783E7000	PRDT entry 3	32	4096
0x0000000077F19000	PRDT entry 4	32	4096
0x00000000788E9000	PRDT entry 5	32	4096
0x0000000078CEA000	PRDT entry 6	32	4096
0x0000000078DEB000	PRDT entry 7	32	4096
0x00000000786EC000	PRDT entry 8	32	4096

M-PCIe Packets: Nested (Time Ordered) | Show Completions | Ac

ID	Packet	Timestamp	Delta Time
PRD 3.25	Memory Write 32b	123.596 u	416 ns
PRD 3.26	Memory Write 32b	124.028 u	432 ns
PRD 3.27	Memory Write 32b	124.460 u	432 ns
PRD 3.28	Memory Write 32b	124.876 u	416 ns
PRD 3.29	Memory Write 32b	125.340 u	464 ns
PRD 3.30	Memory Write 32b	125.756 u	416 ns
PRD 3.31	Memory Write 32b	126.194 u	438 ns
PRD 3.32	Memory Write 32b	126.610 u	416 ns
PRD 4.1	Memory Write 32b	127.870 u	1.260 us
PRD 4.2	Memory Write 32b	128.286 u	416 ns
PRD 4.3	Memory Write 32b	128.750 u	464 ns
PRD 4.4	Memory Write 32b	129.173 u	423 ns
PRD 4.5	Memory Write 32b	129.605 u	432 ns
PRD 4.6	Memory Write 32b	130.021 u	416 ns
PRD 4.7	Memory Write 32b	130.453 u	432 ns

PRDT entry Payload: DWord | 4 Columns | Little Endian

```

00000000: 5308A012 3DEFACAD 219A4962 8F995863
00000010: 177A8843 0CA94CA1 6A2D0503 EA9192D5
00000020: CEB020A5 FCBCF540 4D5A382D 35CC0F03
00000030: BCF806AC A21ACC5B 277956EB 52D3359C
00000040: 4CED0F3B 040F6EA4 7028F8D7 208D4401
  
```

Viewing SSIC Transactions

This topic is specific to SSIC transactions and provides examples and information on how you can interpret SSIC transactions that are computed and displayed in the **Transaction Decode** tab. To get information on how you can configure and compute SSIC transactions in the Transaction Decode tab, refer to the following previous topics in this chapter.

- [“Transaction Decoding - Overview”](#) on page 94
- [“Configuring and Computing Decoded Transactions”](#) on page 95
- [“To view/add details of an SSIC device”](#) on page 102
- [“Interpreting and Navigating Through the Transaction Decode Results”](#) on page 109

For SSIC, the Transaction Decode tab displays transactions as commands. These commands can be the following requests that the host sends to the device:

- USB standard device requests such as SET_ADDRESS and GET_DESCRIPTOR.
- ACM device subclass requests such as SET_COMM_FEATURE, GET_COMM_FEATURE, and SET_LINE_CODING.
- Network Control Model (NCM) subclass requests such as GET_NET_ADDRESS, GET_CRC_MODE, and SET_NTB_INPUT_SIZE
- Mobile Broadband Interface Model (MBIM) subclass requests such as SEND_ENCAPSULATED_COMMAND and GET_ENCAPSULATED_RESPONSE and also RESPONSE_AVAILABLE and Unsolicited Events notifications from the device to the host.

Understanding the SSIC Transactions Display

On expanding an SSIC transaction, you can view all the packets exchanged between the host and device for that transaction (if bidirectional data is captured). Clicking a packet from this list displays the packet's decoded header in the **Decodes** tab on the right. If it is a Data packet, then the Decodes tab also displays the data defining the request sent from the host or the data returned by the device in response to the host's request, as the case may be.

Each transaction row represents the first packet exchanged for that transaction and therefore color coded as per the color coding applicable for this first packet. Clicking the transaction row displays the following information in the Decodes tab on the right.

- the header for the first packet of the transaction.
- the data sent by host and device in Data Packet(s) applicable for the transaction.

A sample SSIC transaction for the SET_ADDRESS command is displayed below with all the applicable packets nested within it. The decoded data for the SET_ADDRESS request sent by host is shown in the Decodes tab on the right.

Transaction Decode Table:

ID	Command	Control	SSIC-101A	SSIC-101E	Timestamp	Dev Addr
0	SET_ADDRESS	Data Packet			192 ns	1
0.1				ACK	401 ns	1
0.2			STATUS		513 ns	1
0.3			NRDY		561 ns	1
0.4			ERDY		721 ns	1
0.5			STATUS		865 ns	1
0.6				ACK	946 ns	1
1	GET_DESCRIPTOR	Data Packet			1.218 us	1
2	GET_DESCRIPTOR	Data Packet			3.125 us	1
3	GET_DESCRIPTOR	Data Packet			7.452 us	1
4	GET_DESCRIPTOR	Data Packet			12.1925709	1
5	SET_CONFIGURATI	Data Packet			26.1389553	1
6	SET_INTERFACE	Data Packet			26.1389563	1
7	SEND_ENCAPSULA	Data Packet			26.1389574	1
8	GET_ENCAPSULAT	Data Packet			26.1389585	1
9	SET_COMM_FEATU	Data Packet			26.1389605	1
10	GET_COMM_FEATL	Data Packet			26.1389615	1
11	CLEAR_COMM_FEA	Data Packet			26.1389635	1
12	SET_LINE_CODING	Data Packet			26.1389645	1
13	GET_LINE_CODING	Data Packet			26.1389657	1

Data Packet Header:

Byte	Field	Value
3	EPF	0xF7
7	Device Address	0x01
7	Hub@Tier	0x0
11	Data Length	0x0008
15	# of Bus Int	0x0
15	P	1
15	D	0
15	W	0
15	S	0
15	Reserv	0x00
19	CRC-5	0x0A
19	DefDel	0
19	Hub D	0x0
19	RLCW	0x0

SET_ADDRESS Device Request

Field	Value
bmRequestType	0
Direction	7
Type	6:5
Recipient	4:0
bRequest	1

Overview tab

The Overview tab on the right side displays statistics for the computed SSIC transactions. This tab lists the transaction types (device requests) applicable for the computed transaction data and the number of events/occurrences for each transaction type in the computed data. The occurrences are also categorized based on the direction, that is host to device and device to host.

Decodes Overview			
Transactions / Directions->	SSIC-101A	SSIC-101B	Total
CLEAR_COMM_FEATURE	1	0	1
GET_COMM_FEATURE	1	0	1
GET_CRC_MODE	1	0	1
GET_DESCRIPTOR (CONFIGURATION)	3	0	3
GET_DESCRIPTOR (DEVICE)	1	0	1
GET_ENCAPSULATED_RESPONSE	8	0	8
GET_ETHERNET_POWER_MANAGEMENT_PATTEF	1	0	1
GET_ETHERNET_STATISTIC	1	0	1
GET_LINE_CODING	1	0	1
GET_MAX_DATAGRAM_SIZE	1	0	1
GET_NET_ADDRESS	1	0	1
GET NTB_FORMAT	1	0	1
GET NTB_INPUT_SIZE	1	0	1
GET NTB_PARAMETERS	1	0	1
MBIM TRANSFER BLOCK	0	2	2
RESPONSE_AVAILABLE	0	6	6
SEND_BREAK	1	0	1
SEND_ENCAPSULATED_COMMAND	7	0	7
SET_ADDRESS	1	0	1
SET_COMM_FEATURE	1	0	1
SET_CONFIGURATION	3	0	3
SET_CONTROL_LINE_STATE	1	0	1
SET_CRC_MODE	1	0	1
SET_ETHERNET_MULTICAST_FILTERS	1	0	1

The following sections provide a few examples of decoded SSIC transactions of the supported device class and subclasses.

USB Standard Device Request Transactions - Example

In the following example, the decoded transaction for a **GET_DESCRIPTOR** request with the descriptor type set to Device is displayed. The first packet is a Data Packet sent by host to device to define the Device Descriptor request. After the subsequent ACKs and Endpoint Not Ready (NRDY) /Ready (ERDY) packets, the Device Descriptor is returned by the device in a Data Packet. The host then initiates the STATUS stage by sending the STATUS packet which is acknowledged by the device.

Notice that different colors have been used to represent different packet types in the transaction.

The Decodes tab on the right displays the:

- header for the first Data Packet of the transaction.
- the data for the packet defining the **GET_DESCRIPTOR** request.
- the data for the Device Descriptor returned by the device.

The screenshot shows the Keysight U4431A MIPI M-PHY Protocol Analyzer interface. The main window displays a list of transactions with columns for ID, Command, Control, SSIC-101A, SSIC-101E, Timestamp, and Dev A. Transaction 1 is selected, showing a GET_DESCRIPTOR (DEVICE) command. The right pane shows the 'Decodes' tab with a 'Data Packet Header' section and a 'Device Descriptor' section.

ID	Command	Control	SSIC-101A	SSIC-101E	Timestamp	Dev A
0	SET_ADDRESS		Data Packet		192 ns	1
1	GET_DESCRIPTOR (DEVICE)		Data Packet		1.218 us	1
1.1				ACK	1.410 us	1
1.2			ACK		1.571 us	1
1.3				NRDY	1.715 us	1
1.4				ERDY	1.875 us	1
1.5			ACK		1.971 us	1
1.6				Data Packet	2.035 us	1
1.7			ACK		2.259 us	1
1.8			STATUS		2.420 us	1
1.9				NRDY	2.676 us	1
1.10				ERDY	2.836 us	1
1.11			STATUS		2.868 us	1
1.12				ACK	2.997 us	1
2	GET_DESCRIPTOR (CONFIG)		Data Packet		3.125 us	1
3	GET_DESCRIPTOR (CONFIG)		Data Packet		7.452 us	1
4	GET_DESCRIPTOR (CONFIG)		Data Packet		12.192570s	1
5	SET_CONFIGURATION		Data Packet		26.138955s	1
6	SET_INTERFACE		Data Packet		26.138956s	1

Data Packet Header:

Byte	7	6	5	4	3	2	1	0	7	6	5
Byte 3	EPF										
Byte 7	Device Address						Hub@Tier5				
Byte 11	Data Length										
Byte 15	# of Bus Int										
Byte 19	CRC-5										

Device Descriptor

Index	Field
0	bLength
1	bDescriptorType
2	bcdUSB

ACM Device Subclass Requests Transactions - Example

In the following example, the decoded transaction for a **GET_COMM_FEATURE** ACM request is displayed. The first packet is a Data Packet sent by host to device to get the current settings for the Abstract state communication feature. The device returns a Data Packet with the current settings for its Abstract state feature.

The Decodes tab on the right displays the:

- header for the first Data Packet of the transaction.
- the data for the packet defining the **GET_COMM_FEATURE** ACM request.
- the data for the Abstract state feature response returned by the device.

The screenshot shows the Keysight U4431A MIPI M-PHY Protocol Analyzer interface. The main window displays a list of transactions with columns for ID, Command, Control, SSIC-101A, SSIC-101E, Timestamp, and Dev. Transaction 10 is selected, showing a sequence of ACK, NRDY, ERDY, ACK, Data Packet, ACK, STATUS, NRDY, ERDY, STATUS, and ACK. The right pane shows the 'Decodes' tab with a detailed view of the first Data Packet (Byte 19) and the 'Abstract state Feature Response' section.

ID	Command	Control	SSIC-101A	SSIC-101E	Timestamp	Dev
10	GET_COMM_FEATURE		Data Packet		26.138961E	1
10.1				ACK	26.1389617	1
10.2			ACK		26.1389619	1
10.3				NRDY	26.1389620	1
10.4				ERDY	26.1389621	1
10.5			ACK		26.1389623	1
10.6				Data Packet	26.1389623	1
10.7			ACK		26.1389626	1
10.8			STATUS		26.1389629	1
10.9				NRDY	26.1389629	1
10.10				ERDY	26.1389631	1
10.11			STATUS		26.1389632	1
10.12				ACK	26.1389632	1
11	CLEAR_COMM_FEATURE		Data Packet		26.138963E	1
12	SET_LINE_CODING		Data Packet		26.138964E	1
13	GET_LINE_CODING		Data Packet		26.1389657	1
14	SET_CONTROL_LINE_STA		Data Packet		26.1389677	1
15	SEND_BREAK		Data Packet		26.138968E	1
16	SET_CONFIGURATION		Data Packet		37.6345034	1
17	SET_INTERFACE		Data Packet		37.6345044	1

The right pane shows the 'Decodes' tab with a detailed view of the first Data Packet (Byte 19) and the 'Abstract state Feature Response' section.

Byte 19: 0x12 0 0 0x0 0x0

☐ Show Collapsed

ACM Request

0	bmRequestType
7	Direction
6:5	Type
4:0	Recipient
1	bRequest
2	wValue
4	wIndex
6	wLength

Abstract state Feature Response

0	ABSTRACT_STATE selector
15:2	Reserved
1	D1(Data Multiplexed State)
0	D0(Idle Setting) 0x0 (End)

NCM Device Subclass Requests - Example

In the following example, the decoded transaction for a **SET NTB FORMAT** NCM request is displayed to set the device's Network Transfer Blocks (NTB) format to 32 bit.

The Decodes tab on the right displays the:

- header for the first Data Packet of the transaction.
- the data for the packet setting the NTB format to 32 bit.

The screenshot displays the 'Transaction Decode' tab of the Keysight U4431A MIPI M-PHY Protocol Analyzer. The main window shows a list of transactions. Transaction 27, 'SET NTB_FORMAT', is selected, and its details are shown in the right pane. The details include a sequence of ACK, STATUS, NRDY, ERDY, STATUS, and ACK. The right pane also shows the decoded data for transaction 27, including a network control model request with fields like bmRequestType, Direction, Type, Recipient, bRequest, wValue, wIndex, and wLength.

ID	Command	Control	SSIC-101A	SSIC-101E	Timestamp	Dev
25	SET_NET_ADDRESS		Data Packet		37.634516E	1
26	GET NTB_FORMAT		Data Packet		50.5411304	1
27	SET NTB_FORMAT		Data Packet		50.5411324	1
27.1				ACK	50.541132E	1
27.2				STATUS	50.5411327	1
27.3				NRDY	50.541132E	1
27.4				ERDY	50.541132E	1
27.5				STATUS	50.5411331	1
27.6				ACK	50.5411332	1
28	GET NTB_INPUT_SIZE		Data Packet		50.5411334	1
29	SET NTB_INPUT_SIZE		Data Packet		50.5411354	1
30	GET_MAX_DATAGRAM_SI		Data Packet		50.5411364	1
31	SET_MAX_DATAGRAM_SI		Data Packet		50.5411384	1
32	GET_CRC_MODE		Data Packet		50.541139E	1
33	SET_CRC_MODE		Data Packet		50.541141E	1
34	SET_CONFIGURATION		Data Packet		62.4937771	1
35	SET_INTERFACE		Data Packet		62.4937781	1
36	SET_INTERFACE		Data Packet		62.493779E	1
37	SEND_ENCAPSULATED_C	MBIM_C	Data Packet		62.493780E	1
38	RESPONSE_AVAILABLE		Data Packet		62.493782E	1

The right pane shows the decoded data for transaction 27. It includes a network control model request with the following fields:

Field	Value
bmRequestType	0x0 (Host-to-Device)
Direction	0x0 (Host-to-Device)
Type	0x1
Recipient	0x01 (Internal)
bRequest	0x84 (SET NTB_FORMAT)
wValue	0x0001 (1)
wIndex	
wLength	

MBIM Device Subclass Requests - Example

In the following example, a sequence of decoded transactions for MBIM requests are displayed. The first transaction is a **SEND_ENCAPSULATED_COMMAND** request sent by host with an MBIM_COMMAND_MSG control message to request information associated to MBIM_CID_DEVICE_CAPS. The transaction is followed by a **RESPONSE_AVAILABLE** notification from device to host. The last transaction is a **GET_ENCAPSULATED_RESPONSE** transaction from the host to retrieve the MBIM_DEVICE_CAPS_INFO information in an MBIM_COMMAND_DONE control message.

Header Payload Bytes Traffic Overview **Transaction Decode** Test Assertion Performance Overview Compare 1

Settings Data Range Beginning Of Data to: End Of Data Compute Synchronize

ID	Command	Control Message	SSIC-101A	SSIC-101E
40	SEND_ENCAPSULATED_COMM	MBIM_COMMAND_MSG(MBIM_CIC	Data Packet	
40.1				ACK
40.2			Data Packet	
40.3				ACK
40.4			STATUS	
40.5				NRDY
40.6				ERDY
40.7			STATUS	
40.8				ACK
41	RESPONSE_AVAILABLE			Data Packe
42	GET_ENCAPSULATED_RESPON	MBIM_COMMAND_DONE(MBIM_C	Data Packet	
42.1				ACK
42.2			ACK	
42.3				NRDY
42.4				ERDY
42.5			ACK	
42.6				Data Packe
42.7			ACK	
42.8			STATUS	
42.9				NRDY
42.10				ERDY
42.11			STATUS	
42.12				ACK

Decodes Overview

MBIM request

0	bmRequestType
7	Direction
6:5	Type
4:0	Recipient
1	bRequest 0x
2	wValue
4	wIndex
6	wLength

MBIM_COMMAND_MSG

0	MessageType
4	MessageLength
8	TransactionId
12	Total Fragments
16	Current Fragments
20	DeviceServiceId
36	CID
40	CommandType
44	InformationBufferLength

8 Computing and Running Test Assertions

Test Assertion - Overview / 142
Computing the Test Assertions / 144
Viewing and Interpreting the Test Results / 146

Test cases are a set of predefined logical rules. Running the test cases on captured packets allows you to check whether or not in a trace the captured packets have been exchanged as per the defined rules. This chapter describes how you can run the test cases, and compute and assess the assertion results.

Test Assertion - Overview

Test Assertion is the process of running one or more test cases on the captured packets which allows you to check whether the captured packets have been exchanged as per the predefined rule. The **Test Assertion** tab in the Protocol Viewer window allows you to compute the test assertions for one or more traces.

Types of Protocols Supported

In this release, assertion of SSIC test cases is supported.

Test Assertion Tab

You use the **Test Assertion** tab displayed in the lower pane of the Protocol Viewer to view and run the test cases.

The **Test Assertion** tab consist of two panes. The left pane displays a list of test cases. When you select any test case, the sub-pane in the bottom of the left pane displays the details of the selected test case. The right pane displays the test summary of the computed test assertion(s).

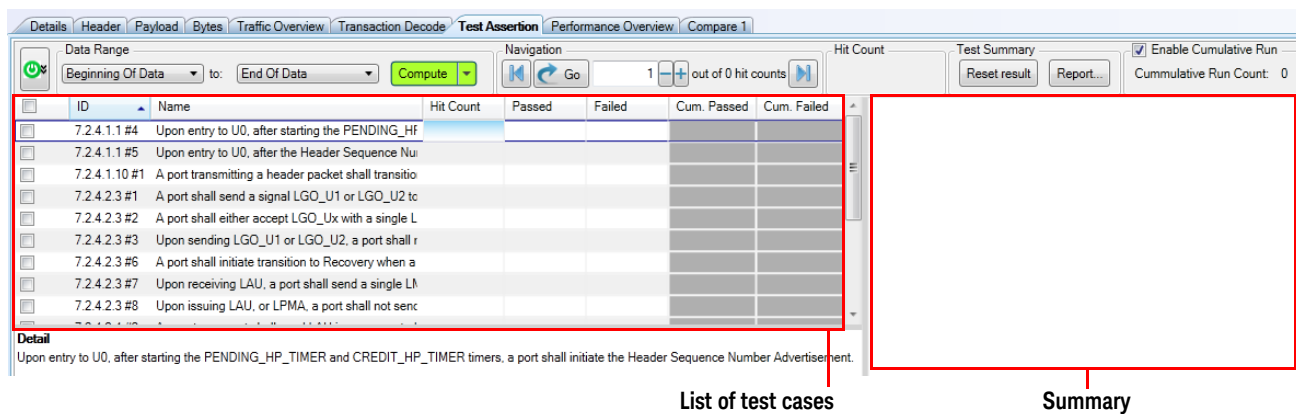


Table 1 lists the various test cases for this release.

Table 1 Test Cases

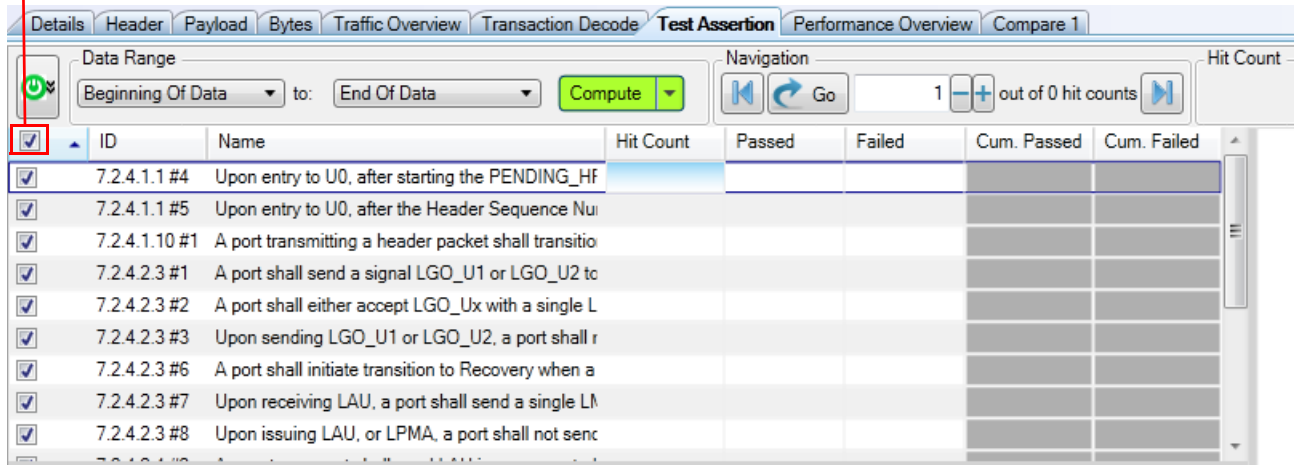
Sr. No.	ID	Description
1	7.2.4.1.1 #4	Upon entry to U0, after starting the PENDING_HP_TIMER and CREDIT_HP_TIMER timers, a port shall initiate the Header Sequence Number Advertisement
2	7.2.4.1.1 #5	Upon entry to U0, after the Header Sequence Number Advertisement, a port shall initiate the Rx Header Buffer Credit Advertisement
3	7.2.4.1.10 #1	A port transmitting a header packet shall transition to Recovery upon its PENDING_HP_TIMER timeout
4	7.2.4.2.3 #1	A port shall send a signal LGO_U1 or LGO_U2 to request a transition to a low power link state
5	7.2.4.2.3 #2	A port shall either accept LGO_Ux with a single LAU or shall reject LGO_U1 or LGO_U2 with a single LXU and remain in U0
6	7.2.4.2.3 #3	Upon sending LGO_U1 or LGO_U2, a port shall not send any packets until it has received LXU or re-entered U0

Sr. No.	ID	Description
7	7.2.4.2.3 #6	In U1/U2 entry flow, a port shall initiate transition to Recovery when a single LAU or LXU is not received upon PM_LC_TIMER timeout
8	7.2.4.2.3 #7	In U1/U2 entry flow, upon receiving LAU, a port shall send a single LPMA and then the requested low power link state should be entered
9	7.2.4.2.3 #8	In U1/U2 entry flow, upon issuing LAU, or LPMA, a port shall not send any packets or link commands
10	7.2.4.2.4 #2	In U3 entry flow, an upstream port shall send LAU in response to LGO_U3
11	7.2.4.2.4 #3	In U3 entry flow, an upstream port shall not send any packets or link commands subsequent to sending an LAU
12	7.2.4.2.4 #5	In U3 entry flow, a downstream port shall send a single LPMA and then transition to U3 when LAU is received
13	7.2.4.2.4 #7	In U3 entry flow, an upstream port shall transition to U3 when LPMA is received
14	7.3.4 #6	A port shall transition to Recovery upon its PM_LC_TIMER timeout
15	7.3.6 #1	A port shall transition to Recovery upon its PENDING_HP_TIMER timeout before the Header Sequence Number Advertisement is received
16	7.4.2#4	Upon completion of Hot Reset, the LTSSM of a port shall transition to U0
17	7.4.2#4	When a PORT_RESET is directed, when a Hot Reset fails due to a TS1/TS2 handshake timeout, a downstream port shall transition to Rx. Detect and attempt a Warm Reset

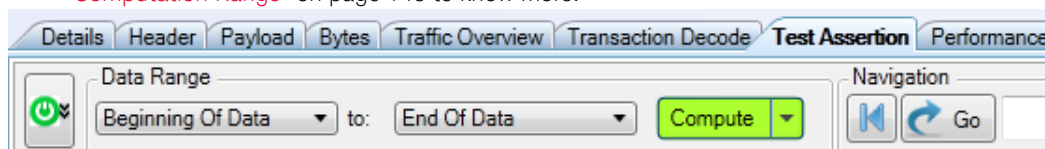
Computing the Test Assertions

- 1 Click the **Test Assertion** tab in the Protocol Viewer window.
- 2 Select all or the individual test cases you want to assert by selecting the required checkboxes. For selecting all the test cases, you can select the checkbox as shown below.

Select this check box to select all the test cases

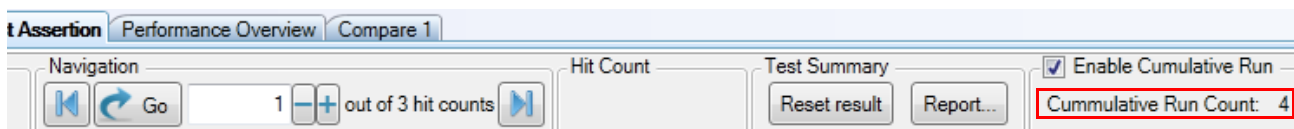


- 3 In the **Data Range** groupbox, specify the start and end points of the captured data for which you want to run the test cases. Following options are available for setting the data range.
 - **Beginning and End of data** - This data range selection ensures that test cases are run for the entire trace.
 - **Trigger** - Selecting Trigger in the data range ensures that test cases are run from the point where the U4431 module's trigger condition was met.
 - **Markers** - Selecting markers in the data range ensures that test cases are run for the specific portion of the traffic defined by markers. Refer to ["Defining Markers for Setting the Computation Range"](#) on page 145 to know more.



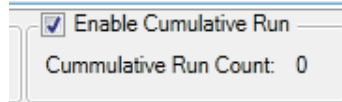
- 4 Click the **Compute** button displayed with the Data Range fields. The **Compute This** and **Compute All** are the two options available with this button.
 - **Compute All** allows you to compute traffic overview statistics (see [page 64](#)), decoded transactions, the test assertions and offline performance summary (see [page 156](#)) for the captured packets by a single click of this button.
 - **Compute This** allows you to compute only the test assertions. When you click **Compute**, then also only the test assertions are computed.

Cumulative Run Count shows the total number of compute operations performed for the trace.



NOTE

The total number of compute operations are only counted when you have selected the **Enable Cumulative Run** check box. Selecting the **Enable Cumulative Run** check box also displays the **Cumulative Passed** and **Cumulative Failed** columns.



Test cases are computed and displayed for the specified data range.

ID	Name	Hit Count	Passed	Failed	Cum. Passed	Cum. Failed
7.2.4.2.3 #1	A port shall send a signal LGO_U1 or LGO_U2 to	1	1	0	1	0
7.2.4.2.3 #2	A port shall either accept LGO_Ux with a single L	3	0	3	0	3
7.2.4.2.3 #3	Upon sending LGO_U1 or LGO_U2, a port shall r	1	0	1	0	1
7.2.4.2.3 #6	A port shall initiate transition to Recovery when a	1	0	1	0	1
7.2.4.2.3 #7	Upon receiving LAU, a port shall send a single LM	0	0	0	0	0
7.2.4.2.3 #8	Upon issuing LAU, or LPMA, a port shall not senc	1	1	0	1	0
7.2.4.2.4 #2	An upstream port shall send LAU in response to I	3	0	3	0	3
7.2.4.2.4 #3	An upstream port shall not send any packets or li	0	0	0	0	0
7.2.4.2.4 #5	A downstream port shall send a single LPMA and	0	0	0	0	0

Detail
A port shall either accept LGO_Ux with a single LAU or shall reject LGO_U1 or LGO_U2 with a single LXU and remain in U0.

NOTE

You need to recompute the test assertion(s) if you want to change the data range for which test result is to be displayed.

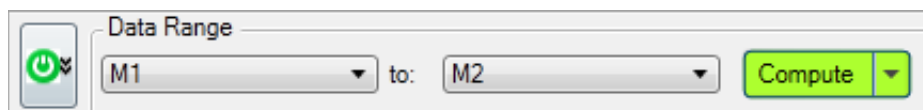
Defining Markers for Setting the Computation Range

If the captured traffic is too large and you want to view the test results from a specific portion of this traffic, then you can limit the computation range by defining start and end markers in the traffic.

To define markers

- 1 From the upper pane of the Protocol Viewer, right-click the row in the captured traffic that should act as the starting point for test assertion.
- 2 Select **Place Marker** from the displayed context menu and then select an existing marker or click **New Marker** to define a new marker at this point.

Once markers are defined, these are available for selection in the **Data Range** group box of the **Test Assertion** tab.



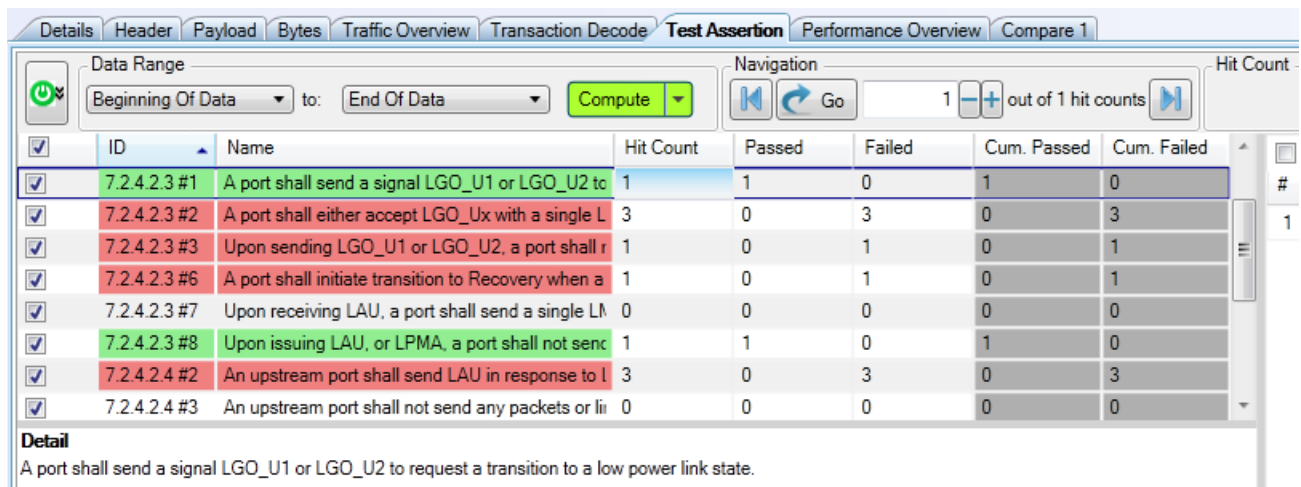
Viewing and Interpreting the Test Results

A test assertion is successful if the selected test case(s) passes all the encountered instances in the most recent compute operation. A test assertion has failed if the selected test case couldn't pass at least one encountered instance in the most recent compute operation.

After computing the selected test assertion(s), the test results are displayed in the **Test Assertion** tab.

Viewing the Test Cases Pane

In the left pane of the **Test Assertion** tab, test cases with their details are displayed. In this pane, a test case is uniquely identified by a reference ID. The description of the specific test case is also displayed. You can sort the test cases based on their IDs and descriptions.



A color coding scheme is used to clearly indicate the results of the test assertion. The background color of the selected test case:

- changes to green, if the test assertion is successful.
- changes to red, if the test assertion has failed.
- retains the default color, if the test case wasn't encountered at all in the most recent compute operation.

Based on the most recent compute operations, the results section also displays the following informations:

Column	Description
Hit Count	Displays the number of instances the selected test case was encountered in the most recent compute operation
Passed	Displays the number of instances the selected test case has passed the test assertion
Failed	Displays the number of instances the selected test case has failed the test assertion
Cumulative Passed	Displays the cumulative number of instances the selected test case has passed the test assertion over a number of compute operations
Cumulative Failed	Displays the cumulative number of instances the selected test case has failed the test assertion over a number of compute operations

Viewing the Test Summary

You can view the summary of the most recent test assertion or the collective summary of all the test assertions.

Viewing the Individual Test Results

You can click the individual test cases in the left pane of the **Test Assertion** tab for viewing the summary of the desired test assertions in the right pane of the **Test Assertion** tab. In the summary section, the detailed result of the specific test assertion is displayed.

- The **Time** column displays the time of the first packet at which the selected test assertion started.
- The **Status** column displays whether the selected test case has passed or failed the desired assertion.
- The **Description** column displays the failure causes of the test assertion in that particular time interval. For example in the below screen, when the test case was encountered the second time, the test assertion failed at 4.943 us. The detailed description of the failure is also displayed.

Select this checkbox to view only the Failed assertions

The screenshot shows the 'Test Assertion' tab with the 'Test Summary' section. The 'Exclude Passed result' checkbox is checked. The table below shows the results of the test assertions.

#	Time	Status	Description
1	176 ns	Failed	PM_LC_TIMER Timer elapsed after 4.719 us at 4.895 us
2	4.943 us	Failed	PM_LC_TIMER Timer elapsed after 4.680 us at 9.623 us
3	9.759 us	Failed	PM_LC_TIMER Timer elapsed after 4.840 us at 14.599 us

You can view only the failed test results by clicking the **Exclude Passed result** checkbox as shown above.

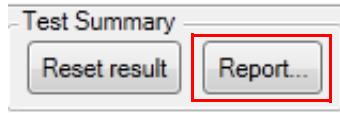
If you want to clear the test results from the **Test Assertion** tab after a series of compute operations, click **Reset result** in the **Test Summary** section.

The screenshot shows the 'Performance Overview' tab with the 'Test Summary' section. The 'Reset result' button is highlighted.

When you click **Reset result**, the **Cumulative Run Count** shows the default value "0". You can then recompute the test cases for the most recent test result.

Viewing the Collective Summary

For viewing the test summary of all the test assertions over a number of compute operations, click **Report** in the **Test Summary** section.



The **Test Assertion Report** window is displayed.

Test Assertion Report

Save As HTML...

KEYSIGHT TECHNOLOGIES

Test Report

Overall Result: FAIL
7 out of 17 tests executed. 3 passed 4 failed

Test Assertion Summary

LA Configuration	
Version	91.21.1083
File	C:\Users\bimishra.KEYSIGHT\Desktop\ala files\test assertion\TestAssetion13.ala
File Description	
Last Run	04/22/2015 11:05:52

No. of Compute Operations → **Total run count: 2**

ID	Name	Run Count	Passed	Failed	Not executed
7.2.4.1.1 #4	Upon entry to U0, after starting the PENDING_HP_TIMER and CREDIT_HP_TIMER timers, a port shall	2	0(0%)	0(0%)	2(100%)

The test report is a collective summary of all the individual test results displayed for a series of compute operations. In the test report,

- The **Overall Results** section displays the total number of instances the selected test cases were encountered over a series of compute operations. This section also displays whether or not the test assertion has passed over a series of compute operations.
- The **Test Assertion Summary** section summarizes the test results for a series of compute operations. For example in the above screen, seventeen test cases were tested twice in which seven test cases were encountered in the compute operation. Out of these seven test cases, four test cases failed the desired assertions. Since at least one test case has failed the assertion, the overall test assertion has failed.

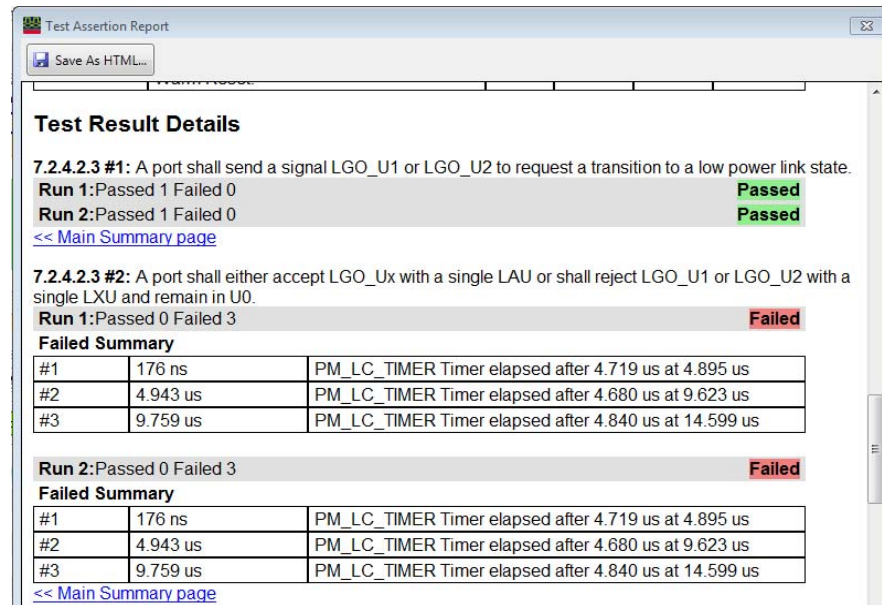
Overall Result: FAIL

- The **Test Result Details** shows the test result for the most recent compute operation or a collective summary of the individual test results over a series of compute operations.

NOTE

You can view the detailed test report of the most recent compute operation without viewing the individual test results by deselecting the **Enable Cumulative Run** check box and then computing the required test cases.

For example in the below screen, the test assertion failed in both the instances when the test case having reference ID 7.2.4.2.3 #2 was encountered twice. You can view the individual failed summary reports for both the runs and assess the causes of failures.



You can save the detailed result in HTML format on your computer by clicking **Save As HTML**.

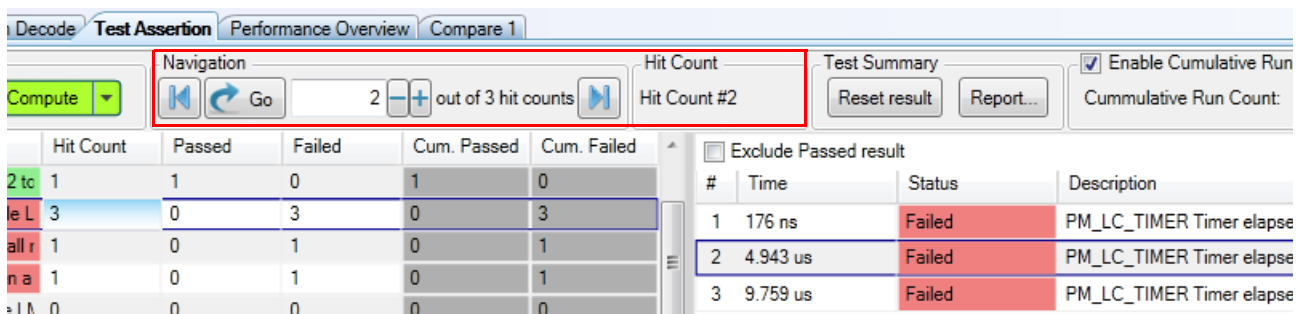






Navigating Through Captured Packets from Test Results

You can navigate to the individual first captured packets which the selected test cases encountered when the test assertion started.

- 1 Click a test case on the left pane of the **Test Assertion** tab.

The navigation bar in the **Test Assertion** tab now displays the total number of instances the selected test case was encountered in the compute operation.



- To view the first encountered instance of the selected test assertion in the computed data, click  button in the navigation bar. Click  to go to the last encountered instance of the selected test assertion.
- To sequentially move through the encountered instances of the selected test assertion in the computed data, use the   buttons in the navigation bar. The **Hit Count** section displays the current instance number of the individual test result.
- To go to a specific encountered instance of the selected test assertion in the computed data, specify the hit count in the text box displayed in the navigation bar and click **Go**.

You can also double-click the required test result in the right pane of the **Test Assertion** tab to navigate to the first packet which the selected test case encountered in the Protocol Viewer in that particular time duration.

Direction	Sequence Number	SSIC Packet	Type	SubType	Payload	Time
		Start of Burst				0 s
SSIC-101B		Start of Burst				168 ns
		LGO_U3		LGO_U3, LGO_U3		176 ns
SSIC-101B		SKP Ordered Set				192 ns
SSIC-101B		SKP Ordered Set				208 ns
SSIC-101B		SKP Ordered Set				224 ns
SSIC-101B		SKP Ordered Set				240 ns
		LPMA				304 ns
		Start of Burst				4.895 us
SSIC-101B		Start of Burst				4.935 us

Transaction Decode | **Test Assertion** | Performance Overview | Compare 1

Navigation

Go

1

out of 3 hit counts

Hit Count #1

Test Summary

Reset result

Report...

Enable Cumulative Run

Cummulative Run Count: 1

Exclude Passed result

#	Time	Status	Description
1	176 ns	Failed	PM_LC_TIMER Timer elapsed after 4.719 us at
2	4.943 us	Failed	PM_LC_TIMER Timer elapsed after 4.680 us at
3	9.759 us	Failed	PM_LC_TIMER Timer elapsed after 4.840 us at

	Hit Count	Passed	Failed	Cum. Passed	Cum. Failed
U1 or LGO_U2 to	1	1	0	1	0
Ux with a single L	3	0	3	0	3
_U2, a port shall r	1	0	1	0	1
Recovery when a	1	0	1	0	1
I send a single LM	0	0	0	0	0
port shall not sen	1	1	0	1	0
J in response to I	3	0	3	0	3

150

Keysight U4431A MIPI M-PHY Protocol Analyzer User Guide

Navigating Between Test Cases and their Associated Packets

You can double-click a specific test case row in the left pane of the **Test Assertion** tab to navigate to the first packet which the particular test case encountered in the Protocol Viewer. Doing so, highlights the first captured packet encountered by the selected test case in the upper pane of the Protocol Viewer.

For example in the following screen, double-clicking the test case having reference ID 7.2.4.2.3 #6 highlights the Start of Burst packet in the Protocol Viewer, which is the first captured packet encountered by this test case.

Packets

Sample Number	Direction	Sequence Number	SSIC Packet	Type	SubType
8			SKP Ordered Set		
6	SSIC-101A		SKP Ordered Set		
9			SKP Ordered Set		
7	SSIC-101A		SKP Ordered Set		
8	SSIC-101A		LGO_U3		LGO_U3, LGO_U3
10			Start of Burst		
9	SSIC-101A		Start of Burst		
10	SSIC-101A		LGO_U3		LGO_U3, LGO_U3
11			SKP Ordered Set		
12			SKP Ordered Set		

Test Assertion

Data Range: Beginning Of Data to End Of Data [Compute]

Navigation: Go 1 out of 1 hit counts

Hit Count: Hit Count #1

ID	Name	Hit Count	Passed	Failed	Cum. Passed	Cum. Failed
7.2.4.2.3 #1	A port shall send a signal LGO_U1 or LGO_U2 to	1	1	0	1	0
7.2.4.2.3 #2	A port shall either accept LGO_Ux with a single L	3	0	3	0	3
7.2.4.2.3 #3	Upon sending LGO_U1 or LGO_U2, a port shall r	1	0	1	0	1
7.2.4.2.3 #6	A port shall initiate transition to Recovery when a	1	0	1	0	1
7.2.4.2.3 #7	Upon receiving LAU, a port shall send a single LM	0	0	0	0	0
7.2.4.2.3 #8	Upon issuing LAU, or LPMA, a port shall not send	1	1	0	1	0

Test Summary

#	Time	Status
1	9.623 us	Failed

9 Viewing Offline Performance Summary

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Customizing Charts / 175

In the Protocol Viewer window, you can generate and view the performance summary from the PCIe and SSIC trace that you captured using the U4431A Analyzer module. This chapter describes how you can compute and view offline performance summary from the captured PCIe/SSIC data.

Offline Performance Summary – Overview

The *Performance Overview* tab in the Protocol Viewer window allows you to perform post processing on the captured MPCle/SSIC traffic to generate an offline performance summary of bus utilization. This tab presents statistics for various performance parameters in tabular as well as charts form.

In this tab, you define the range of captured data for which performance summary is to be generated. The software decodes transactions from this specified range of data and then computes performance statistics and charts from the decoded transactions. The specified range of trace data is sampled and statistics is computed from these samples to generate charts for each performance parameter.

Performance statistics are displayed separately for upstream and downstream link directions.

NOTE

You do not need connectivity to the U4431A hardware module to generate performance summary from a captured trace.

Performance Overview Tab


You use the *Performance Overview* tab displayed in the lower pane of the Protocol Viewer to compute and view performance summary.

This tab displays the performance data in the following two panes.

Pane	Description
Performance Statistics pane	This is the left pane that displays a list of categories and the performance parameters for each of the categories for which statistics will be generated and displayed. For each of the parameters that you select, statistics are displayed based on the link directions (upstream as well as downstream).
Performance Charts pane	This is the right pane and displays a performance chart for selected performance parameters listed under the category.



NOTE

If the Performance Overview tab is not visible, click the  button displayed at the top of the Protocol Viewer window.

The Overlay View – At a Glance

The information in the Performance Overview tab is presented using the Overlay view. The Overlay view allows you to select the series that you want to be displayed in the chart area. You can select the series by clicking the required performance parameter check box listed under Statistics tree/table on the left. This view displays graphs for the selected series in a single chart with a black background. It displays the Band Chart area where you can view the MSI Write, Msg Assert_INT and Msg Deassert_INT and error packets occurrence plotted in the chart. It also displays multiple Y-axis which you select in the **Setup** dialog box. To know more about how to select and interpret multiple Y-axis, see [“Showing/Hiding multiple Y-axis”](#) on page 158. Following screen displays the Overlay view after you select and compute a range:



In the above screen, graphs for the Update_FC_P series (represented by dotted lines) in both directions are displayed. The blue dotted line represents the graph for Update_FC_P series in upstream, whereas, the red dotted line represents the graph for Update_FC_P series in the downstream direction.

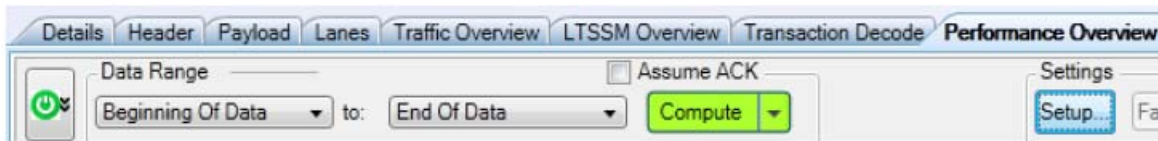
Configuring and Computing Offline Performance Summary

Before you Start

- Ensure that the data in the required direction(s) is already captured and available in the Logic and Protocol Analyzer GUI for performance summary computation. You may save the captured data in a Logic Analyzer configuration (.ala) file and access this data offline for performance summary computation.

Computing Offline Performance Summary

- 1 Click the **Performance Overview** tab in the Protocol Viewer window.



- 2 In the **Data Range** groupbox, specify the start and end points of the captured PCIe/SSIC data for which you want to compute performance summary. Only the specified range of data is analyzed to compute performance. Following options are available for setting this data range.
 - **Beginning and End of data** - This data range selection ensures that performance summary is computed for the entire trace.
 - **Begin Extent and End Extent** - The Extent markers indicate the pan/zoom extents. The software automatically places extent markers on the beginning and end of the current pan/zoom extents defined in charts. When you change the pan/zoom extents in charts, the extent markers are automatically moved to changed extents.
 - **Trigger** - Selecting Trigger in the data range ensures that performance summary is computed from the point where the U4431 module's trigger condition was met.
 - **Markers** - Selecting markers in the data range ensures that performance summary is computed for the specific portion of PCIe traffic defined by markers. Refer to ["Defining Markers for Setting the Computation Range"](#) on page 157 to know more.
- 3 For generating performance statistics, only complete transactions from the captured trace are used. Select the **Assume ACK** checkbox to instruct the software to assume ACKs for the transactions in which only ACK is missing. Assuming ACKs, therefore ensures that the transactions with missing ACK are considered complete and used in performance summary computation. You may want to use this Assume ACK option in situations such as when you have filtered ACKs while data capture to make more memory available to TLPs.
- 4 For producing the performance statistics, transaction of only error free packets from the captured trace are taken into account. The packets containing error are ignored while generating the performance statistics. Select the **Include Errors** check-box to let the software include error packets and compute the performance statistics including these packets. Selecting Include Errors checkbox increases the computation time of Performance Overview. It creates a series that is displayed in the Band Chart with an element for each packet containing error in the list.
- 5 Click the **Compute** button displayed with the Data Range fields. The **Compute This** and **Compute All** are the two options available with this button. **Compute All** allows you to compute traffic overview statistics, decoded transactions, and offline performance summary for the captured packets by a single click of this button. **Compute This** allows you to compute only offline performance statistics from the captured packets. When you click **Compute**, then also compute only offline performance statistics.

On clicking Compute, first the transactions are decoded from the specified data range of PCIe trace. Then, statistics and charts are computed from these decoded transactions and results are displayed.

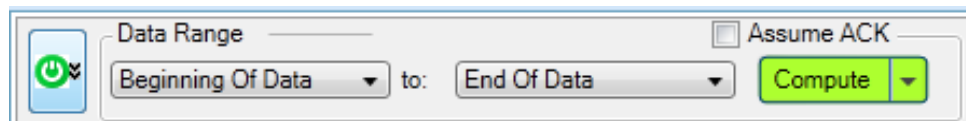
Defining Markers for Setting the Computation Range

If the captured PCIe/SSIC traffic is too large and you want to view performance summary for a specific portion of this traffic, then you can limit the computation range by defining start and end markers in the traffic.

To define markers

- 1 From the upper pane of the Protocol Viewer, right-click the row in the captured traffic that should act as the starting point for performance summary computation.
- 2 Select **Place Marker** from the displayed context menu and then select an existing marker or click **New Marker** to define a new marker at this point.

Once markers are defined, these are available for selection in the **Data Range** group box of the **Performance Overview** tab.



Saving the Computed Performance Summary Data

Once you computed the performance summary data, you can save the performance configurations along with the captured traffic in a logic analyzer .ala configuration file. On saving, the settings such as zoom, pan, sample rate, markers, chart type, and chart order that you configured in the Performance Overview tab are also saved in the .ala file along with the trace data. Loading this .ala file retrieves these settings and computes and displays performance summary based on the saved settings.

To save the performance summary settings

- 1 Click **File > Save as**.
- 2 In the **Save As** dialog box, specify the name of the file.
- 3 Ensure that the **Standard Configuration (*.ala)** option is selected as the file type and **All Data and Setup** is selected in the **File Options** group box.
- 4 Click **Save**.

To access and view previously saved performance summary settings

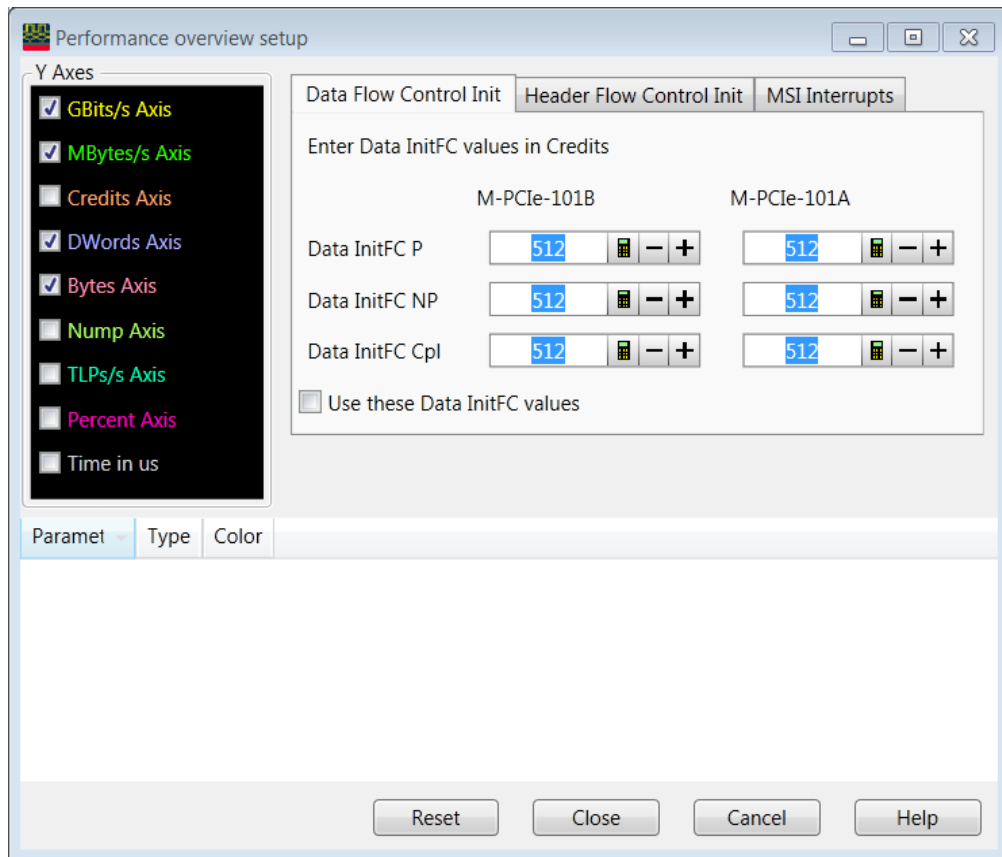
- 1 Click **File > Open**.
- 2 In the **Open** dialog box, navigate to the **Standard Configuration (*.ala)** file in which you saved the data.
- 3 Click **Open**.

Defining Chart Settings

You can define settings for the overlay chart in the **Setup...** tab of the **Settings** section and change the appearance of the displayed chart.

To access the Setup dialog box

Click the **Setup...** button under the **Settings** section. The Setup dialog box appears as shown below.

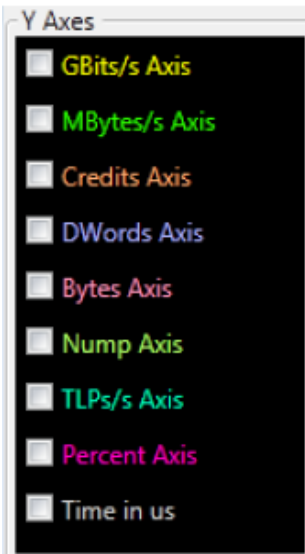


The setup details that you specify in this dialog box helps you change the way series and axes are displayed in the overlay graph. For example- you can change the graph display type and color of the displayed series or you can show or hide the Y-axis by selecting or de-selecting the respective Y-axis checkbox.

The following is the list of tasks that you can do using the **Setup** dialog box:

Showing/Hiding multiple Y-axis

Select or de-select the Y-axis checkbox to show or hide the Y-axis displayed in the chart.

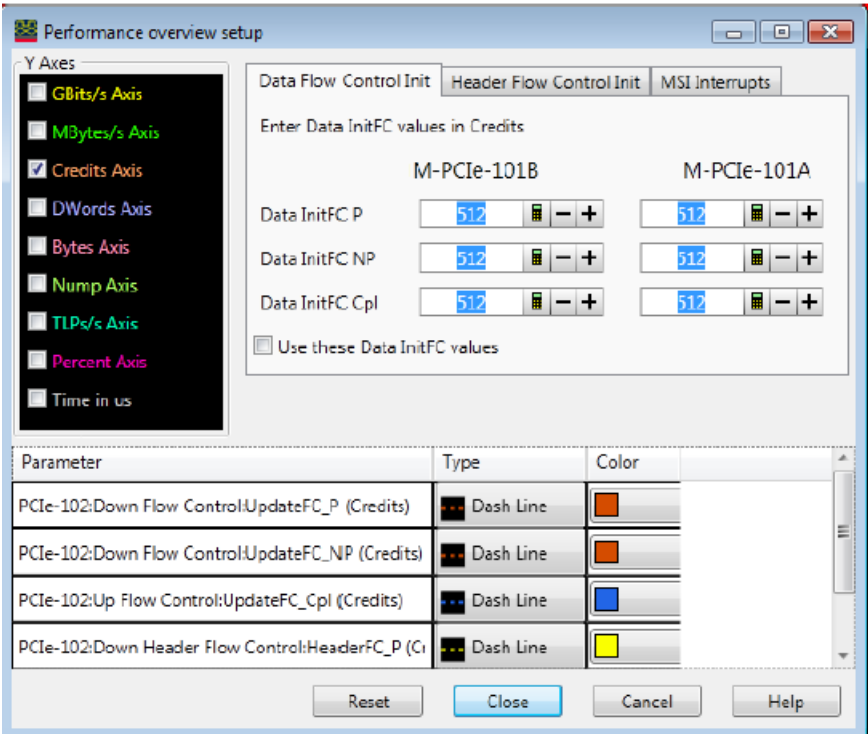



NOTE

Showing or hiding a particular Y-axis does not affect how series are computed or displayed in the chart.

Specifying InitFC values

You can initially define the maximum flow control credit limits by entering InitFC values for posted, non-posted, and completion transactions in both directions (Upstream and Downstream). You can do this separately for Data and Header flow control Init values.



Click  button displayed with the InitFC P, InitFC NP, and InitFC Cpl fields to open the calculator and input values. Select the checkbox 'Use these InitFC values' to use the InitFC values defined by you when you are computing and interpreting offline performance summary. In case InitFC values are not defined or 'Use these InitFC values' field is not selected, the minimum and maximum credit levels for the captured trace is automatically computed and flow control mechanism works on the basis of those calculated values. The minimum and maximum credit levels varies from one trace to other depending upon the size of the trace.

NOTE

InitFC values are applicable only for Flow Control performance statistics category. You can use InitFC values for posted, non-posted and completions transactions only when you are computing flow control credits.

Specifying Min/Max value for MSI and MSI-X Address

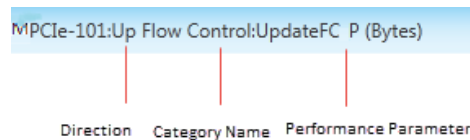
You can specify the minimum and maximum values for the Message Signaled Interrupt (MSI/MSI-X) address in the **MSI and MSI-X Address** row for which you want Interrupt to be generated and plotted in the Band Chart.

Selecting this checkbox will generate interrupts for the specified address which you can view in the Band Chart.

MSI Interrupts		
	Minimum (HEX)	Maximum (HEX)
<input checked="" type="checkbox"/> MSI Address	FEE00000	FEEFFFFFFF
<input checked="" type="checkbox"/> MSI-X Address	1000000000401000	1000000000401FFF

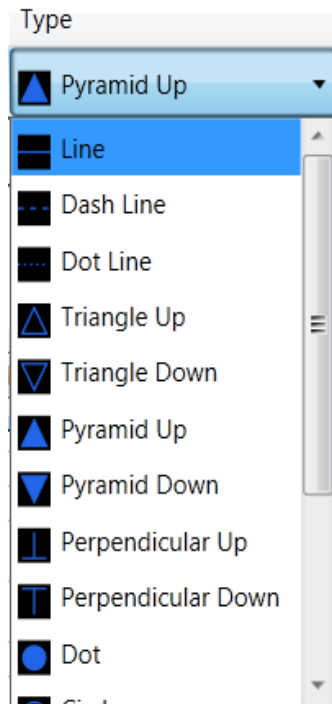
Understanding Parameter Column

The series which you select by clicking their respective check-box in the Statistics Tree/Table of the Performance Overview Tab is displayed here in the **Parameter** column of the **Setup** dialog box. The Parameter is the full name of the series and is comprised of the Direction (Up or Down), the Category name picked from the Performance Statistics tree/table, and the checked Series name.



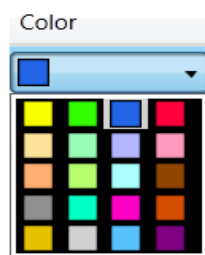
Changing the appearance of the graph

You can change the appearance of the graph representing the respective series listed in the **Parameter** column of the **Setup** dialog box. To do so, click the drop-down in the **Type** column and select the graph type you want to change with.



Changing the color of the Graph Line

You can also change the color of the graph line by picking color of your choice from the color palette. To do so, click the **Color** drop-down to display the color combo box consisting of different colors, and select the color of your choice.



- Once all the settings are defined in the Setup dialog box, click the **Close** button to apply those settings and exit from the dialog box.
- Click the **Reset** button to restore the last settings done previously when you opened the Setup dialog box. This resets the Axis, Types and Colors to their previous state.
- Clicking the **Cancel** button allows you to exit from the dialog box without making any changes.

Interpreting the Performance Summary Results

Performance Statistics panes

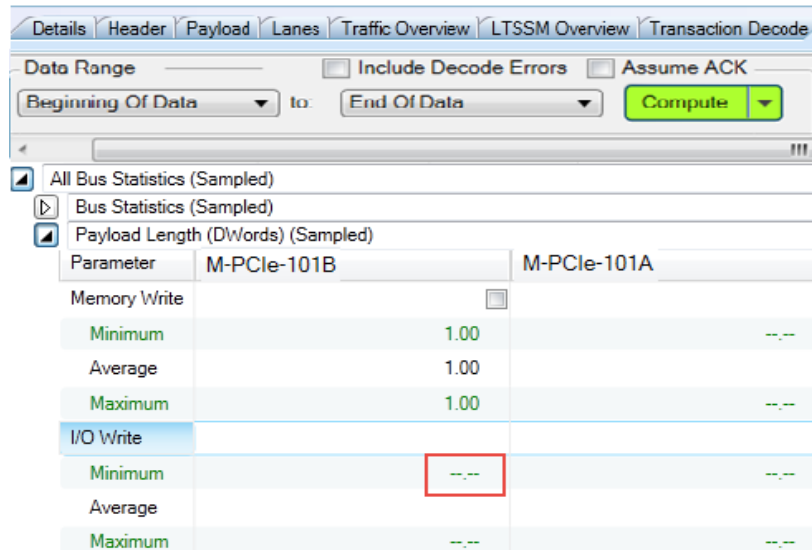
The **Performance Statistics** pane displays a hierarchical list in which various performance statistics are categorized in groups and sub-groups.

Clicking a group or category in this pane displays the link-wise statistics for associated performance parameters. The following table briefly describes the performance statistics displayed for categories

Category	Performance Statistics Displayed
Flow Control	Tracks and computes the available flow control credits for a bidirectional data trace and helps in analyzing the flow of data. To know more refer to the topic "Flow Control" .
Bus Statistics	<ul style="list-style-type: none"> ▪ Band width (GBits/s): Shows the number of non- idle symbol bits transferred per second. ▪ Data Throughput (MBytes/s): Shows the number of TLP payload bytes transferred per second. ▪ Payload Length (DWords): Shows the minimum, maximum, as well as average payload size for TLPs. ▪ TLP Count (TLPs/s): Shows the number of TLPs transferred per second. ▪ Link Efficiency (%): Shows the efficiency of the link. It is calculated as Symbol time for payload / (Symbol time for all DLLP + Symbol time for all TLP + Symbol time for OS). ▪ Link Utilization (%): Shows the percentage of non- idle symbols in total number of symbols transferred. ▪ TLP Utilization (%): Shows the percentage of TLP symbols in total number of symbols transferred.
Transaction Performance	<ul style="list-style-type: none"> ▪ Non- Posted Transactions: Shows the minimum, maximum, and average values of the following performance parameters for all non- posted transactions. <ul style="list-style-type: none"> • Response Time (ns): It is same as the duration of the transaction. • Latency (us): It is the time duration between the end of a request transaction and the arrival of its first completion. • Throughput (MBytes/s): It is the length of complete data divided by the response time. ▪ Posted Transactions: Shows the minimum, maximum, and average values of the following performance parameters for all posted transactions. <ul style="list-style-type: none"> • Response Time (ns): It is same as the duration of the transaction. • Throughput (MBytes/s): It is the length of complete data divided by the response time.

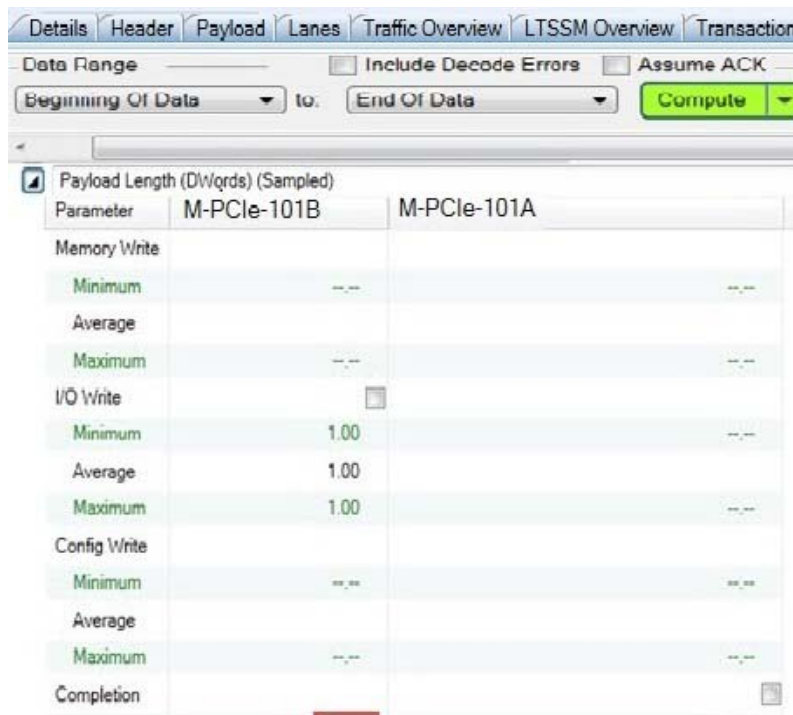
Some important points while viewing and interpreting performance summary:

- A --.-- value displayed in a statistics tree/table for a performance parameter indicates that the packet(s) required to generate that statistics is not found in the trace. It signifies 'no data' received for generating the statistics. Consequently, the chart corresponding to such a performance parameter contains no data and is therefore not displayed in the charts pane. Such a chart gets displayed only when the required packets are found while computing performance summary to generate data for the chart.



For calculating the statistics for the **Completion** parameter, only *Completions with Data* packets are included. Any *Completions without Data* packets found in the trace are ignored for this parameter's statistics calculations.

For instance, in the following screen, the *Completion with Data* packets are used for calculating the *minimum*, *maximum*, and *average* payload size for Completions. In the absence of *Completion with Data* packets in the trace, the *Completion* statistics is shown --.--.



- Some performance parameters are displayed in blue color. This indicates that the navigation to the associated MPCle packet is applicable for that performance parameter. To know more, refer to ["From the Performance Statistics pane"](#) on page 173.

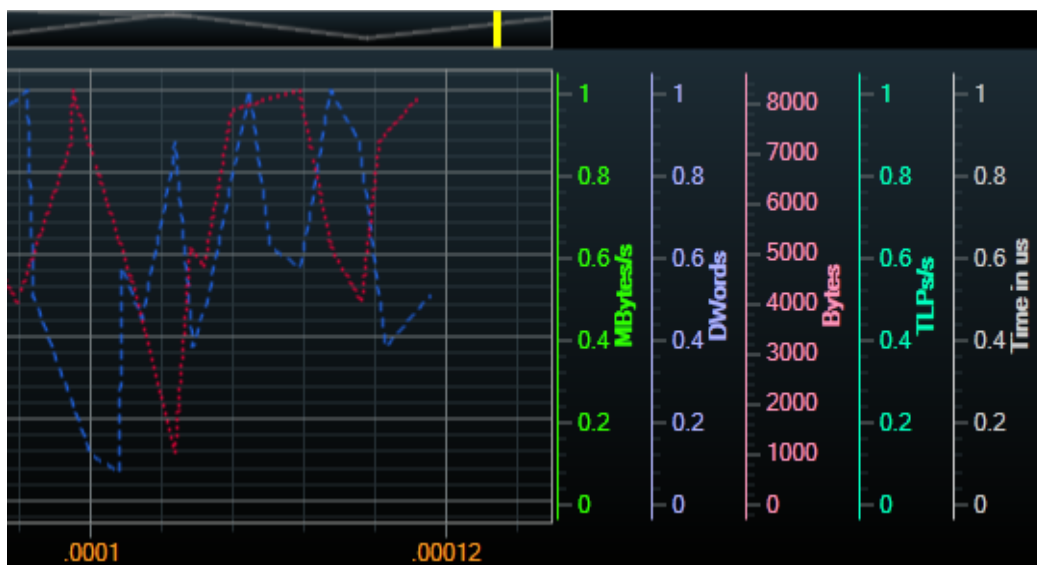
Parameter	M-PCle-101B	M-PCle-101A
UpdateFC P (Bytes)	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
Minimum	0.00	0.00
Average	4444.51	4571.28
Maximum	7712.00	7272.00

Charts pane

This pane displays charts for all the selected performance parameters listed in the statistics tree table.

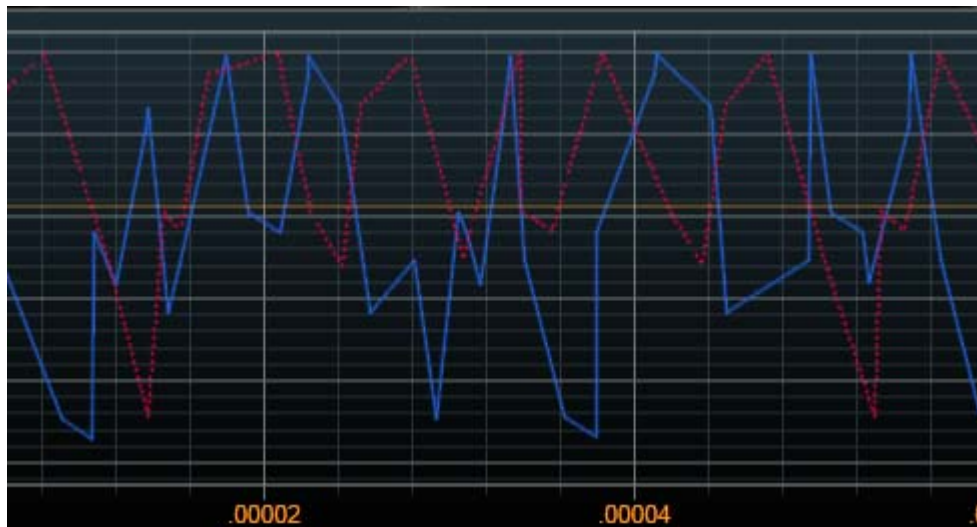
Viewing Multiple Y-axis in the Chart Pane

The chart pane displays multiple Y-axis if you select multiple Y-axis checkbox in the **Setup...** dialog box. To know more, see ["Showing/Hiding multiple Y-axis"](#) on page 158.



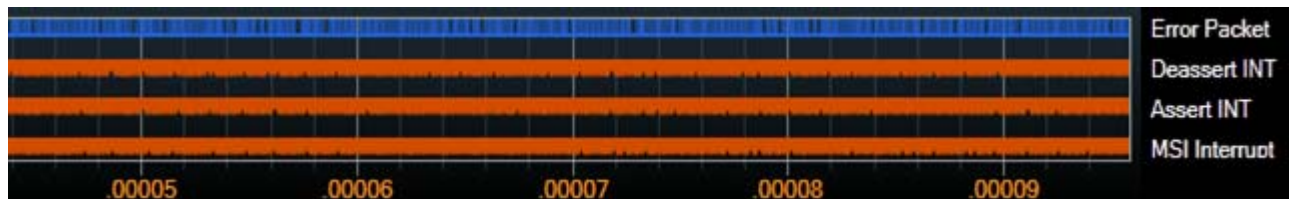
Viewing Overlay Charts and defining Color code for graphs

The Chart pane displays Overlay chart for selected performance parameters listed in the categories pane. You can define different color codes to display the performance data for downstream and upstream directions in charts. To accomplish this, click the **Setup...** button under the **Settings** section. Then, click the **Color** drop-down to select the color from the color palette. To know more, refer ["Changing the color of the Graph Line"](#) on page 161.



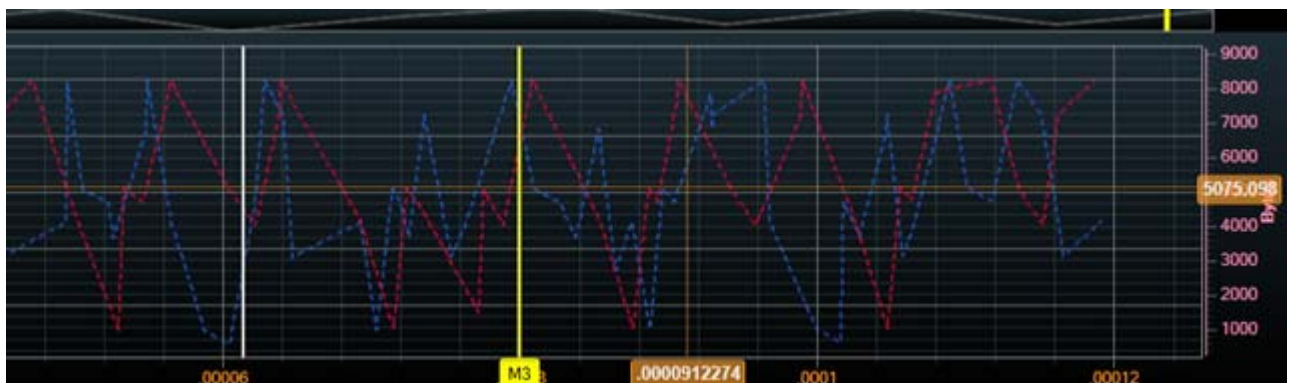
Viewing Band Chart

The lower section of the Chart pane displays the Band Chart. You can view MSI Write, Msg Assert_INT and Msg Deassert_INT and error packets in the Band chart area of the Chart pane.



Viewing X and Y axes values in a chart

Hovering the mouse over a chart location displays the applicable values of X-axis and Y-axis for that location.



Flow Control

Flow Control computes and displays the available flow control credits from the data trace that has a bidirectional traffic. It presents flow control statistics and graphs and helps in analyzing the flow of data. Flow control credit level is determined by the Writes and Completions in one direction and the UpdateFC packets in the other direction. The data series created for the flow control charts contain a point for every packet that is a part of that series. This is in contrast to all other charts which represent a group of packets in a sample period. Flow control credit levels are represented by UpdateFC series. The performance parameters for the Flow Control are explained below:

- **UpdateFC_P (Bytes)** - Shows the minimum, maximum, as well as average flow control credit levels for posted transactions.
- **UpdateFC_NP (Bytes)** - Shows the minimum, maximum, as well as average flow control credit levels for non-posted transactions.
- **UpdateFC_Cpl (Bytes)** - Shows the minimum, maximum, as well as average flow control credit levels for Completions data.
- **Memory Read (Bytes)** - Charts the size of Memory Read requests in each direction (X and Y axes).
- **Memory Write (Bytes)** - Maps the size of Memory Write and MsgD packets in each direction.
- **Memory_CplD (Bytes)** - Charts the size of Memory completion with data packets in each direction.
- **Inst. CplD and Inst. Write (Mbytes/s)** - The instantaneous completion with data (Inst. CplD) and instantaneous write (Inst. Write) show Instantaneous Bandwidth in each direction. Instantaneous Bandwidth is defined as the number of data bytes (in MegaBytes) in the packet divided by the time (in seconds) since the end of the previous data carrying packet to the end time of the current data carrying packet.
Each packet carrying data is represented by a point in these series.
- **Interrupts**- Shows the location in the graph where MSI Write, Msg Assert_INT and Msg Deassert_INT occurred. This is displayed in the Band chart area of the chart pane.
- **Error Packet**- Shows the location in the graph where packets with any error occurred. This is displayed in the Band chart only when the Include Errors checkbox is enabled under Data Range heading.

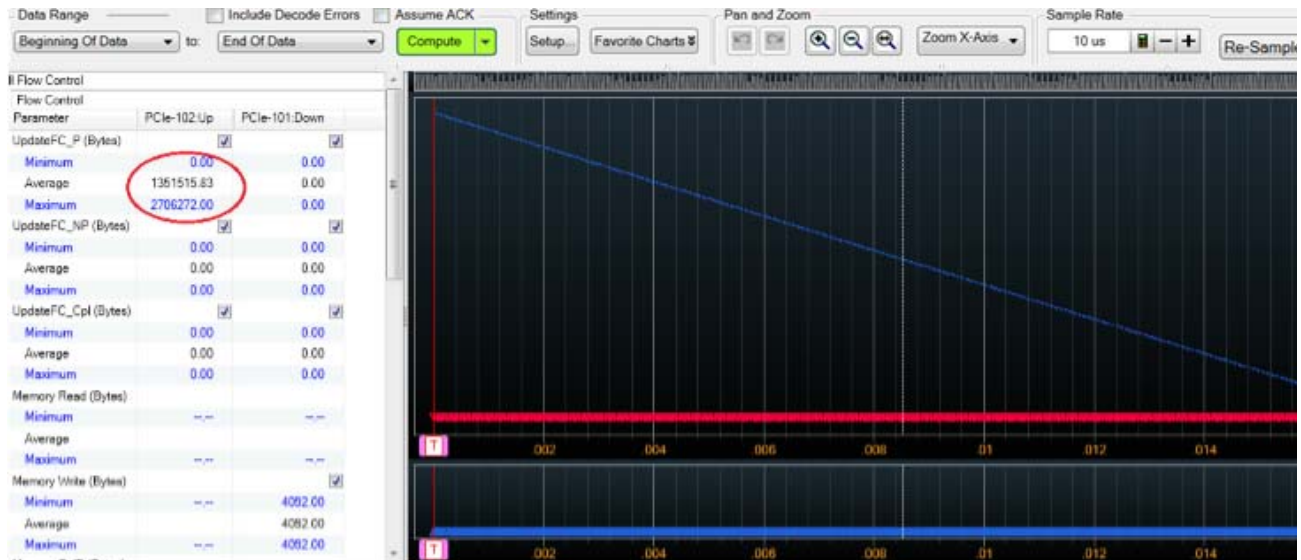
Flow Control- Examples

The following examples elaborate the usage of Flow Control feature:

Example-1

The flow control charts represent the computed flow control credit level, which is meant for how many bytes the receiver is ready to receive. When the data is sent in a higher volume than the receiving capacity of the receiver, there will be a continuous slow movement of data from transmitter to receiver. This sometimes also leads to data loss. In such scenarios, you can use the Flow Control feature to display the credit limits of the data to be sent and the data received by the receiver.

In this example, 4092B (1023 DW) packets of data are being sent in the downstream direction and the link partner is updating credits in increments of 4096B. In the following screen, a steep decline in the blue line clearly indicates the situation that FC credit is being updated at higher rate than the rate at which data is being posted. The blue line represents the computed UpdateFC_P regulating against the red line representing the corresponding Memory Write packets in the trace.

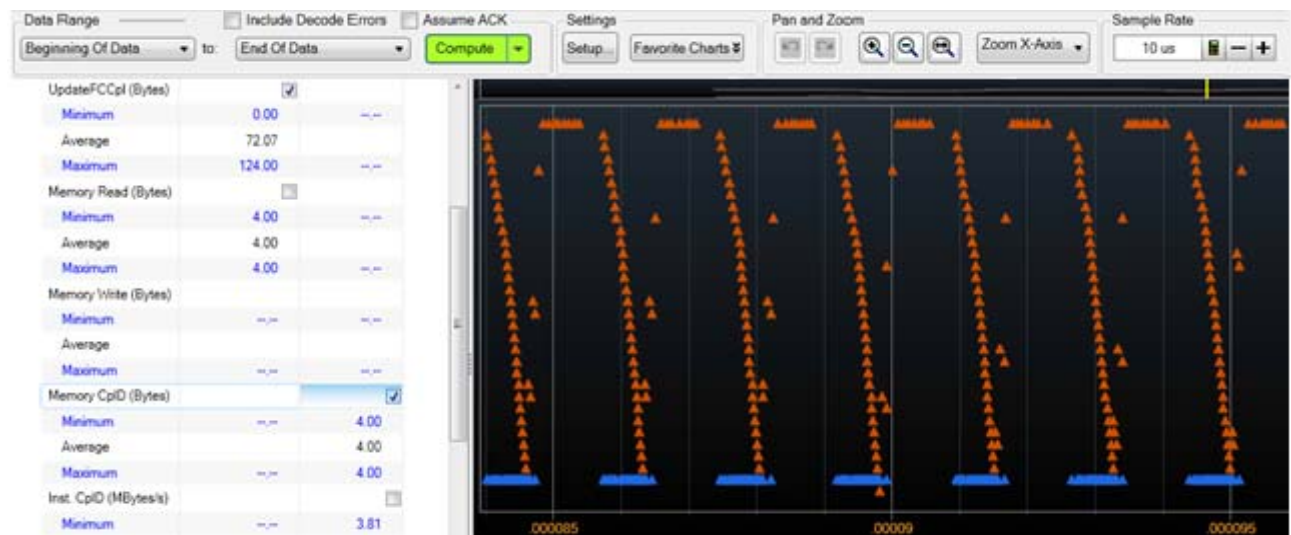


The UpdateFC_P Flow Control credit level is reduced by Memory Write packets coming from the root occurred in the trace. As displayed in the above graph, the Maximum is near the beginning of the trace and the Flow Control credit level continuously drops down and reaches zero on the right side of the trace.

This too many credits difference may indicate problem in the credit update behavior of the DUT.

Example-2

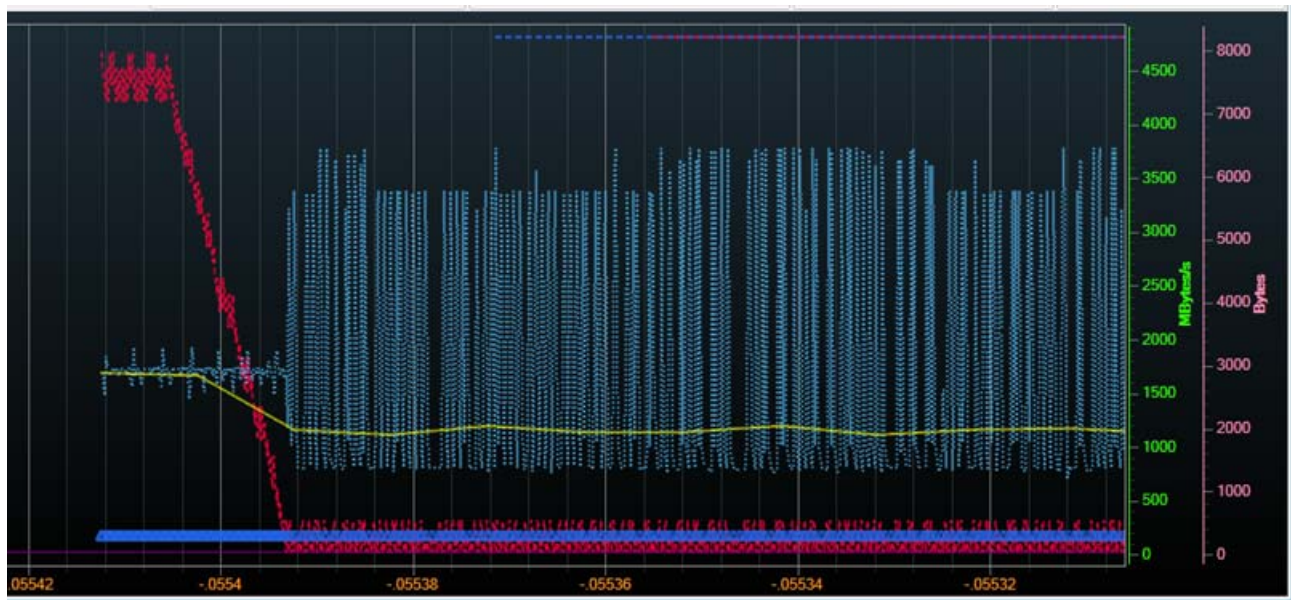
In the following screen, when memory completion data (Memory CplD) is being sent, the UpdateFC_Cpl performance parameter computes and displays minimum, maximum, as well as average flow control credit levels for Completions data. The blue and orange Pyramid Up chart line represents Memory CplD and UpdateFC_Cpl respectively. The UpdateFC_Cpl credit starts from an initial counter value and keeps on updating its credit limit till all the packets are sent and received.



As displayed in the above graph, the UpdateFC_Cpl graph line keeps showing the credit fluctuation regulating against the corresponding Memory Completion packets coming from root that occurred in the trace. The drop in credit level shows how many packets are left to be transmitted and helps in regulating the flow of data packets. When all packets are transmitted and received, the FC level changes to zero counter value. It remains constant till any further credits are added into account. Once the FC level is incremented with some value, the same mechanism is repeated till all the packets are transmitted and received and FC level again comes to a zero value.

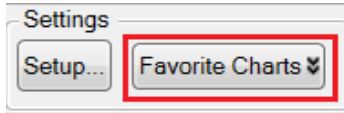
Example-3

In the start of the DMA buffer transfer, the link credits are quickly exhausted and reaches to zero counter value because the buffer transfer runs at a higher rate in comparison to the rest of the data transfer. The quick drop in credit level is caused by the chain of link buffering. In the start, the data write or completion capacity remains higher than the sink capacity but lower than the link capacity. The data transfer at higher rate only happens while the link buffering capacity can sustain it.



Saving the Favorite Overlay Chart

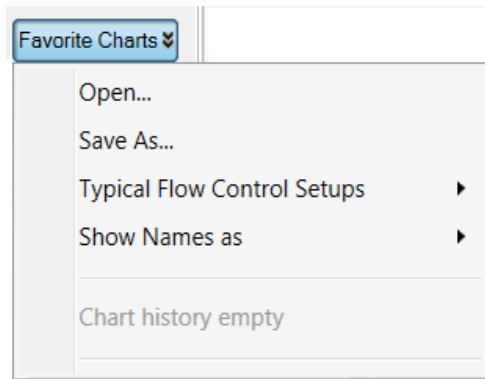
In the Performance Overview tab, the **Settings** section has a **Favorite Charts** button which allows you to save your favorite charts and open the previously saved overlay charts.



The overlay chart is saved along with all the settings that you made to the graph displayed in the chart pane of performance tab (such as selecting or deselecting various performance parameters in the Categories pane, zooming or panning the chart, making changes in the Setup dialog box etc.).

To save the Overlay Chart

- 1 Click **Favorite Charts** button > **Save as**.



- 2 In the Save As dialog box, specify the name of the chart. Ensure that *.cfv (Chart Favorite) option is selected as the file type.
- 3 Click **Save**.

To view previously saved Overlay chart

- 1 Click **Favorite Charts** button > **Open**.
- 2 In the Open dialog box, navigate to the *.cfv file name by which you saved the Overlay chart.
- 3 Click **Open**.

Navigating Through the Performance Summary Results

Navigating Through a Chart

By Using Pan option

To navigate through a chart horizontally, that is X-axis, click the **Pan X-Axis** button displayed at the top of the charts pane.

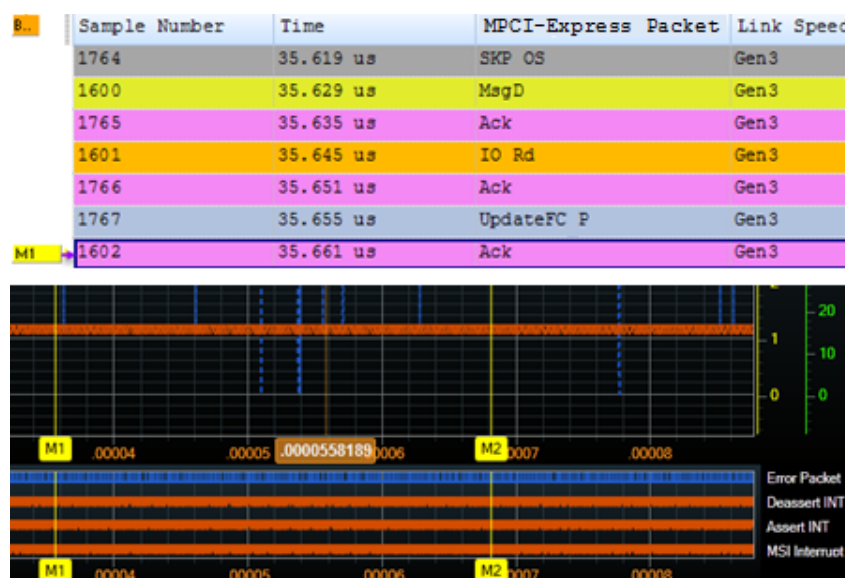
To navigate through a chart horizontally and vertically, that is both axis, click the **Pan Both Axis** button displayed at the top of the Charts pane. You can then drag the chart up, down, left, and right.

Navigating Between Performance Statistics and Associated MPCle Data

By placing markers in charts

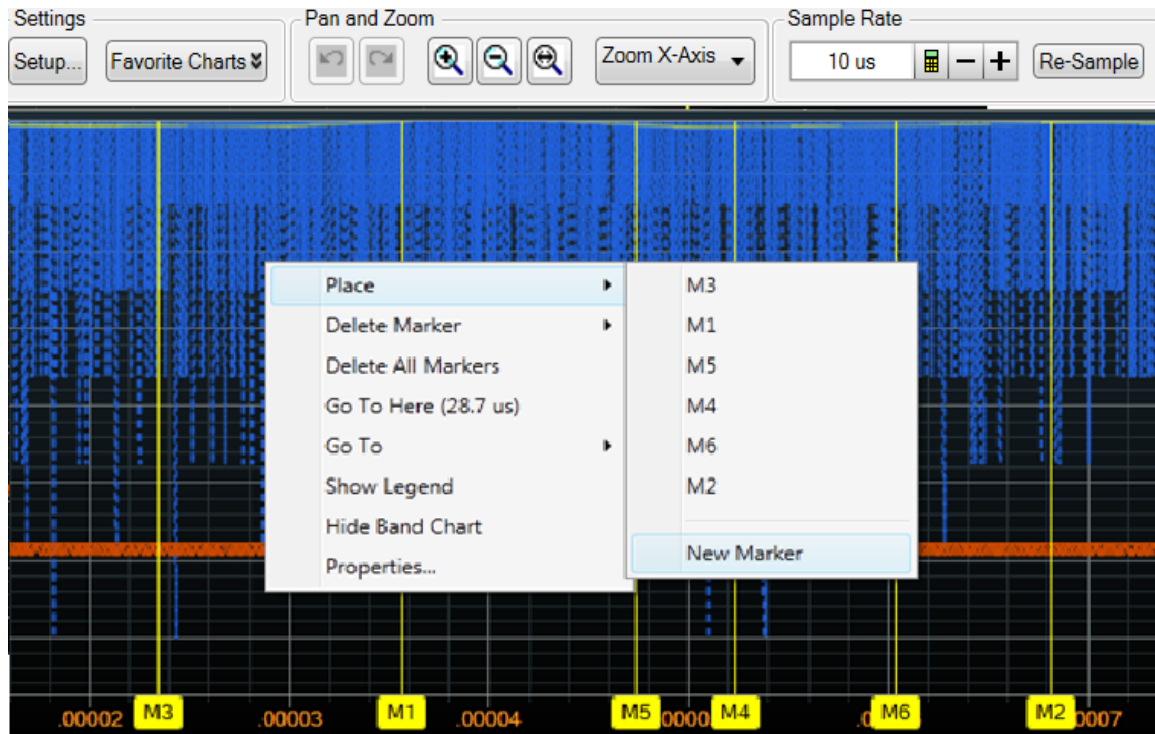
You can place markers in charts and use these markers to navigate to the MPCle packet associated with the chart location at which you placed a marker. Markers placed in charts are correlated to markers displayed in the trace data in the upper pane of the Protocol Viewer.

This type of navigation is particularly useful when you notice a sudden variation in a chart and want to navigate to the exact trace position that corresponds to that chart location.



To place a marker in charts

- 1 Double-click the location in the chart at which you want to place a marker. A new marker is added to that chart location and the corresponding trace location in the upper pane (trace view). Alternatively, right-click the chart location where you want to place a marker. Then select **Place > New Marker** or select an existing marker to place that marker at the current location.



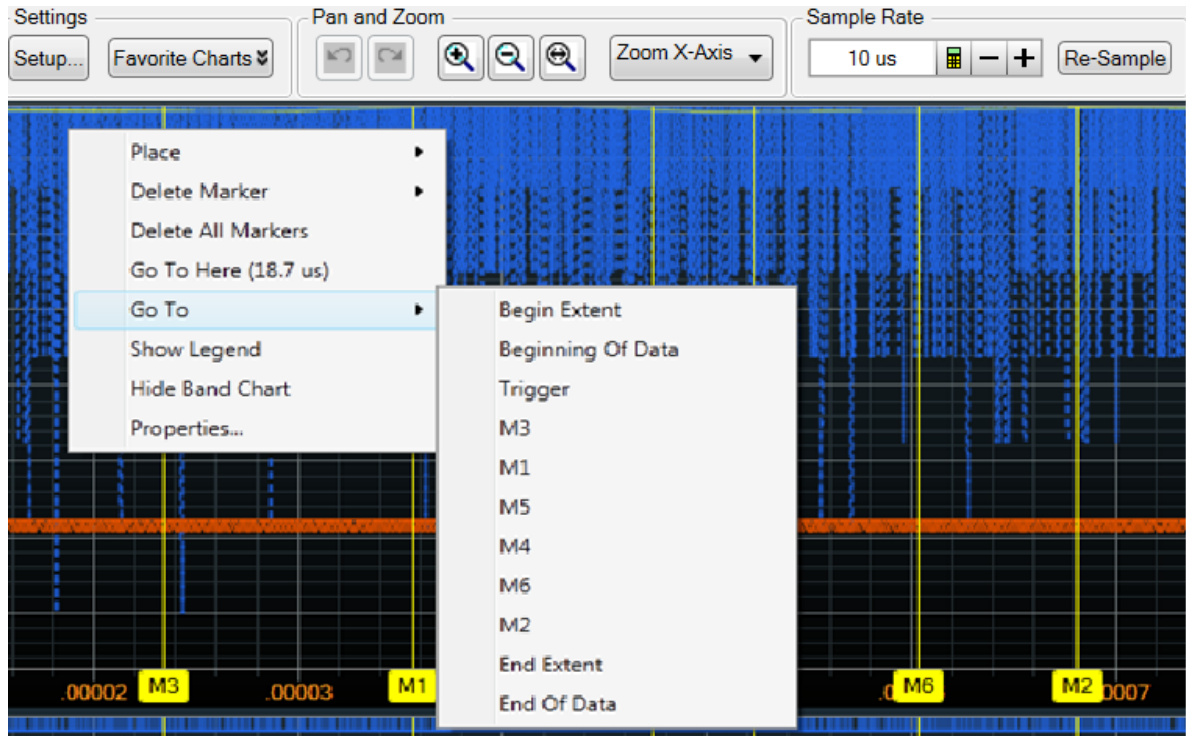
To navigate to a particular marker placed in charts

In situations when you have placed multiple markers in charts, you may want to navigate to a particular marker and its associated trace position in the upper pane. To do so, right-click anywhere in a chart, select **Go To** and then select the marker to which you want to navigate.

On doing so, the chart display moves to the point at which the selected marker is located. Also, the trace position corresponding to the selected marker is highlighted in the upper pane.

NOTE

If the markers are not displayed in charts, click the **Show Markers** button at the top of the charts pane.



By using Extent Markers

When you pan/zoom a defined area in charts, Extent Markers are automatically placed at the beginning and end of this defined pan/zoom extent in charts. On changing the pan/zoom area, these markers are automatically moved. There may be situations when you zoom a specific area in charts and then want to navigate to the MPCle data associated with the chart's zoomed area in a Waveform Viewer or a Listing. To accomplish this, you can navigate using Extent Markers.

To navigate using Extent markers:

- 1 Right-click anywhere in the zoomed area in a chart and select **Go To**.
- 2 Then select **Begin Extent** or **End Extent** to navigate to the data associated with the beginning or end of the zoomed area.

The applicable data is highlighted in the upper pane of Protocol Viewer as well as in Waveform and Listing viewers.

Begin	1	-83 ns	MagD	Gen3
	1	-67 ns	Cpl	Gen3
	2	-67 ns	IO Rd	Gen3
	3	-51 ns	Ack	Gen3
	2	-47 ns	UpdateFC P	Gen3

NOTE

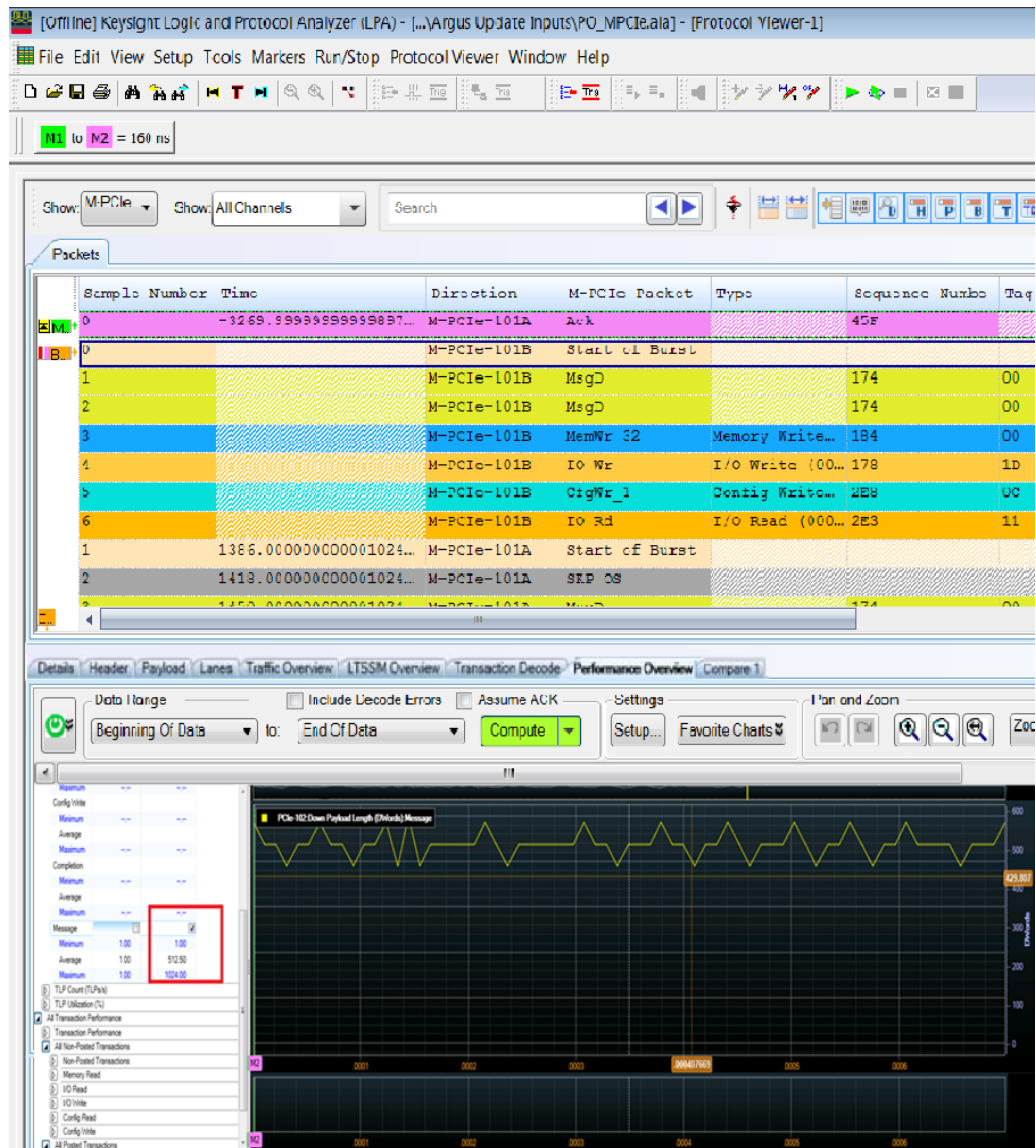
If the Extent Markers are not visible, then click the **Extent Markers** button at the top of the charts panes to display these markers in trace view as well as other viewers in Logic and Protocol Analyzer application.

From the Performance Statistics pane

For some performance parameters such as **Minimum** and **Maximum**, it is possible to navigate directly to the PCIe/SSIC packet that accounted for the generation of a particular statistical value of that performance parameter.

Such performance parameters for which navigation to the packet is applicable are displayed in blue color.

In the following screen the “Message” parameter in the downstream direction has a Min of 1.0 and Max of 1024 displayed in blue color. Clicking these numbers, navigate you to MsgD packets with lengths of 1 and 1024 (DWords) that is accounted for the minimum and maximum value respectively in the upper pane.



NOTE


The chart pane does not show the exact value of the data packets as displayed in the upper pane of the Protocol Viewer window. This is because the chart pane displays the graph of sampled series that represents an average value of all the packets transmitted during a particular micro second as defined in the sample rate field.

Customizing Charts

Changing the Sampling Rate and Re-sampling the Chart

Sampling is a process by which captured data trace is sampled on the basis of specified time period to create charts. The trace data is sampled to compute statistics from these samples for charts generation. This sampling is done as per the *Sample Rate* set for charts. By default, the sample rate is set to 10 microseconds. You can change this sample rate and can re-sample the data trace by specifying time slices. By re-sampling the data you can regenerate charts based on the changed sampling rate.

To change the sample rate of charts

- 1 Access the **Performance Overview** tab.
- 2 Click the  button displayed with the **Sample Rate** field in the Charts pane on the right.
- 3 In the **Time** dialog box, specify the value and unit for the sample rate. The permissible range for sample rate is 1 us to 100 ms.
- 4 Click **OK**.

To regenerate charts based on the new sample rate

- 1 Click the **Re-Sample** button displayed with the **Sample Rate** field at the top of the Charts pane.

NOTE

Sampling is done on the basis of specified time period defined as per the sample rate. It is not applicable for Flow Control category because Flow Control statistics is not time dependent and is based on the flow control credits of the data transmitted and received, The series listed under the Flow Control category have packet granularity whereas sampling is significant for all other categories listed in the Statistics tree/table of the Performance Overview tab.

When you capture a data trace and sample it at defined sample rate to generate charts, there may be repeated scenarios when no message packets are received during a particular slice of time while sampling the entire trace. Such scenarios are represented in the graph by plotting glyphs at zero entry. The glyphs plotted at zero, which you see in the chart, signifies no data packet received at that time.

NOTE

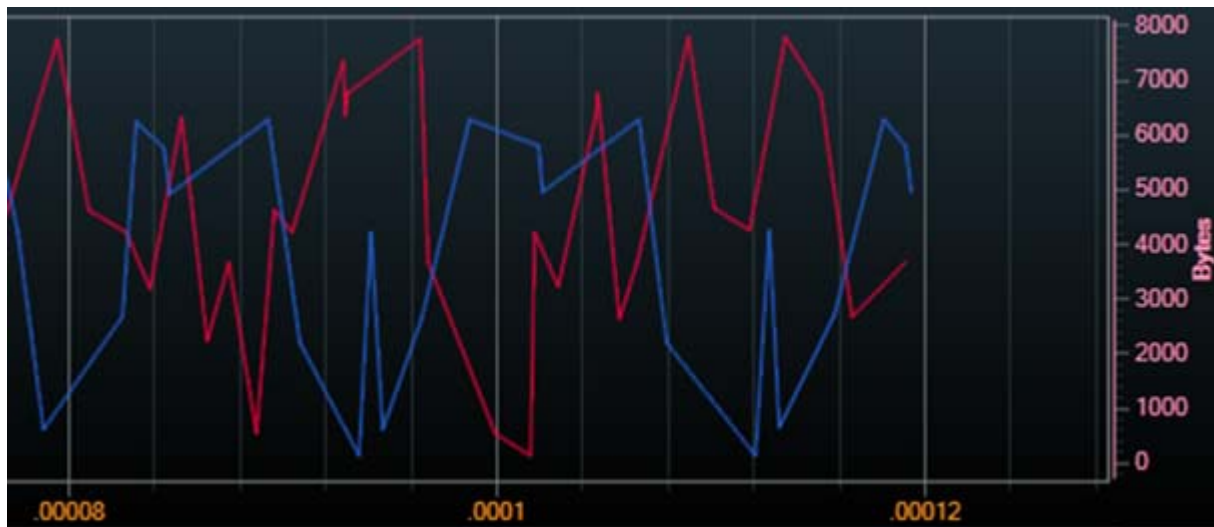
Message packet displaying a zero/hyphen value in the chart does not mean it has zero value, rather it means no relevant data received at that time.

Changing the Chart Display

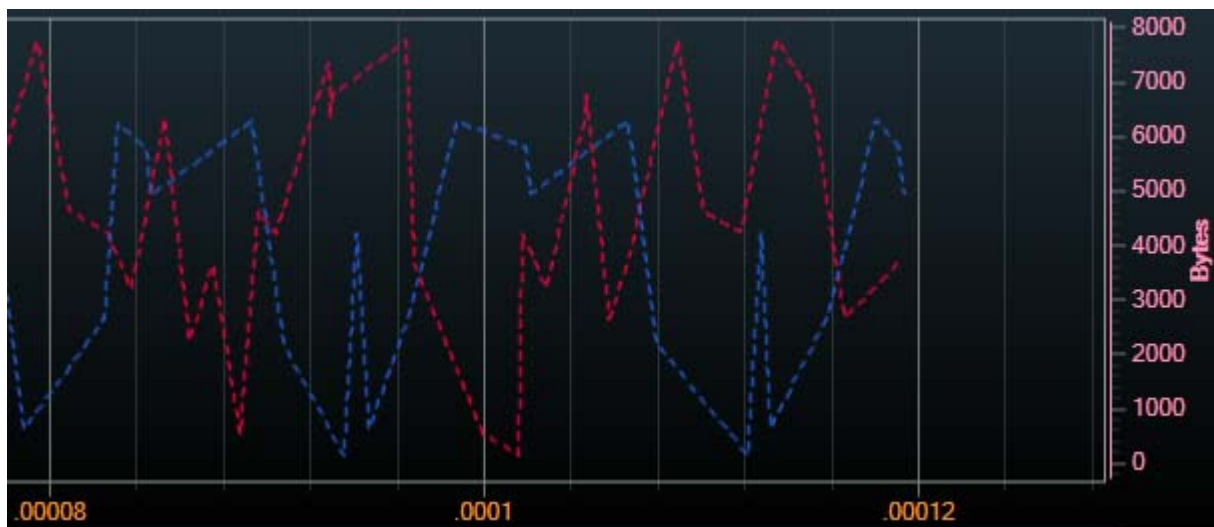
You can change how data is presented in a performance chart. To know, how to change the chart display, see ["Changing the appearance of the graph"](#) on page 160.

By default, data is presented as Dash Line chart type. The following chart type options are available.

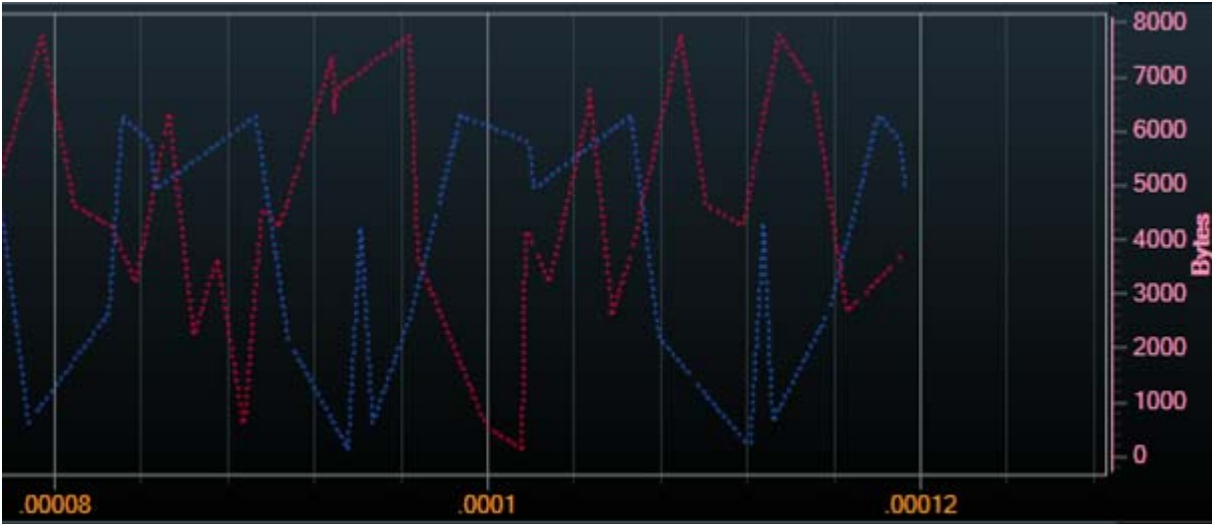
· Line -



· Dash Line-



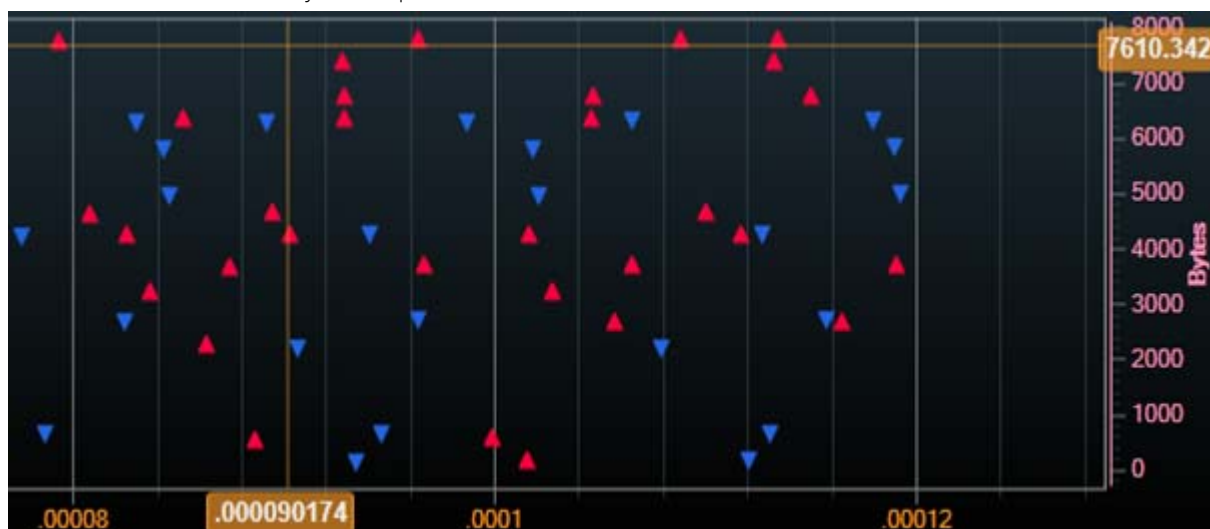
· Dot Line -



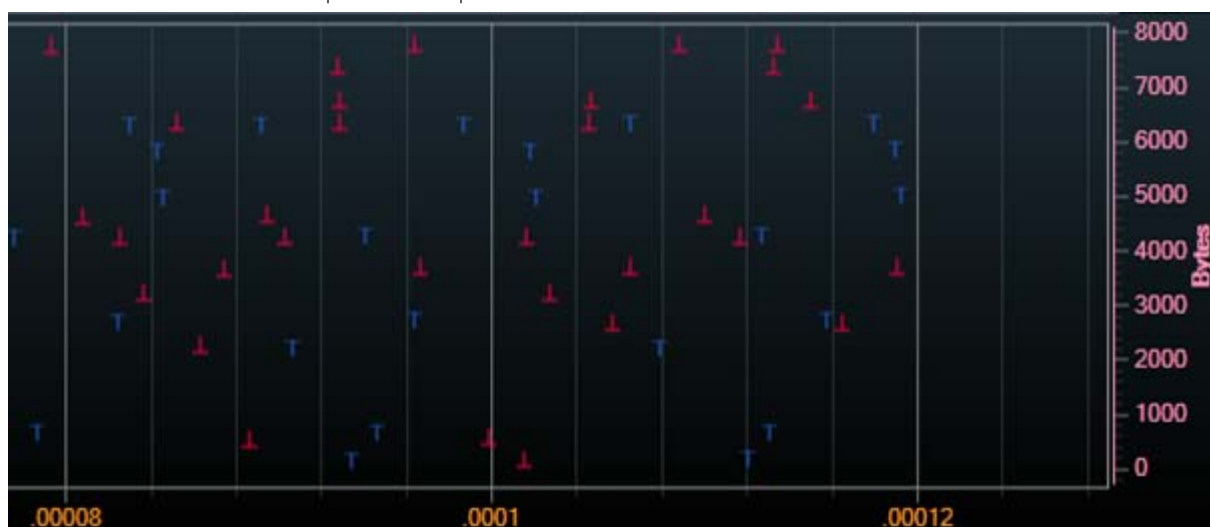
· Triangle Up/Down -



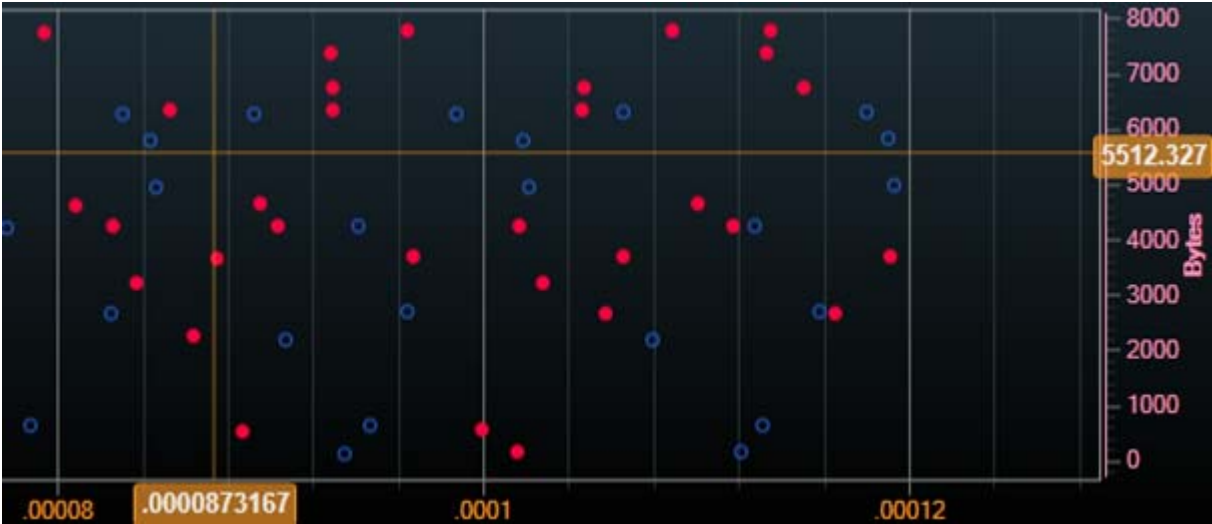
Pyramid Up/Down -



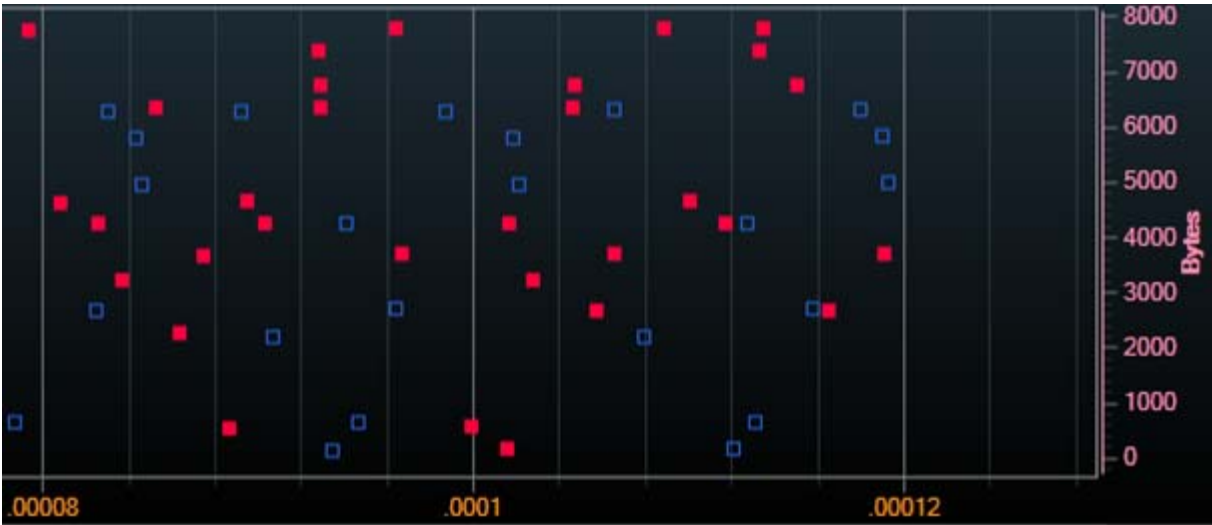
Perpendicular Up/Down-

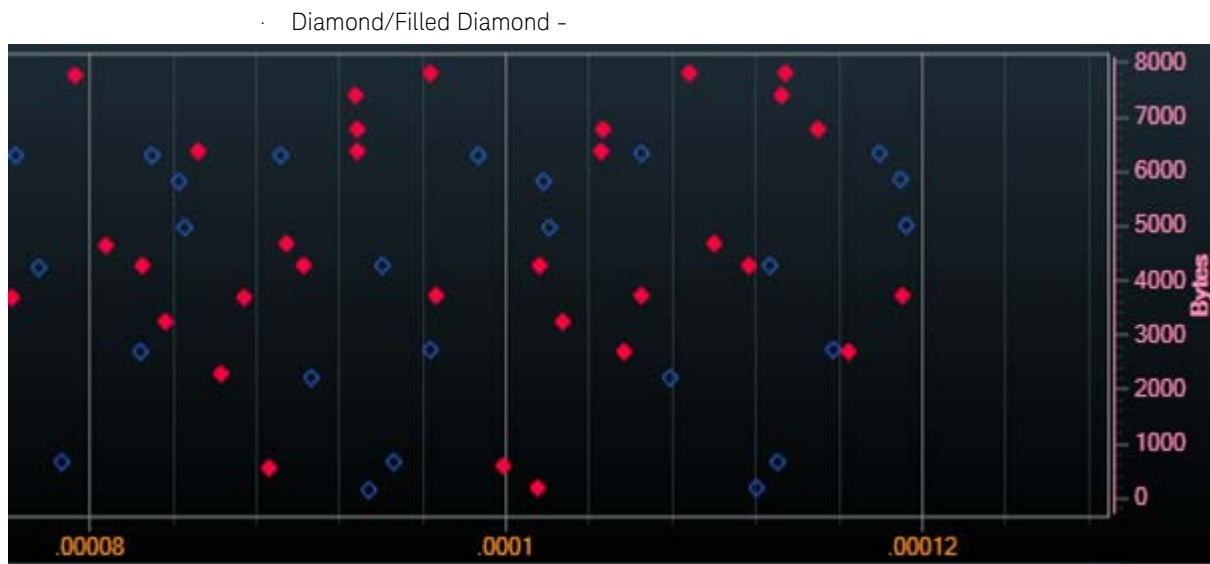


· Dot/Circle -



· Box/Square -





To change chart type for a performance chart

Click the drop-down in the **Type** column from the **Setup...** dialog box and select the type you want to change with.

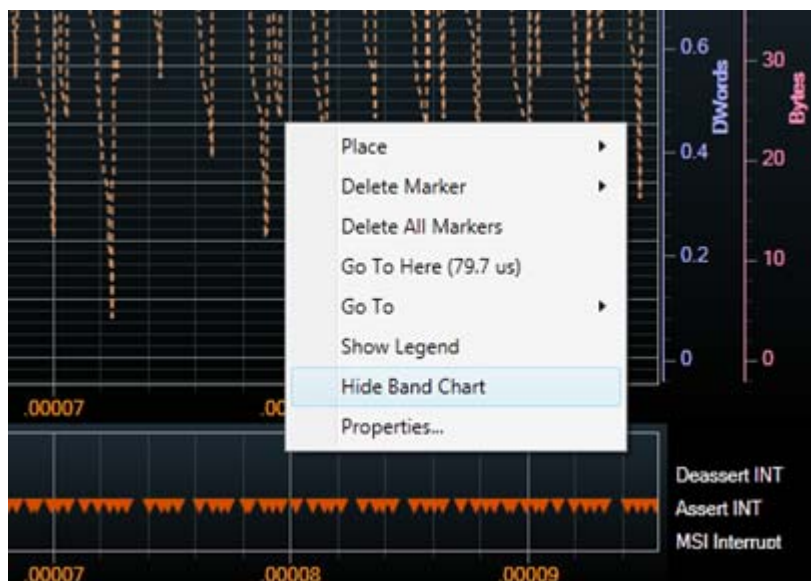
The chart is displayed as per the changed chart type.

Showing/Hiding the Band Chart

You can show or hide the Band Chart displayed in the lower pane of the chart area.

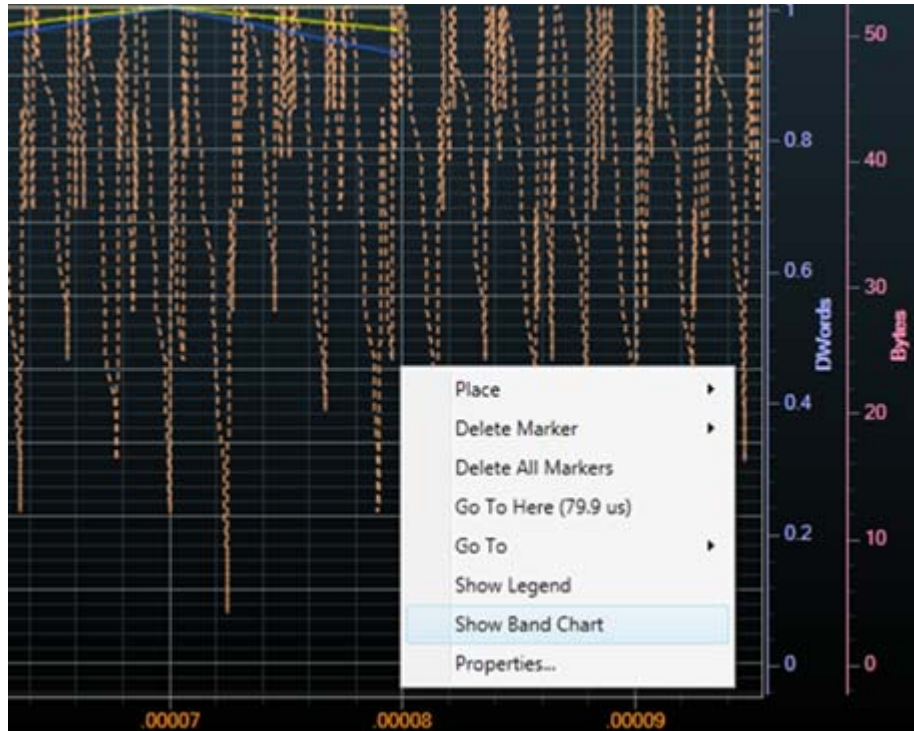
To hide the Band Chart

Right-click anywhere in the chart location and then select **Hide Band Chart**.



To show the Band Chart

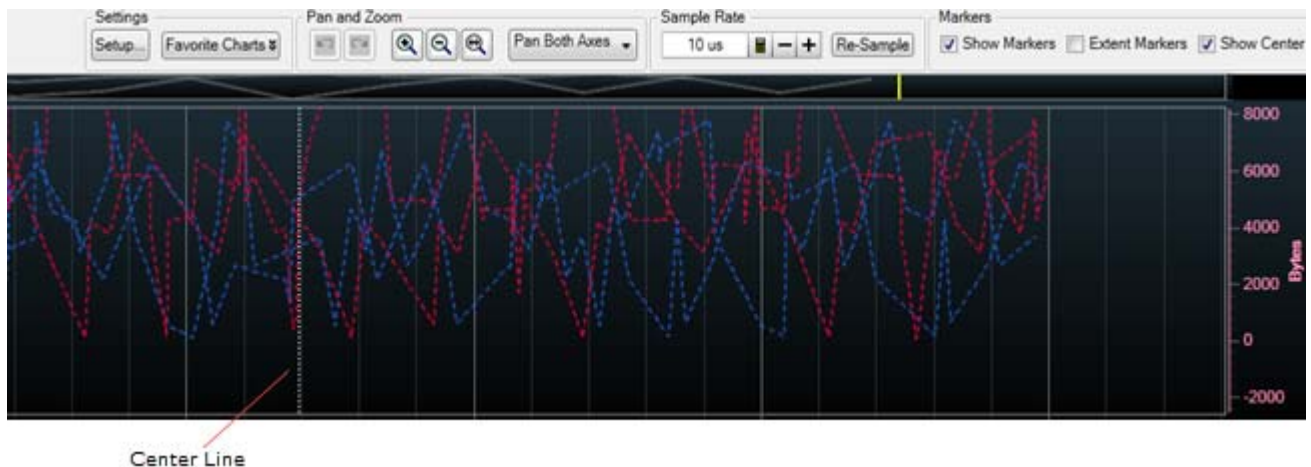
Right-click anywhere in the chart location and then select **Show Band Chart**.



Viewing the Center of the Chart

If the captured traffic is too large and defined pan/zoom extent in the overlay chart is outsized, there may be situations when you want to view where the center of the chart's zoomed area lies. You can do so by using **Show Center** check-box.

To view the center of the chart's zoomed area, click the checkbox **Show Center**. Selecting this checkbox displays a white colored vertical dotted line at the center of the Overlay Chart. It makes the center of the chart apparent as displayed in the following screen:

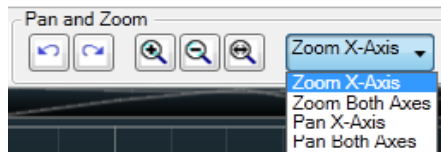


Zooming In/Out Charts

You can zoom in or zoom out a defined area in the chart or the complete chart.

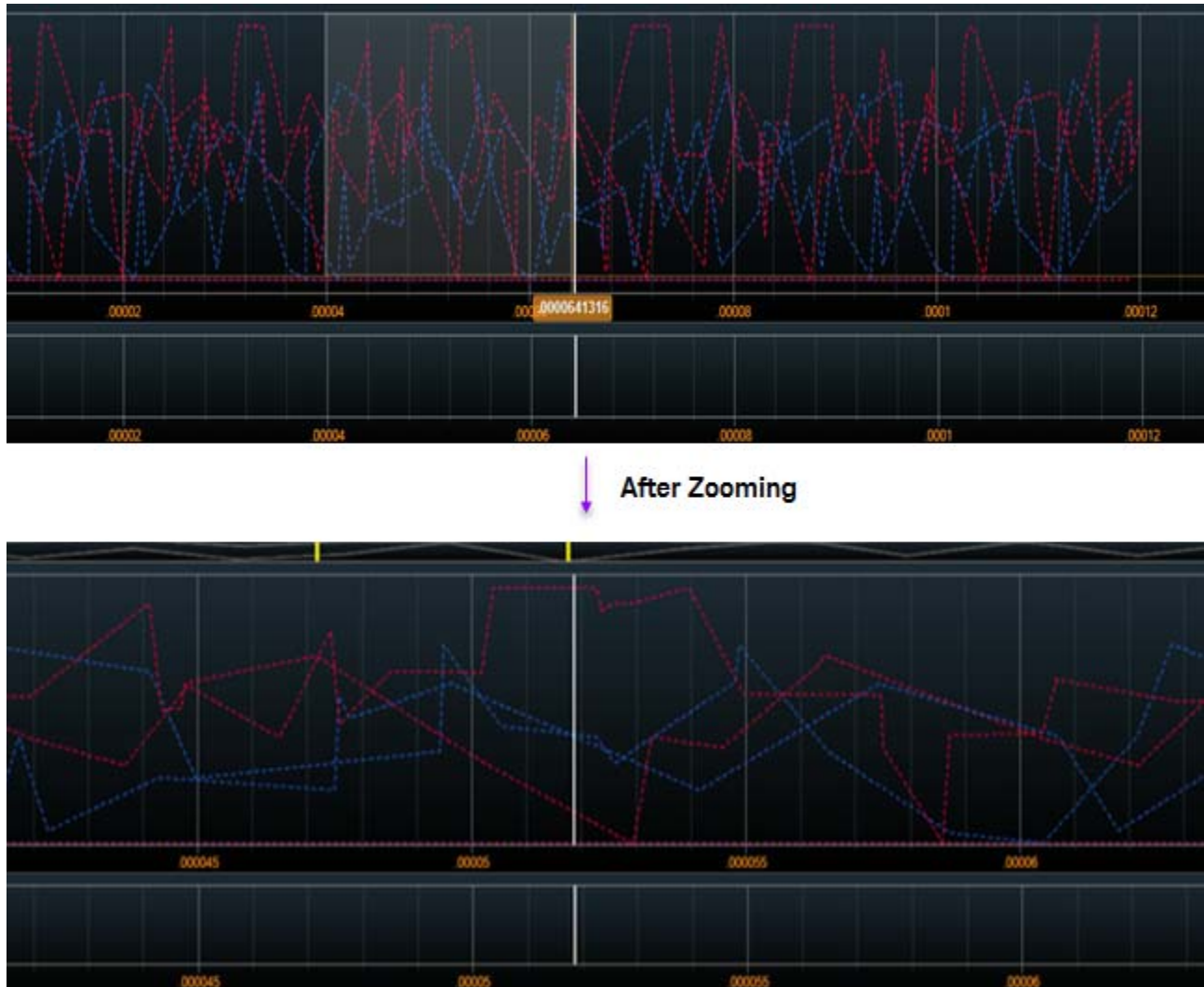
To zoom X-Axis for a defined area in the chart

- 1 Click the **Zoom X-Axis** option from the combo box displayed in the **Pan and Zoom** section of the charts pane to make it active.

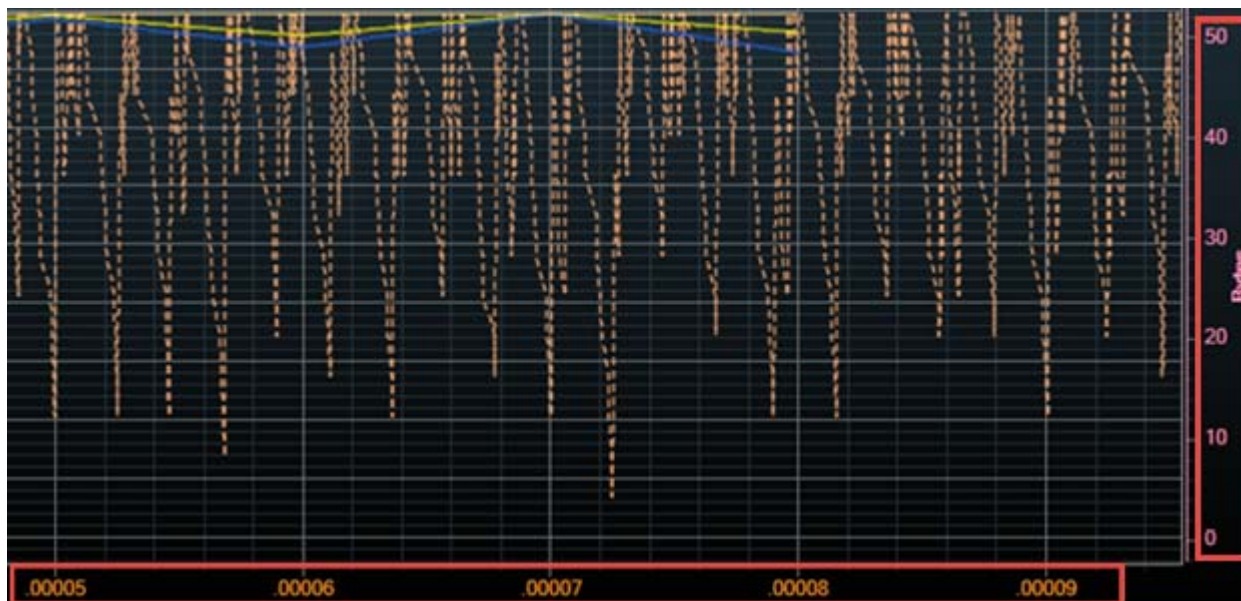


- 2 Move the mouse pointer to the chart location from which you want to begin zooming.
- 3 Left-click at this location and while keeping the left mouse button pressed, drag the mouse to the chart location till which you want to zoom the display. As you move the mouse, the zooming extent is defined in chart and highlighted with grey.

When you release the left mouse button, the defined X-axis area is zoomed for all the displayed charts. Following screen displays the chart before and after zooming in Overlay view:






Similarly, you can zoom both X and Y axes of the defined area in the chart by selecting the **Zoom Both Axes** option from the combo box displayed in the **Pan and Zoom** section of the performance charts pane.

**NOTE**

The X-axis zoom applies to all the displayed charts where as the Y-axis zoom (in both axis zoom) applies only to the chart in which you define the area to zoom.

You can also zoom in or zoom out complete charts. To do this, use the following buttons in the **Pan and Zoom** section of the charts pane.

-  - **Zoom In** magnifies the center 50% of the chart to the full width of the chart.
-  - **Zoom Out** doubles the time displayed in the full width of the chart.
-  - **Zoom Out Full** displays the entire range of Computed data across the full width of the chart.

NOTE

You can undo and redo zooms by clicking the  and  buttons in the **Pan and Zoom** section of the charts pane.

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