
34934A High Density Matrix Module

Notices

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CAUTION

A CAUTION notice denotes a hazard. It calls attention to an operating procedure, practice, or the like that, if not correctly performed or adhered to, could result in damage to the product or loss of important data. Do not proceed beyond a CAUTION notice until the indicated conditions are fully understood and met.

WARNING

A WARNING notice denotes a hazard. It calls attention to an operating procedure, practice, or the like that, if not correctly performed or adhered to, could result in personal injury or death. Do not proceed beyond a WARNING notice until the indicated conditions are fully understood and met.

NOTE

This document contains references to Agilent. Please note that Agilent's Test and Measurement business has become Keysight Technologies. For more information, go to www.keysight.com.

Safety Symbols

The following symbols or markings that may be on or with the instrument and in the documentation indicate precautions which must be taken to maintain safe operation of the instrument.

	Alternating current (AC)		Frame or chassis (ground) terminal
	Standby supply. Unit is not completely disconnected from ac mains when switch is off		Caution, risk of electric shock
	Caution, risk of danger (refer to this manual for specific Warning or Caution information)		Direct current (DC)
	Off (mains supply)		On (mains supply)
	Three phase alternating current		Presence of a laser device
	Protective earth (ground) terminal		Equipment protected throughout by double insulation or reinforced insulation
	Caution, hot surface		Product is sensitive to electrostatic discharge

Additional Safety Notices

The following general safety precautions must be observed during all phases of operation of this instrument. Failure to comply with these precautions or with specific warnings or instructions elsewhere in this manual violates safety standards of design, manufacture, and intended use of the instrument. Keysight Technologies assumes no liability of the customer's failure to comply with the requirements.

WARNING

Refer to the 34980A User's Guide before using the equipment. The 34980A User's Guide contains additional important information about the modules.

WARNING

GENERAL

If this product is not used as specified in the operating instructions, the protection provided by the equipment could be impaired. This product must be used in a normal condition (in which all means for protection are intact) only. Any external connections must be made prior to applying power.

CAUTION

This instrument has auto-ranging line voltage input. Be sure the supply voltage is within the specified range and voltage fluctuations do not to exceed 10 percent of the nominal supply voltage.

NOTE

Pollution Degree 1: No pollution or only dry, non-conductive pollution occurs. The pollution has no influence (on insulation).

Pollution Degree 2: Normally only non-conductive pollution occurs. Occasionally, a temporary conductivity (leakage current between isolated conductors) caused by condensation can be expected.

WARNING**GROUND THE INSTRUMENT**

This is a Safety Protection Class I Product (provided with a protective earthing ground incorporated in the power cord). The mains plug shall only be inserted in a socket outlet provided with a protective earth contact. Any interruption of the protective conductor inside or outside of the product is likely to make the product dangerous. Intentional interruption is prohibited. The mains wiring and connectors shall be compatible with the connector used in the premise electrical system. Inadequate earth grounding can damage the instrument. Always use the three-prong AC power cord supplied with the instrument.

Connect the AC power cord as follow:

- Ensure that the power cord is not damaged.
 - Install the signal generator so that one of the following items is readily identifiable and easily reached by the operator: AC power cord, alternative switch or circuit breaker.
 - Insert the mains plug into a socket outlet provided with a protective earth grounding.
-

WARNING

Removal of the instrument's cover is to be conducted by qualified personnel only. Only qualified, trained personnel who are aware of the hazards involved should remove instrument covers. Prevent operators from accessing any external circuits, test fixtures, cables or wherever hazardous voltages may be present. Failure to recognize and observe normal safety precautions could result in personal injury or death.

WARNING

Safety of any system incorporating the equipment is the responsibility of the assembler of the system.

WARNING

The detachable power cord is the instrument disconnecting device. It disconnects the mains circuits from the mains supply before other parts of the instrument. The front panel switch is only a standby switch and is not a LINE switch (disconnecting device). The instrument power cord does not disconnect or de-energize external circuits connected to the analog bus, terminal blocks or modules.

WARNING

Do not use the instrument if it is damaged. Before you use the instrument inspect all connections. Pay particular attention to the insulation surrounding connectors and / or cable assembly insulation. NEVER use a cable showing any signs of damage. Faulty cables can cause electrical shock and /or fire hazards and could lead to personal injury or death.

WARNING

Keysight Customers utilizing the Open Platform Test Systems are classified as follows and require the user to have the appropriate skillset:

Operator: Interacts with the test system in a production environment, selection of test sequences, defining variables, running tests (test results, test statistics, control of marking devices)

Supervisor: Includes access to maintenance functions and utility sequences (control of hardline system functions, access to test area)

Developer: Full access

WARNING

Dangerous voltage levels capable of causing death, may be present on a channel. Use extreme caution when handling and testing and adjusting this instrument. Any voltages greater than 30 Vrms, 42.4 Vpeak and 60 Vdc are considered hazardous (IEC 61010-1).

WARNING

The maximum common mode input to any one system component within the system installation is not to exceed the maximum stated ratings.

WARNING

High Energy Sources: The Analog Buses are designed to handle inputs up to their rated currents or their rated powers, whichever is less. Under certain fault conditions, high energy sources could provide substantially more current or power than the instrument can handle. It is important to provide external current limiting, such as fuses if the inputs are connected to high-energy sources. The overcurrent protection is to be rated for the maximum available short circuit current of the hazardous sources. Ensure that the current limiting devices / snubber circuits are appropriate for the signal being tested. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death. Refer to [34980A Current Limiting Graphs](#) for the current limiting graphs of 34890A.

WARNING

When working with dangerous voltage levels, intentionally closing of multiple bus and channel relays could cause a potentially lethal hazard on external connections. Use extreme caution when handling and testing and adjusting this instrument. Do not perform these procedures unless qualified to do so. Failure to recognize and observe normal safety precautions could result in personal injury or death.

WARNING

No operator serviceable parts inside. Do not install substitute parts or perform any unauthorized modifications to the instrument. Return the instrument to Keysight for service and repair to ensure the safety features are maintained in operational condition. Instruments that appear damaged or defective should be made inoperative and secured against unintended operation.

WARNING

To prevent electrical shock, disconnect the instrument from mains and external circuits before cleaning. Use a dry cloth or one slightly dampened with water (or 70% Isopropyl Alcohol) to clean the external case parts. Do not attempt to clean internally. Allow any moisture to evaporate prior to energizing the instrument.

WARNING

This product is designed for use in INSTALLATION CATEGORY II and POLLUTION DEGREE 1 and 2 (See module specifications for ratings for each Pollution Degree environment).

WARNING

The AC Voltage source (outlet) must be in proper working order and provide a secure electrical connection. Do not use the outlet if the power cord makes a loose connection or if the power cord plug does not match the outlet. Do not use the outlet if it is damaged or if the voltage is outside the required range.

WARNING

Do not use adapters or extension cords. These devices may not be sufficiently rated to carry the required current and become a safety hazard.

WARNING

ENVIRONMENTAL HEALTH & SAFETY: When any channel is connected to a hazardous voltage source, the instrument and the device under test should be supervised, following local EHS practices to restrict access.

Environmental Conditions

Keysight 34980A is designed for indoor use in an installation category II and low condensation environment. Table below shows the general environmental conditions for this instrument. Refer to the product data sheet at <https://literature.cdn.keysight.com/litweb/pdf/5989-1437EN.pdf> for more information on the instrument general specifications.

General specifications	Requirement
Temperature	Operating condition: 0°C to 55°C Storage condition: -40°C to 70°C
Humidity	Maximum Relative Humidity (non-condensing): 80% RH up to 40°C, decreases linearly to 37% RH at 55°C ^[a]
Altitude	Up to 2,000 m
Pollution degree	1 or 2

[a] From 40°C to 55°C, the maximum % Relative Humidity follows the line of constant dew point.

Regulatory Markings

	<p>The CE mark is a registered trademark of the European Community. This CE mark shows that the product complies with all the relevant European Legal Directives.</p>		<p>The CSA mark is a registered trademark of the Canadian Standards Association.</p>
	<p>The UK conformity mark is a UK government owned mark. Products showing this mark comply with all applicable UK regulations.</p>		<p>The Keysight email address is required by EU directives applicable to our product.</p>
<p>CAN ICES/NMB-001(A)</p>	<p>This indicates that this ISM device complies with the Canadian ICES-001. Interference-Causing Equipment Standard for industrial, scientific and medical (ISM) equipment. Matériel industriel, scientifique et médical (ISM)</p>	<p>ISM 1-A</p>	<p>This is a symbol of an Industrial Scientific and Medical Group 1 Class A product. (CISPR 11, Clause 5)</p>
	<p>The CE mark is a registered trademark of the European Community. This CE mark shows that the product complies with all the relevant European Legal Directives.</p> <p>ICES/NMB-001 indicates that this ISM device complies with the Canadian ICES-001.</p> <p>Cet appareil ISM est conforme à la norme NMB-001 du Canada.</p> <p>ISM GRP.1 Class A indicates that this is an Industrial Scientific and Medical Group 1 Class A product.</p>		<p>This is a combined marking to indicate product compliance with the Industry Canadian Interference-Causing Equipment Standard (ICES/NMB-001). This is also a symbol of an Industrial Scientific and Medical Group 1 Class A product (CISPR 11, Clause 5).</p>
	<p>This symbol is a South Korean Class A EMC Declaration. This is a Class A instrument suitable for professional use and in electromagnetic environment outside of the home.</p>		<p>The RCM mark is a registered trademark of the Australian Communications and Media Authority.</p>

 <p>This symbol indicates the time period during which no hazardous or toxic substance elements are expected to leak or deteriorate during normal use. Forty years is the expected useful life of the product.</p>	 <p>This instrument complies with the WEEE Directive (2002/96/EC) marking requirement. This affixed product label indicates that you must not discard this electrical or electronic product in domestic household waste.</p>
 <p>Universal recycling symbol.</p>	<p>IP x y</p> <p>This mark indicates product has been designed to meet the requirements of "IP x y", where "x" is the solid particle protection and "y" is the liquid ingress protection.</p>

Waste Electrical and Electronic Equipment (WEEE) Directive 2002/96/EC

The crossed out wheeled bin symbol indicates that separate collection for waste electric and electronic equipment (WEEE) is required, as obligated by the EU DIRECTIVE and other National legislation.

Please refer to [keysight.com/go/takeback](https://www.keysight.com/go/takeback) to understand your Trade in options with Keysight in addition to product takeback instructions.



Sales and Technical Support

To contact Keysight for sales and technical support, refer to the support links on the following Keysight websites:

- www.keysight.com/find/34980a
(product-specific information and support, software and documentation updates)
- www.keysight.com/find/assist
(worldwide contact information for repair and service)

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1 Overview of the 34934A

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This chapter provides an overview of the 34934A High Density Matrix Module. It includes operating and general configuration information applicable to all matrix options from 4x32 to 16x32 and larger, with practical examples of the basic matrix options.

34934A High Density Matrix Module

The 34934A is a 512 crosspoint matrix of non-latching reed switches, which you can operate in any of six different 1-wire topologies or three different 2-wire topologies. Larger matrices can be created from the base configurations by linking 34934A modules.

This matrix switch module offers a convenient way for you to connect multiple instruments to multiple points on your device under test (DUT). Instruments and DUTs are connected through the rows and columns of the matrix.

Available base matrix configurations:

You may configure a *single* 34934A module:

- As quad **4x32** matrices (M1H, M1L, M2H, M2L). You can *operate* these as four independent matrices or as two independent 2-wire matrices.
- As dual **4x64** matrices (MH, ML). You can *operate* these as two independent matrices or as a single 2-wire matrix.
- As a single **4x128** matrix.
- As dual **8x32** matrices (MH, ML). You can *operate* these as two independent matrices or as a single 2-wire matrix.
- As a single **8x64** matrix.
- As a single **16x32** matrix.

[Chapter 2, “Base Matrix Configuration”](#) provides detailed instructions for configuring each of these base matrices.

Extended-Row Configurations You can create larger matrix configurations by combining *multiple* 34934A modules. For multiple modules with the same base configuration, extending the rows increases the number of columns in the matrix. For example, two modules each set to 4x128 would combine to create a 4x256 matrix.

[Chapter 3, “Expanded-Row Matrix Configuration”](#) provides detailed instructions for interconnecting modules to create expanded-row matrices.

Operating Considerations

Connection to Voltage Sources

WARNING

It is possible to connect more than one power source at the same time with a matrix. Ensure that dangerous or unwanted conditions are not created by these connections. Failure to recognize or observe normal safety precautions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Safety Interlock

The 34934A module has a hardware Safety Interlock feature that automatically opens all 512 of the module's relays when the associated interlock pins on the high-density D-Sub connectors (faceplate) or extension headers (terminal block or configuration block) lose continuity. This prevents signals from your field wiring from being present on exposed pins on the opposite D-sub connector or extension header.

If you connect to the D-Subs with custom cabling, you must provide continuity for the interlock pins as part of your field wiring / DUT assembly. Specific instructions for providing safety interlock continuity at the D-Sub connectors are provided for each configuration.

If you connect your field wiring using the optional terminal blocks or configuration blocks (available from Keysight) you must also provide continuity for the interlock pins. This can be accomplished by installing the supplied terminators 34934-80006 in the extension headers. Specific instructions for providing safety interlock continuity at the extension headers are provided for each configuration.

Electrical Considerations

See the *Introduction to the Plug In Modules* chapter of the *34980A Mainframe User's Guide* for detailed environmental operating conditions for the 34980A mainframe and its installed modules. That guidance sets maximum per channel current and power ratings at rated voltage for pollution degree 1 and pollution degree 2 conditions, for the 34934A matrix module.

WARNING

High Energy Sources: The Analog Buses are designed to handle inputs up to their rated currents or their rated powers, whichever is less. Under certain fault conditions, high energy sources could provide substantially more current or power than the instrument can handle. It is important to provide external current limiting, such as fuses if the inputs are connected to high-energy sources. The overcurrent protection is to be rated for the maximum available short circuit current of the hazardous sources. Ensure that the current limiting devices / snubber circuits are appropriate for the signal being tested. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death. Refer to [34980A Current Limiting Graphs](#) for the current limiting graphs of 34890A.

Thermal Considerations

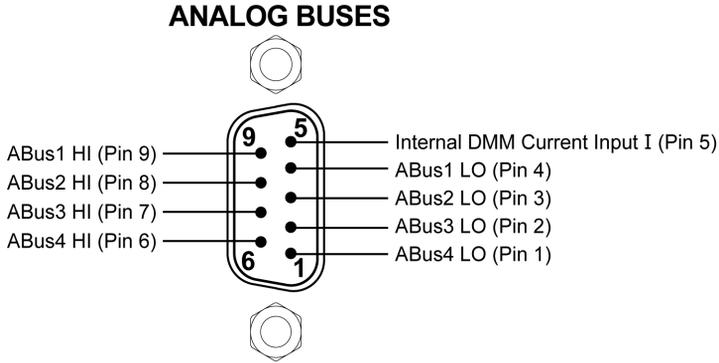
Because of mainframe thermal and power-supply restrictions, you can close no more than 64 channels (crosspoint relays) on the 34934A at one time. If you try to close a 65th channel, an error will be generated from the 34980A, and the channel will remain open.

Connection to the 34980A Analog Buses

This module does not internally connect to the 34980A mainframe's four 2-wire (HI, LO) 300V-rated Analog Buses.

- ABUS1 (MEAS) and ABUS2 (SENSE) connect to the internal DMM.
- ABUS3 and ABUS4 are for signal-routing only, and do not connect to the DMM.

You can connect from a row/column crosspoint directly to the external 9-pin Analog Bus connector located on the 34980A's rear panel (see connector pinout below).



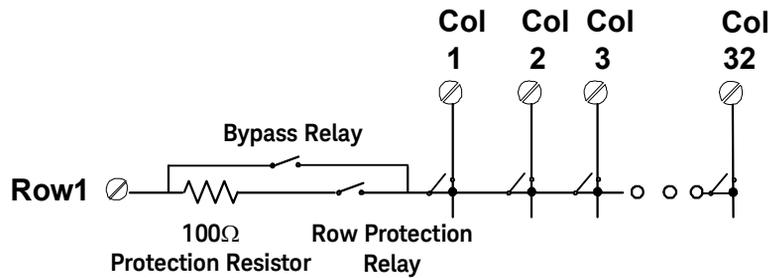
Analog Bus connector (as viewed from rear of instrument)

See the “*Electrical Operating Conditions*” and “*Analog Buses*” sections of the *34980A Mainframe User’s Guide* for complete instructions and safety precautions for accessing the Analog Buses.

Row Protection

For reed relays, user-attached reactive loads and module parasitic capacitance may result in high in-rush currents. The 34934A module provides a method of protecting the reed relays from in-rush current, called Row Protection. The module incorporates sixteen 100Ω resistors, one for each bank of 32 columns within a row. Each of these resistors is in parallel with a bypass relay, as shown below.

Typical Row Schematic, Illustrating Row Protection Relay and Bypass Relay



The row protection mode is set with the command:

```
SYSTem:MODE:ROW:PROTection <slot>, <mode>
```

The mode parameters are listed below along with descriptions of the modes. The selected mode applies to all channels within all matrices of the 34934A module in the selected slot.

AUTO100 In this mode, closing any channel also closes the Row Protection Relay for that row, placing a 100Ω resistor in the signal path from *that* channel's 32-column bank (see above). The Row Protection Relay remains closed while any channel in that 32-channel bank is closed. The protection relay opens *after* all channels in that bank are opened.

AUTO0 In this mode, closing a channel also closes the Row Protection Relay for that row, placing the 100Ω protection resistor *momentarily* in the signal path from *that* channel's 32-column bank to dissipate inrush current. The protection relay is then opened and shunted by the bypass relay.

FIXed In this mode, all 16 Row Protection Relays are closed, placing the 100Ω resistors in the signal paths from each 32-column bank. This operation is

independent of the state (open, closed) of any channel relays. The Row Protection Relays remain closed until the row protection mode is changed, the instrument is reset, or power is cycled to the instrument.

NOTE

Fixed mode is not recommended for matrix configurations (i.e. 4x64, 4x128, 8x64) where rows are extended to provided additional columns - unless only DC signals are used. The extended rows can create large stubs and capacitances.

ISOLated In this mode, all 16 Row Protection Relays and Bypass Relays are open, allowing no signals to pass between 32-column banks via the rows. *This mode only applies to the 4x32, 8x32 and 16x32 matrices.* By isolating the rows, you can route signals column to column within the same bank of 32 columns, using the row as a connection bus between columns. The Bypass Relays remain open until the row protection mode is changed, the instrument is reset, or power is cycled to the instrument. This operation is independent of the state (open, closed) of any channel relays.

See “[Setting the Relay Protection Mode](#)” on page 276 for sample command examples.

General Configuration Procedures for the 34934A

WARNING

Under certain conditions, dangerous voltage levels capable of causing injury or death may remain even after external circuits have been disconnected. To avoid electrical shock, remove the main power cord from the 34980A and ensure all connections to the DUT, including field wiring to the instrument and the analog bus (if present) are deenergized and all circuits are discharged before coming in contact with the system. Ensure no hazardous voltages remain on any accessible before handling any part of the instrument and test setup, making connections to the system, removing covers or cleaning the instrument.

WARNING

Do not connect the Analog Buses directly to a mains power outlet. If it is necessary to measure a mains voltage or any circuit where a large inductive load may be switched, you must add signal conditioning elements to reduce the potential transients before they reach the Analog Buses. Refer to User's Guide for the maximum rated transients for each external source.

Base Matrix Configurations

In practice, you will follow these steps to select, configure, and wire a *single* 34934A matrix module:

1 Select a base matrix configuration:

- 4x32
- 4x64
- 4x128
- 8x32
- 8x64
- 16x32

The 4x32, 4x64, and 8x32 configurations can be *operated* as 2-wire channel pairs programmatically.

2 Select your wiring method:

- use of an optional screw terminal block
- use of an optional configuration block
- direct wiring to the module's high-density D-Sub connectors

3 *If using a terminal block:*

- a** Select and apply a wiring guide overlay to the terminal block, if required, for the chosen configuration.
- b** Place jumpers and terminators on the terminal block to configure the selected base matrix.
- c** Connect your field wiring to the terminal block.

4 *If using a configuration block:*

- a Place jumpers and terminators on the configuration block to configure the selected base matrix and wiring options.
 - b Wire jumpers to short the safety interlock pins on each of the configuration block's D-Sub connectors.
 - c Connect your field wiring to the configuration block.
- 5** *If using direct wiring:*
- a Connect your field wiring to the module's D-Sub connectors, using the corresponding D-Sub connector drawings and pin assignment table.
 - b Make additional wiring connections to configure the selected base matrix configuration and safety interlock continuity.

Chapter 2, “Base Matrix Configuration” has one section for each base matrix configuration (e.g. 4x64, 16x32, etc.). Each section contains complete instructions for terminal block use, configuration block use, and direct wiring.

Extended-Row Matrix Configurations

In practice, you will follow these steps to select, configure, and wire an extended-row matrix, combining two or more 34934A modules:

- 1** Determine your matrix configuration (just a few possibilities are shown):
 - 4x256 (requires two 34934A modules, each with a base configuration of 4x128)
 - 4x512 (requires four 34934A modules; base configuration of 4x128)
 - 8x128 (requires two 34934A modules; base configuration of 8x64)
 - 16x64 (requires two 34934A modules; base configuration of 16x32)
 - 16x256 (requires eight 34934A modules; base configuration of 16x32)

The 4x32, 4x64 and 8x32 base configurations can be *operated* as 2-wire channel pairs programmatically.

- 2** Select your wiring method:
 - use of an optional screw terminal block
 - use of an optional configuration block
 - direct wiring to the module's high-density D-Sub connectors
- 3** Configure and wire each individual module as if a stand-alone matrix using the base configuration instructions in [Chapter 2](#).

- 4 Create a daisy-chain connection between the modules (either by interconnecting the terminal blocks or configuration blocks), using the instructions in [Chapter 3](#).

Benefits of Using the Terminal and Configuration Blocks with the 34934A

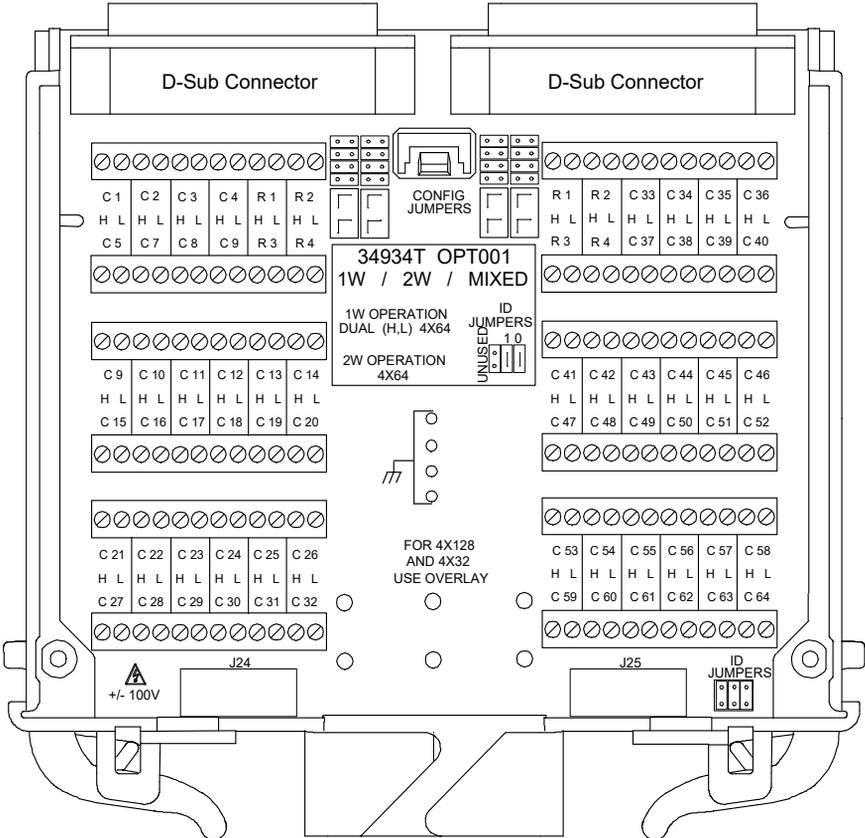
You have three options for connecting field wiring to the 34934A:

- Terminal Blocks
- Configuration Blocks
- Direct Wiring

Terminal Blocks

Terminal blocks provide convenient access through screw terminals to the rows and columns of the matrix. Additionally, the rows can also be wired to 20-pin extension headers on the block.

Terminal block 34934T-001 is used with the 4x32, 4x64 and 4x128 matrices. Terminal block 34934T-002 is used with the 8x32, 8x64 and 16x32 matrices. The 34934T-001 is shown below.



WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Wiring of the terminal block must be performed by qualified persons. A **MAXIMUM** of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

WARNING**External Wiring for Hazardous Voltages:**

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
- are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
- flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm^2 meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm^2 or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
- temperature rated for the application.
- classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)

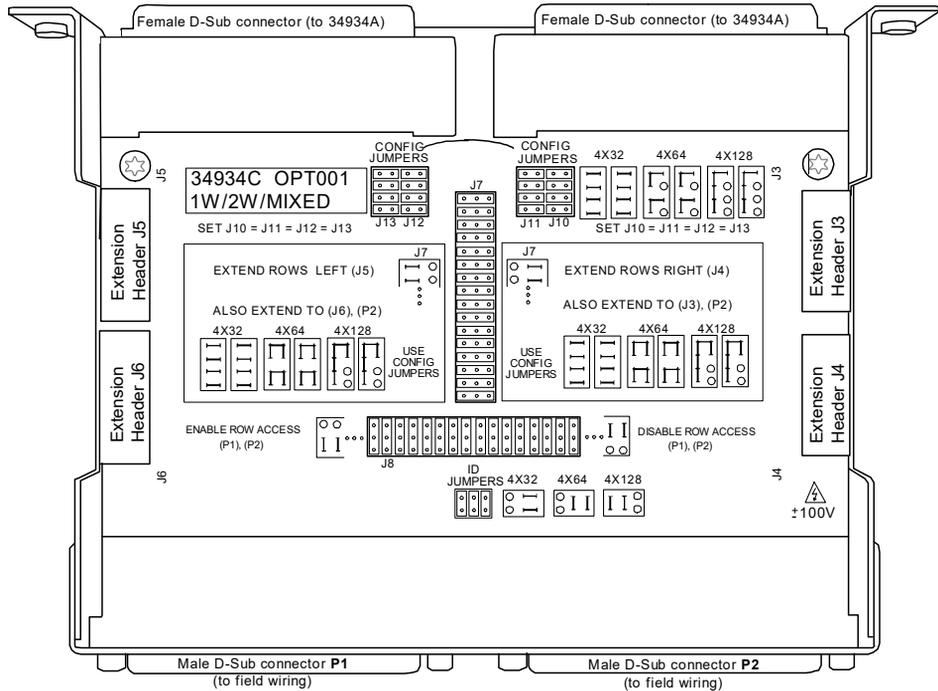
The silk-screen on each terminal block (34934-001, 34934-002) represents one of the three configurations supported by each block. Removable silk-screen overlays are used for each block's other (two) supported configurations.

The terminal block attaches to the 34934A module's D-Sub connectors. Matrix configuration (selection) is accomplished by placing jumpers on the terminal blocks. You choose a configuration, place jumpers, and then wire your test circuit to the screw terminals and/or extension headers.

Configuration Blocks

Configuration blocks allow access through pre-made molded cables to the rows and columns of the matrix. Additionally, the rows can also be wired to 20-pin expansion headers on the block.

Configuration block 34934C-001 is used with the 4x32, 4x64 and 4x128 matrices. Configuration block 34934C-002 is used with the 8x32, 8x64 and 16x32 matrices. The 34934C-001 is shown below.



The configuration block attaches to the 34934A module's D-Sub connectors. Matrix configuration (selection) is accomplished by placing jumpers on the configuration block. You choose a configuration, place jumpers, then wire your test circuit to the *configuration block's* male D-Sub connectors and/or extension headers. In addition, you will have to short two pins on each D-Sub to provide safety interlock continuity. *All other configuration is done within the block; this saves significant wiring time compared to direct wiring (which requires user-supplied configuration jumper wiring between the 34934A's D-Sub connectors).*

Configuration blocks can be used with a single 34934A. They can also be used in extended-row matrix configurations employing multiple 34934A modules; daisy-chain connections between the modules are made using the extension headers.

WARNING

For the 3434C-001, the D-Sub connector pin assignments are identical to the D-Sub connectors on the 34934A module. For the 34934C-002, the D-Sub connector pin assignments are not identical to the D-Sub connectors on the 34934A module. Pay careful attention to the wiring requirements found in the instructions when making D-Sub connections! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring

The 34934A has two 78-pin male high-density D-Sub connectors. You can connect your field wiring directly to these connectors using custom D-Sub cables. All matrix configuration must be accomplished by user-supplied wiring between these two cables.

WARNING

For some modes (8x32, 8x64 and 16x32), direct wiring is extensive. Using the 34934C-002 configuration block in these instances will significantly reduce the additional wiring required. Failure to correctly wire the instrument could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Both the 34934A module and 34934C (Options 001 and 002) configuration blocks have 78-pin male D-Sub connectors. Wiring to these is covered in separate sections, and requires different wiring (34934A vs. 34934C) for each base configuration.

Use of the 34980A Web Browser Interface with the 34934A

The *34980A Web Browser* interface, accessible by PC via a LAN connection, is highly useful for helping you visualize the 34934A matrix topologies, nomenclature, and channel numbering.

Using the Web Browser, you can observe and fully control the 34934A module. The Web Browser displays the matrix in use, with rows, columns and channel numbers for each matrix crosspoint. An example is shown below, illustrating the 4x128 matrix.

Agilent Technologies Multifunction Switch/Measure Unit

Observe Only | **Allow Full Control** | Scan Control... | Sequences... | System Overview... | Alarms... | Commands... | Update View

Modules in Mainframe

(1) 1, 4x128 Matrix [34934A-4x128] (5) Slot 5 empty
 (2) Slot 2 empty (6) Slot 6 empty
 (3) Slot 3 empty (7) Slot 7 empty
 (4) Slot 4 empty (8) Slot 8 empty

Analog Bus Overview

	Slot								
	1	2	3	4	5	6	7	8	
ABus1	-----	-----	-----	-----	-----	-----	-----	-----	MEAS
ABus2	-----	-----	-----	-----	-----	-----	-----	-----	SENS
ABus3	-----	-----	-----	-----	-----	-----	-----	-----	
ABus4	-----	-----	-----	-----	-----	-----	-----	-----	

DMM

Configure DMM...
Monitor DMM...

Slot 1: Single 4x128 High Density Reed Matrix

Matrix 1	Row 1	Row 2	Row 3	Row 4
Column 128	1228	1428	1628	1828
Column 127	1227	1427	1627	1827
Column 126	1226	1426	1626	1826
Column 125	1225	1425	1625	1825
Column 124	1224	1424	1624	1824
Column 123	1223	1423	1623	1823
Column 122	1222	1422	1622	1822
Column 121	1221	1421	1621	1821
Column 120	1220	1420	1620	1820
Column 119	1219	1419	1619	1819
Column 118	1218	1418	1618	1818
Column 117	1217	1417	1617	1817
Column 116	1216	1416	1616	1816
Column 115	1215	1415	1615	1815
Column 114	1214	1414	1614	1814
Column 113	1213	1413	1613	1813
Column 112	1212	1412	1612	1812
Column 111	1211	1411	1611	1811
Column 110	1210	1410	1610	1810
Column 109	1209	1409	1609	1809
Column 108	1208	1408	1608	1808
.....

Module Overview...
Reset Module
Open All

Left-click switch to open/close
Right-click switch to view configuration

Besides providing a convenient means to control the module, the Web Browser offers the easiest method to view the channel map for any set configuration of the module.

For complete guidance on LAN connection and navigating the 34980A Web Browser Interface, see the "Getting Started" chapter of the 34980A Mainframe Users Guide.

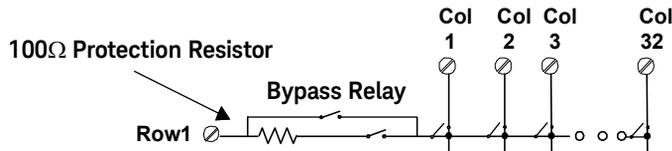
Practical Examples for Selecting a Matrix Configuration Mode

The 34934A provides a wealth of configuration options, using just one module or in combinations of up to eight modules in a single mainframe. Each module combines four electrically separate 4x32 matrices; the 4x32 matrix is the basic elemental component for all larger matrices which can be configured by jumper settings and wiring.

This section provides—for each *base matrix configuration*—a description of the matrix topology and at least one example illustrating its application.

The examples include:

- A simplified electrical schematic of the matrix.
- Physical location of the Row Protection Resistors relative to the rows and columns.



- Row jumpers used to create the matrix from its 4-row by 32-column elemental components. (The row jumper represents the 2-pin CONFIG(uration) jumpers contained on the 34934A terminal and configuration blocks. For direct wiring applications, the rows must be physically shorted together.)



- Practical examples of physical locations to connect your test instrumentation and DUT to the matrix.
- **Bold lines** showing each complete electrical signal path (from DUT to instrument) through the matrix.
- The crosspoint relays included in each circuit path. Each relay has a channel number which uniquely identifies its row-column intersection. The channel numbering scheme is unique to each base matrix configuration (e.g. 4x32).

“[Channel Numbering](#)” on page 272 provides the algorithm for how these channel numbers were computed.

- The syntax for the SCPI command used to close the circuit path, with the appropriate channel numbers.

In most of the examples, all test instruments and DUTs are connected to the columns, while the rows are used as busses to route signals between the components. For applications requiring protection from high in-rush currents, you may also connect your instruments or DUTs to the rows of the matrix which contain the Row Protection Resistors. Some examples illustrate this method of connection.

By exploring these examples, you can *choose the base matrix configuration best suited to your unique test application*; the available base matrix configurations are listed on [page 18](#). Then, you can proceed to the section of [Chapter 2](#) which provides configuration and wiring instructions for the chosen base matrix.

4x32 Matrices

4x32 Topology

You can select the 4x32 matrix configuration by setting jumpers on the appropriate terminal block or configuration block, or by D-Sub wiring.

Once configured for 4x32, the 34934A creates four matrices, each with 128 crosspoint non-latching reed relays organized in a 4-row by 32-column configuration. You can *operate* these as four independent *1-wire* matrices (M1H, M1L, M2H and M2L) or as (H,L) pairs of two independent *2-wire* matrices (M1 and M2).

In 1-wire operation, the four matrices are:

- M1H: uses H (high) columns C1H-C32H and H rows R1H-R4H of M1.
- M1L: uses L (low) columns C1L-C32L and L rows R1L-R4L of M1.
- M2H: uses H columns C1H-C32H and H rows R1H-R4H of M2.
- M2L: uses L columns C1L-C32L and L rows R1L-R4L of M2.

CAUTION

The four matrices (M1H, M1L, M2H, M2L) are isolated from each other; however associated matrices (M1H-M1L, M2H-M2L) have larger capacitances to one another than non-associated matrices (M1H-M2H, M1L-M2L).

In 2-wire operation, the two matrices are:

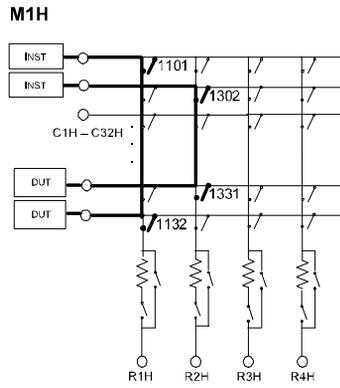
- M1: uses both the H and L columns and rows; C1H-32H, C1L-C32L, R1H-R4H, R1L-R4L; of Matrix 1.
- M2: uses both the H and L columns and rows; C1H-32H, C1L-C32L, R1H-R4H, R1L-R4L; of Matrix 2.

As the examples illustrate, no physical matrix configuration changes are required for 2-wire operation. You wire your test devices between corresponding high and low matrix terminals or D-Sub pins, then operate the matrix in 2-wire mode by using SCPI paired commands (e.g. ROUT:OPEN:PAIR).

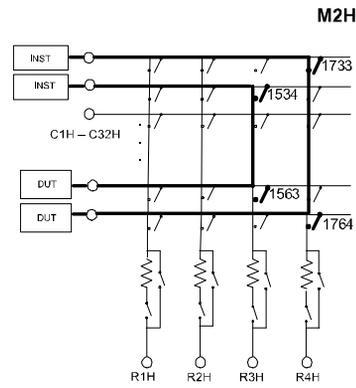
A potential application of the 4x32 topology is to extend one 4x32 matrix to an adjacent 4x128 matrix (a second 34934A module) to create a 4x160 (1-wire) matrix with three independent 4x32 (1-wire) matrices.

4x32 1-Wire Operation, Test Circuit Examples

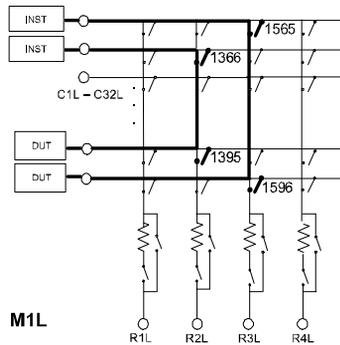
The four examples below each show two 1-wire circuit paths. The M1 matrices wire up to the left side of the 34934T-001 terminal block; the M2 matrices wire on the right.



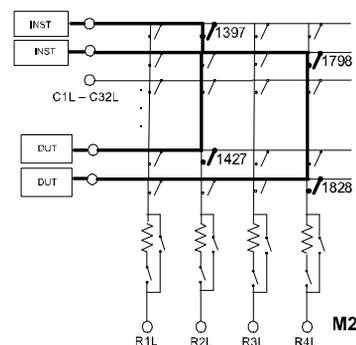
ROUT CLOS (@ 1101 , 1132 , 1302 , 1331)
 Closes: row R1H @ columns C1H and C32H
 row R2H @ columns C2H and C31H



ROUT CLOS (@ 1733 , 1764 , 1534 , 1563)
 Closes: row R4H @ columns C1H and C32H
 row R3H @ columns C2H and C31H



ROUT CLOS (@ 1565 , 1596 , 1366 , 1395)
 Closes: row R3L @ columns C1L and C32L
 row R2L @ columns C2L and C31L



ROUT CLOS (@ 1397 , 1427 , 1798 , 1828)
 Closes: row R2L @ columns C1L and C31L
 row R4L @ columns C2L and C32L

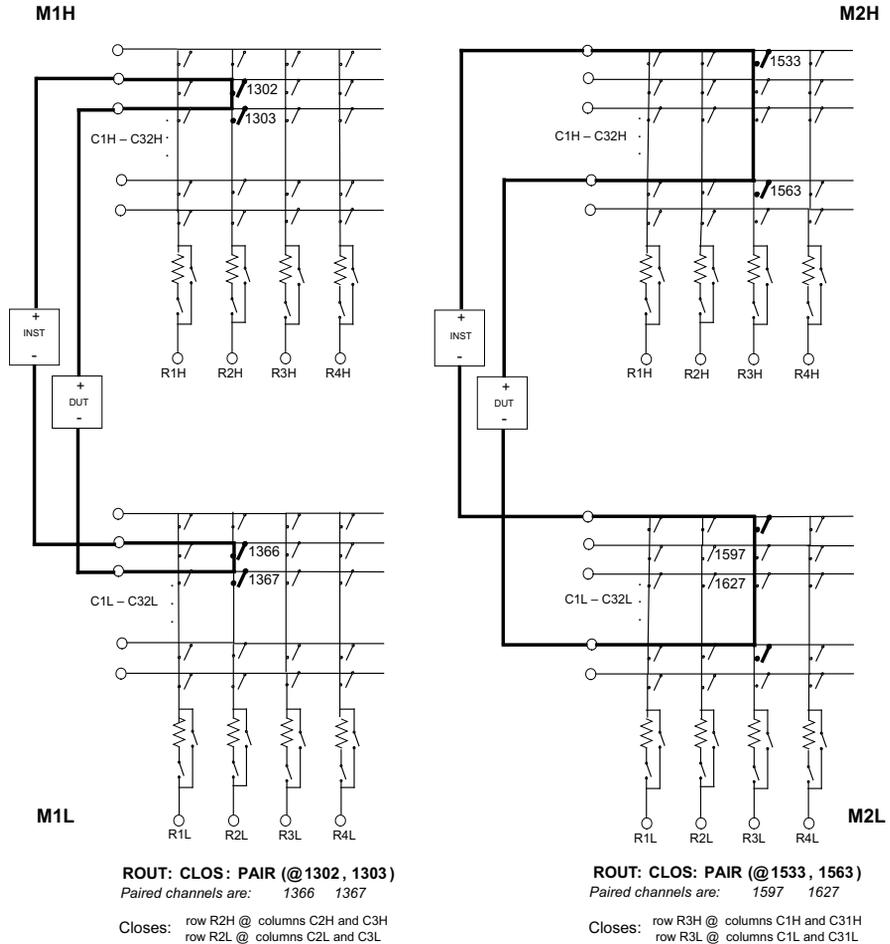
See “Channel Numbering” on page 168 for the numbering scheme.

4x32 2-Wire Operation, Test Circuit Examples

The two examples below each show a 2-wire circuit path. One path uses matrix 1 (left side of the 34934T-001 terminal block); the other path uses matrix 2 (right side of terminal block).

1 Overview of the 34934A

These examples introduce one of the SCPI paired commands; **ROUTE:CLOSE:PAIR**. With pair commands, you specify only the H channel numbers; but both the H and L channels of a pair are operated (e.g. opened or closed).



See "Channel Numbering" on page 168 for the numbering scheme.

4x64 Matrices

4x64 Topology

You can select the 4x64 matrix configuration by setting jumpers on the appropriate terminal block or configuration block, or by D-Sub wiring.

Once configured for 4x64, the 34934A creates two matrices, each with 256 crosspoint non-latching reed relays organized in a 4-row by 64-column configuration. You can *operate* these as two independent *1-wire* matrices (MH, ML) or as a single *2-wire* matrix.

In 1-wire operation, the two matrices are:

- MH: uses H (high) columns C1H-C64H and H rows R1H-R4H.
- ML: uses L (low) columns C1L-C64L and L rows R1L-R4L.

In 2-wire operation, the matrix uses both the H and L columns and rows; C1H-64H, C1L-C64L, R1H-R4H and R1L-R4L.

As the examples illustrate, no physical matrix configuration changes are required for 2-wire operation. You wire your test devices between corresponding high and low matrix terminals or D-Sub pins, then operate the matrix in 2-wire mode by using SCPI paired commands (e.g. ROUT:OPEN:PAIR).

In either mode of operation, each matrix (MH, ML) is created by jumpering the rows on two of the 4x32 relay banks together. To do this, you will either place physical “CONFIG” jumpers on a terminal or configuration block, or create wiring jumpers by shorting pins between your D-Sub cables. The row jumpers are shown on the examples.

Each 32-column bank connects to the other through one 100 Ω Row Protection Resistor (with Bypass Relay) per row. The examples which follow show multiple ways to connect your DUT and test instrument; each circuit path *may* include series resistance, depending on the connection and programmed mode for row protection (see “**Row Protection**” on page 22).

A potential application of the 4x64 1-wire topology is to extend one of the 4x64 matrices to an adjacent 4x128 matrix (a second 34934A module) to create a 4x192 (1-wire) matrix with an independent 4x64 (1-wire) matrix.

The 4x64 2-wire topology is optimal for applications where a limited number of concurrent 2-wire measurements are required over a large number of pins.

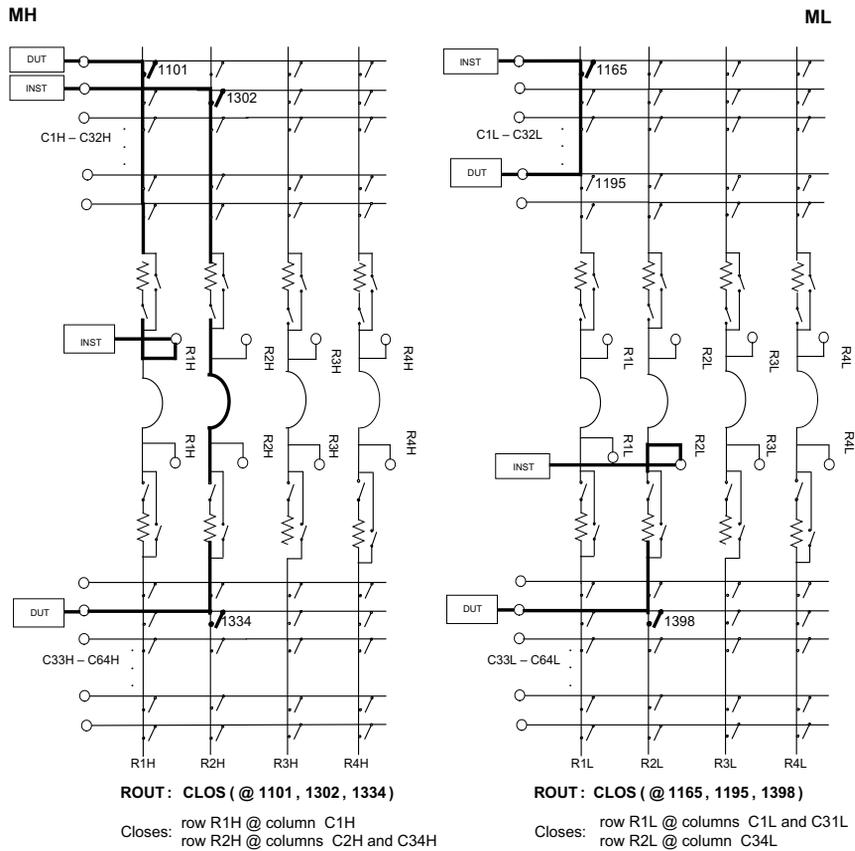
4x64 1-Wire Operation, Test Circuit Examples

The two examples below each show two 1-wire circuit paths.

On MH: The upper example path connects a DUT on column C1 to an instrument on row R1, through one 100Ω series row-protection resistor. The lower path shows both DUT and instrument connected to columns, but through two resistors.

On ML: The upper example path connects the DUT to an instrument on row R1; both crosspoints are on the same 32-relay bank, without the in-rush resistors. The lower example illustrates that even when DUT and instrument are on the same bank, the resistor is included in the circuit path.

In any path which makes a row connection (or which connects columns from separate 32-column banks), you can program the mode for the row protection resistor and bypass relay.



See “Channel Numbering” on page 168 for the numbering scheme.

4x64 2-Wire Operation, Test Circuit Example

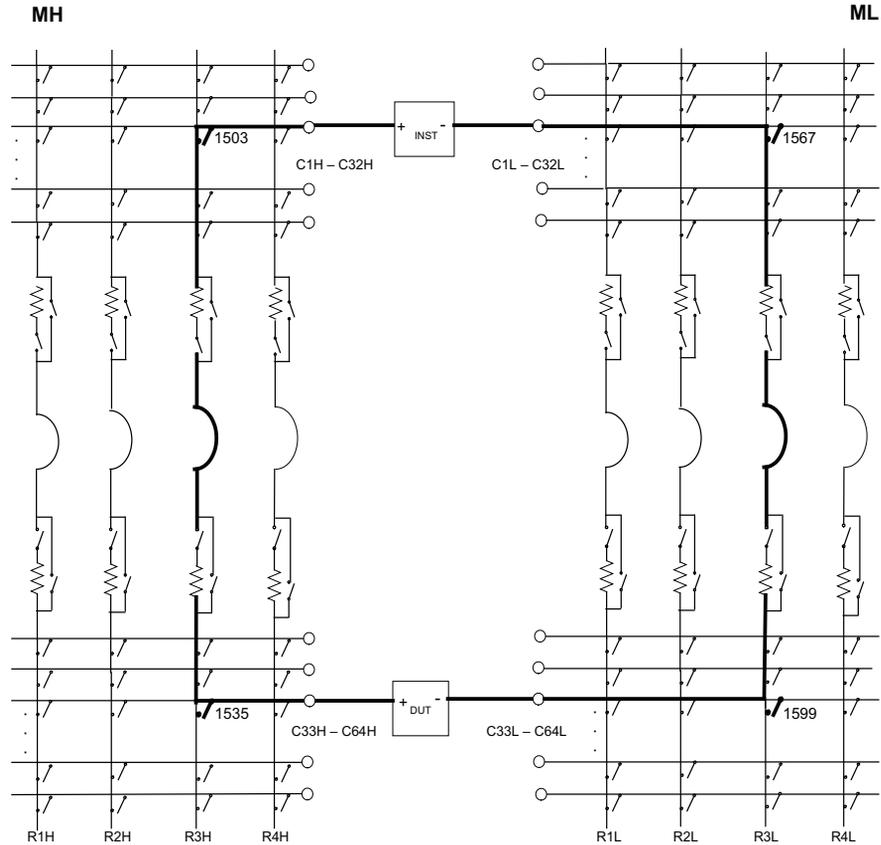
The example below shows a 2-wire circuit path through the matrix.

This example illustrates one of the SCPI paired commands; **ROUTE:CLOSE:PAIR**. To close two crosspoints in 2-wire operation, you specify two H crosspoints; the paired command also closes the two L crosspoints.

The complete circuit includes four 100Ω series Row-Protection Resistors with Bypass Relays. As with any path which makes a row connection (or which

1 Overview of the 34934A

connects columns from separate 32-column banks), you can program the mode for the row protection resistor and bypass relay.



ROUT: CLOS: PAIR (@1503, 1535)
 Paired channels are: 1567 1599
 Closes: row R3H @ columns C3H and C35H
 row R3L @ columns C3L and C35L

See “Channel Numbering” on page 168 for the numbering scheme.

4x128 Matrices

4x128 Topology

You can select the 4x128 matrix configuration by setting jumpers on the appropriate terminal block or configuration block, or by D-Sub wiring.

Once configured for 4x128, the 34934A creates a single matrix, with 512 crosspoint non-latching reed relays organized in a 4-row by 128-column configuration (greatest number of columns available using a single 34934A module). On the 34934T-001 terminal block, the columns are numbered 1-128; the rows R1-R4. In the D-Sub pin assignment tables, the numbering is C1-C128 and R1-R4.

The matrix is created by jumpering the rows on all four of the 4x32 relay banks together. To do this, you will either place physical “CONFIG” jumpers on a terminal or configuration block, or create wiring jumpers by shorting pins between your D-Sub cables. The row jumpers are shown on the example.

Each bank connects to the next through one 100 Ω Row Protection Resistor (with Bypass Relay) per row. The examples which follow show multiple ways to connect your DUT and test instrument; each circuit path *may* include series resistance, depending on the connection and programmed mode for row protection (see “[Row Protection](#)” on page 22).

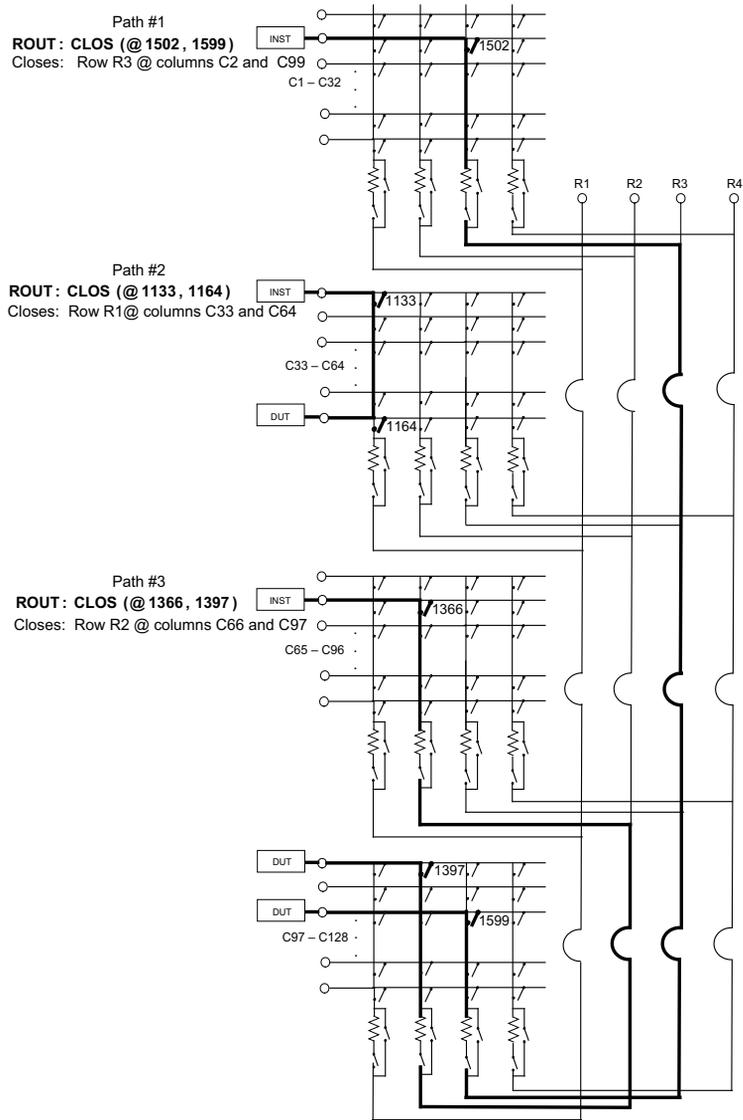
The 4x128 topology is the longest (in terms of columns) and most narrow (in terms of rows) of the matrix configurations. It is optimal for applications where a limited number of instrument connections are required for a large number of signal points (pins).

4x128 Test Circuit Example

The example below shows three circuit paths through the matrix.

Paths #1 and #3 connect columns on separate 32-column banks, so these paths each include two 100 Ω series Row-Protection Resistors with Bypass Relays. No more than 200 Ω can occur within any two crosspoints in the matrix. Path #2 shows the instrument and DUT connected within the same 32-column bank, not including the resistors and relays.

1 Overview of the 34934A



See "Channel Numbering" on page 168 for the numbering scheme.

8x32 Matrices

8x32 Topology

You can select the 8x32 matrix configuration by setting jumpers on the appropriate terminal block or configuration block, or by D-Sub wiring.

Once configured for 8x32, the 34934A creates two matrices, each with 256 crosspoint non-latching reed relays organized in an 8-row by 32-column configuration. You can *operate* these as two independent *1-wire* matrices (MH, ML) or as a single *2-wire* matrix.

In 1-wire operation, the two matrices are:

- MH: uses H (high) columns C1H-C32H and H rows R1H-R8H.
- ML: uses L (low) columns C1L-C32L and L rows R1L-R8L.

In 2-wire operation, a single matrix is formed by “pairing” MH and ML. The matrix uses both the H and L columns and rows; C1H-32H, C1L-C32L, R1H-R8H and R1L-R8L.

As the examples illustrate, no physical matrix configuration changes are required for 2-wire operation. You wire your test devices between corresponding high and low matrix terminals or D-Sub pins, then operate the matrix in 2-wire mode by using SCPI paired commands (e.g. ROUT:OPEN:PAIR).

In either mode of operation, each matrix (MH, ML) is created by jumpering the columns on two of the 4x32 relay banks together. This is done automatically through the “CONFIG” jumpers on the terminal and configuration blocks, or by shorting pins between your D-Sub cables. **The column jumpers are not indicated in the examples.**

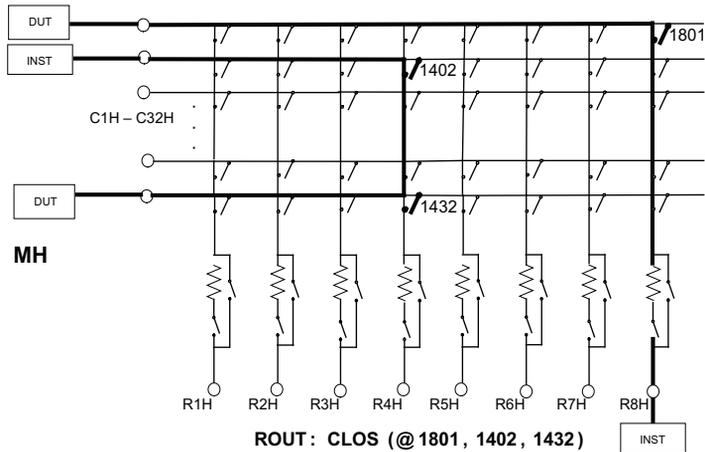
On the 34934T-002 terminal block, there are redundant “H” and “L” sets of terminals provided for columns 1-32, one set on each side of the block. The column terminals on each side of the block are shorted (by traces within the block) to their like-labeled counterparts on the other; you may make any column connection to the appropriate terminal on either side.

8x32 1-Wire Operation, Test Circuit Examples

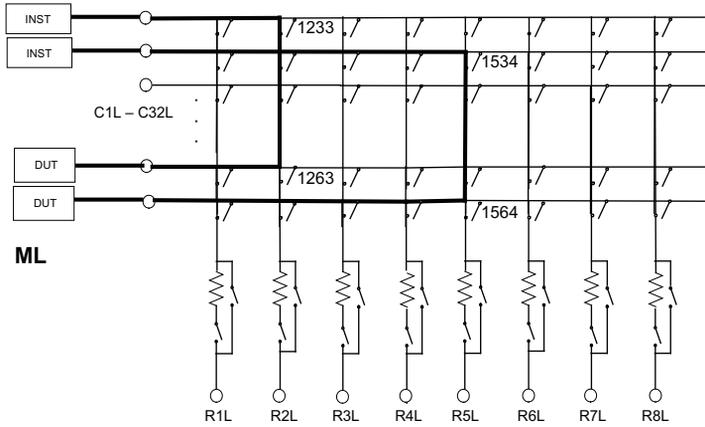
The two examples below each show two 1-wire circuit paths through the matrix. The upper example (MH) shows one path with the DUT and test instrument both connected to the columns; the other path shows the test instrument connected through a row protection resistor. The lower example (ML) shows both paths

1 Overview of the 34934A

connecting both DUT and test instrument to the columns, using the rows as connection busses.



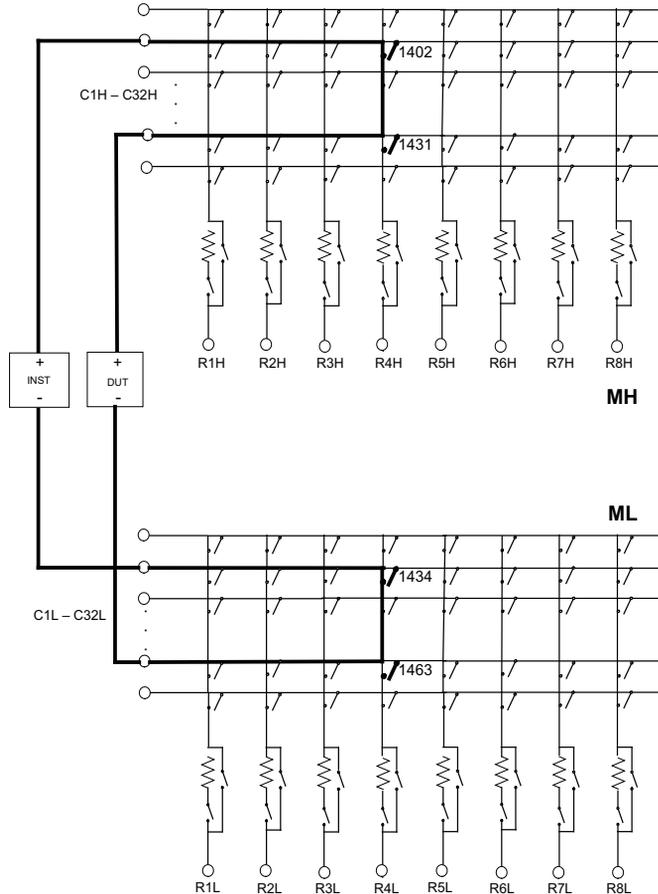
See “Channel Numbering” on page 168 for the numbering scheme.



8x32 2-Wire Operation, Test Circuit Example

The example below shows a 2-wire circuit path through the matrix, with the instrument and DUT wired between the H and L terminals.

This example illustrates one of the SCPI paired commands; **ROUTe:CLOSE:PAIR**. To close two crosspoints in 2-wire operation, you specify two H crosspoints; the paired command also closes the two L crosspoints.



ROUT: CLOS: PAIR (@1402, 1431)

Paired channels are: 1434 1463

Closes: row R4H @ columns C2H and C31H
row R4L @ columns C2L and C31L

See "Channel Numbering" on page 168 for the numbering scheme.

8x64 Matrices

8x64 Topology

You can select the 8x64 matrix configuration by setting jumpers on the appropriate terminal block or configuration block, or by D-Sub wiring.

Once configured for 8x64, the 34934A creates a single matrix, with 512 crosspoint non-latching reed relays organized in a 8-row by 64-column configuration. On the 34934T-002 terminal block, the columns are numbered 1-64; the rows R1-R8. In the pin assignment diagrams and tables, the numbering is C1-C64 and R1-R8.

The matrix is created by jumpering the rows between pairs of the 4x32 relay banks together, then jumpering the columns. This is done automatically through the “CONFIG” jumpers on the terminal and configuration blocks, or by shorting pins between your D-Sub cables. **The row jumpers are shown on the example; the column jumpers are not.**

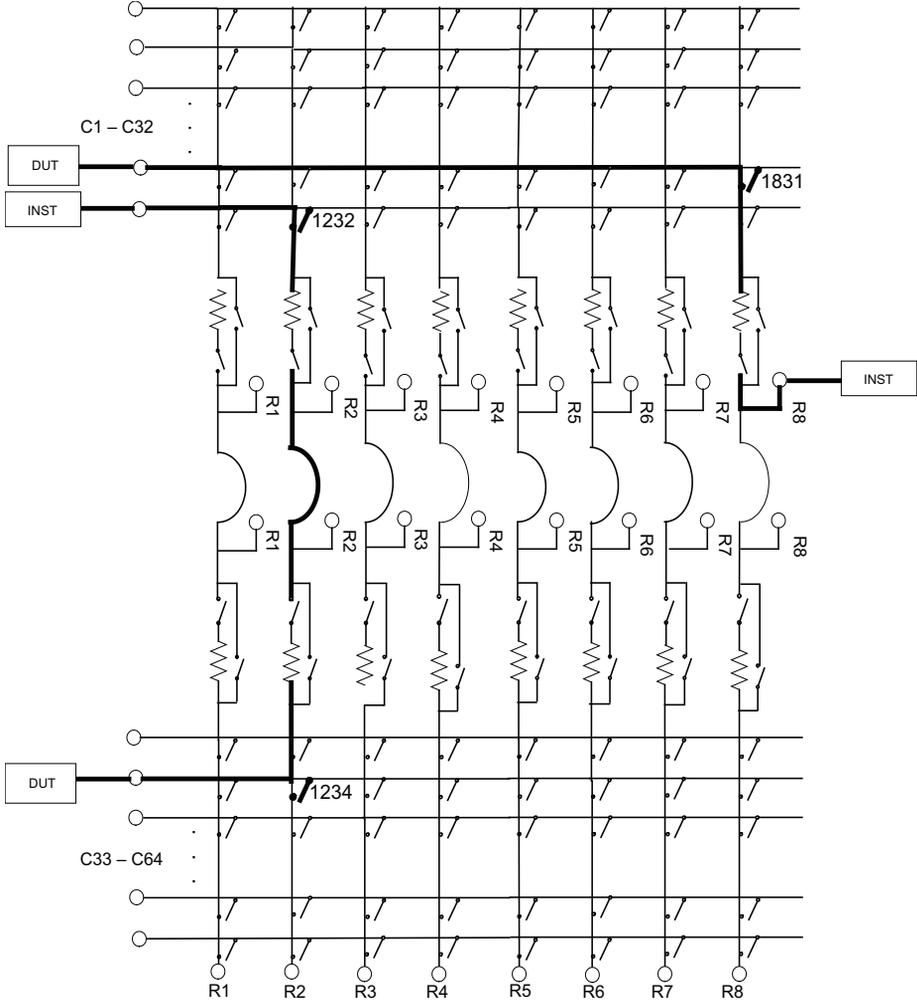
The row jumpers connect a bank to paired bank through one 100 Ω Row Protection Resistor (with Bypass Relay) per row. The example which follows show two ways to connect your DUT and test instrument; each circuit path *may* include series resistance, depending on the connection and programmed mode for row protection (see “[Row Protection](#)” on page 22).

On the 34934T-002 terminal block, there are redundant sets of terminals provided for columns 1-64, one set on each side of the block. The column terminals on each side of the block are shorted (by traces within the block) to their like-labeled counterparts on the other; you may make any column connection to the appropriate terminal on either side.

8x64 Test Circuit Example

The example below shows two circuit paths through the matrix.

The upper example path connects a DUT on column C31 to an instrument on row R8, through one 100 Ω series row-protection resistor. The lower path shows both DUT and instrument connected to columns using row R1, but through two resistors, as the two devices are on separate 32-column banks.



ROUT: CLOS (@ 1831, 1232, 1234)

Closes: row R8 @ column C31
row R2 @ columns C32 and C34

See "Channel Numbering" on page 168 for the numbering scheme.

16x32 Matrices

16x32 Topology

You can select the 16x32 matrix configuration by setting jumpers on the appropriate terminal block or configuration block, or by D-Sub wiring.

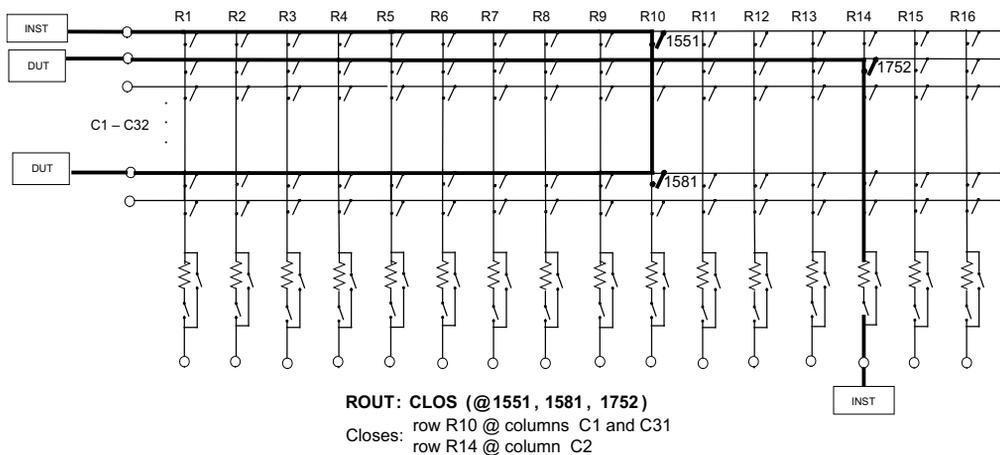
Once configured for 16x32, the 34934A creates a single matrix, with 512 crosspoint non-latching reed relays organized in a 16-row by 32-column configuration. On the 34934T-002 terminal block, the columns are numbered 1-32; the rows R1-R16. In the pin assignment diagrams and tables, the numbering is C1-C32 and R1-R16.

The matrix is created by jumpering the columns on all four of the 4x32 relay banks together. This is done automatically through the “CONFIG” jumpers on the terminal and configuration blocks, or by shorting pins between your D-Sub cables. **The column jumpers are not indicated in the example.**

On the 34934T-002 terminal block, there are redundant sets of terminals provided for columns 1-32, one set on each side of the block. Unlike the 8x32 and 8x64 modes, the like-labeled terminals on both sides of the block are not shorted to each other by traces in the block; you must supply a wire jumper for each column. Details on providing these jumpers are on [page 225](#). After these jumpers are placed, you may connect any column to the appropriate open terminal.

16x32 Test Circuit Example

The example below shows two circuit paths through the matrix. The upper path shows the DUT and test instrument both connected to columns through row R10; the other path shows the test instrument connected through a Row Protection Resistor on row R14. *That* circuit path *may* include series resistance, depending on the programmed mode for row protection (see “[Row Protection](#)” on page 22).



See "Channel Numbering" on page 168 for the numbering scheme.

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2 Base Matrix Configuration

4x32 Matrix Configuration	54
4x64 Matrix Configuration	89
4x128 Matrix Configuration	123
8x32 Matrix Configuration	154
8x64 Matrix Configuration	185
16x32 Matrix Configuration	218

This chapter contains six independent sections. Each contains complete configuration and wiring instructions for creating a selected base matrix using *one* 34934A module.

NOTE: For applications that require $> 10G\Omega$ isolation channel to channel, see [“Appendix A: Handling Considerations”](#) on page 281.

4x32 Matrix Configuration

To create a 4x32 matrix, you may connect your field wiring to the 34934A by three methods:

Terminal Block You can use terminal block 34934T-001. The terminal block connects to the 34934A's two D-Sub connectors; it provides 12 blocks of 12 screw terminals each, and a *4x32 removable overlay* (supplied with the 34934T-001) which provides guidance for field wiring the 4x32 matrices. Follow these sections in order to configure and wire the terminal block:

- “34934T-001 Terminal Block: 4x32 Layout” on page 55
- “34934T-001 Terminal Block: 4x32 Jumper Configuration” on page 60
- “34934T-001 Terminal Block: 4x32 Wiring” on page 64

Configuration Block You can use configuration block 34934C-001. Follow these sections in order to configure and wire the configuration block:

- “34934C-001 Configuration Block: Layout” on page 71
- “34934C-001 Configuration Block: 4x32 Jumper Configuration” on page 72
- “34934C-001 Configuration Block: Wiring” on page 78

WARNING

Do NOT use the 34934C-001 with the 34934T-001 terminal block for field wiring connections! This combination is not mechanically reliable and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring You can wire directly to the module's D-Sub connectors using custom cabling. You will have to short specific pins on and between the D-Sub connectors to create the 4x32 configuration. Review *both* these sections *before* wiring to the module's D-Subs:

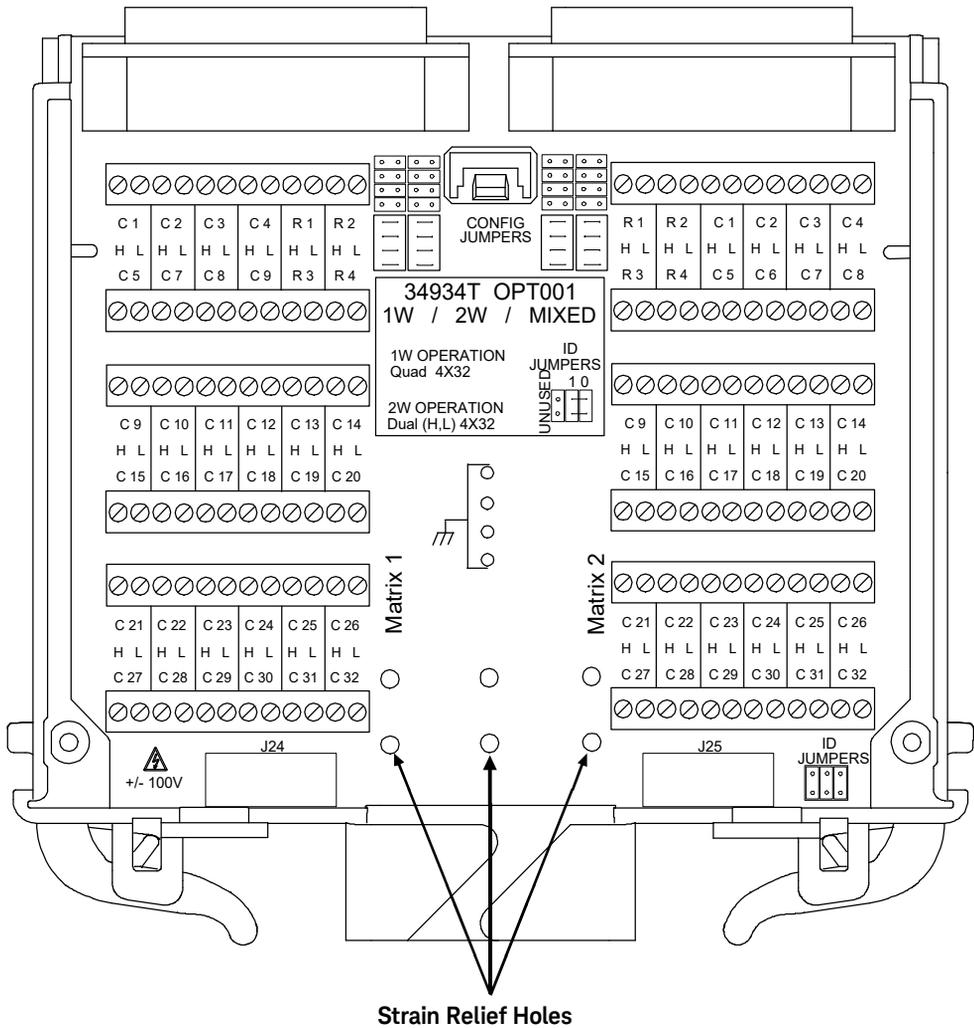
- “Configuring a 4x32 Matrix by Direct Wiring” on page 85
- “34934A Module D-Sub Connectors: 4x32 Wiring” on page 87

Configuring a 4x32 Matrix using a Terminal Block

34934T-001 Terminal Block: 4x32 Layout

This terminal block with screw-type connections is labeled with the model number and the abbreviated module name. In addition, space is available on the label for you to write the slot number.

The *4x32 removable overlay* (supplied with the 34934T-001) shows the row and column terminals for the 4x32 matrix configuration:



Note the six strain relief holes provided in the terminal block PCB. See the following page for instructions on providing wiring strain relief.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

WARNING

External Wiring for Hazardous Voltages:

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
- are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
- flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm^2 meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm^2 or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
- temperature rated for the application.
- classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)

WARNING

Terminal block strain relief

To reduce strain on the wiring at the terminals and maintain connection reliability, a strain relief feature has been incorporated into the terminal block design. Six holes have been placed near the lower edge of the circuit board, lined up with

holes in the terminal block's bottom cover, as seen on “34934T-001 Terminal Block: 4x32 Layout” on page 55.

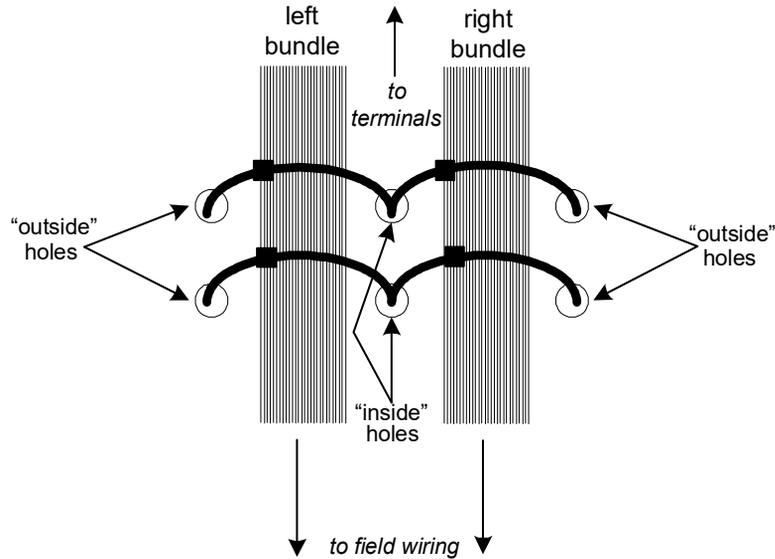
WARNING

Always include wiring strain relief to reduce strain on the connections. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

To use this feature:

- 1 Once all jumpers are installed and wiring is connected to the terminals, arrange the wires leading away from the terminal block in two bundles. This is easiest if the left bundle leads to the terminals on the left side of the block, and conversely for the right side.
- 2 Insert a cable tie (tie-wrap, zip-tie) down through each of the four outside holes in the PCB, continuing down through the holes in the terminal block's bottom cover.
- 3 Route these cable ties up through the center holes in the bottom cover and PCB.
- 4 Close and tighten the cable ties as required, and remove any excess so that it does not interfere with cover closure.

The completed bundles are shown below.



34934T-001 Terminal Block: 4x32 Jumper Configuration

This terminal block has three types of jumpers which must be set to configure the 4x32 matrix (matrices):

- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four 4x2* blocks of jumper pins located near the board's female D-Sub connectors, as shown.
- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins on the lower right corner of the board, as shown.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 or equivalent custom wiring on the two 20-pin extension headers at the bottom of the terminal block, as shown.

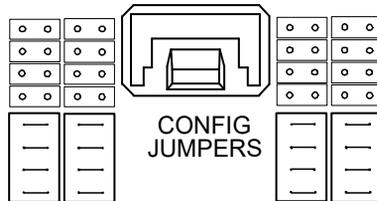
NOTE

In a high temperature–high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation $> 10 \text{ G}\Omega$ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

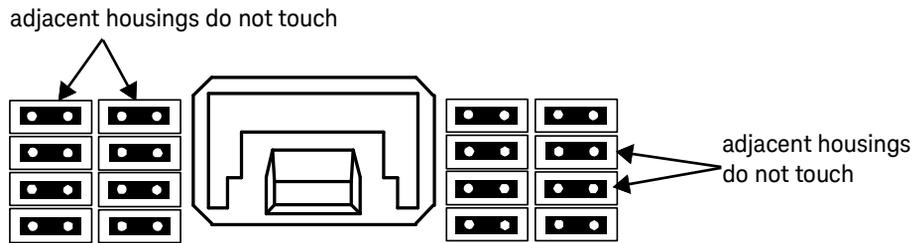
Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

Placing CONFIG Jumpers for 4x32 The CONFIG jumper area—shown below with jumpers removed— includes an illustration (on the 4x32 *removable overlay*) that shows jumper placement for setting a 4x32 matrix configuration.



There are two 4x2 blocks of CONFIG JUMPER pins on each side of the board's center—you *must* set all four blocks identically.

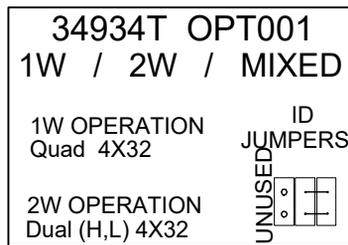
Place sixteen horizontally positioned jumpers as shown below.



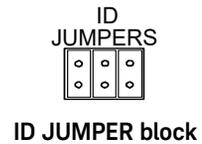
WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Placing ID Jumpers for 4x32 The ID jumpers allow the 34980A to recognize the 4x32 matrix configuration upon mainframe bootup. Jumper placement guidance is provided on the *4x32 removable overlay* (below left); the ID jumper block is shown at right with jumpers removed.



Location of placement guidance for ID Jumpers



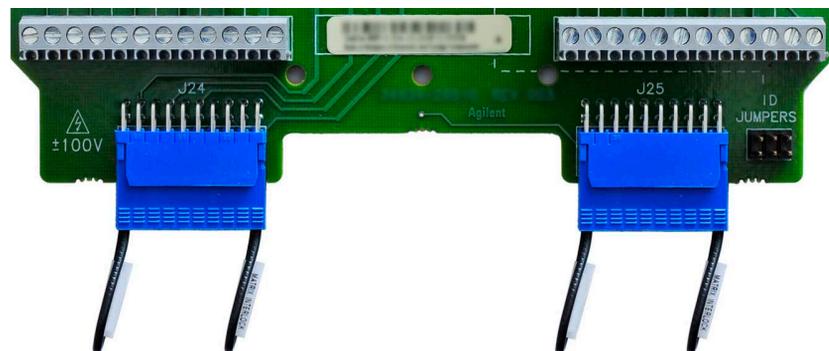
To set the correct ID code for 4x32, place two horizontally positioned jumpers as shown below.

**CAUTION**

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing Safety Interlock Continuity Jumpers for 4x32 At the bottom (end opposite the D-Sub connectors) of the terminal block are two 20-pin extension headers—marked J24 and J25 on the circuit board. These headers provide access to the module’s rows. Pins 19 and 20 on each header must be shorted to provide continuity for the module’s safety interlock function.

Two keyed 20-pin terminators 34934-80006 are supplied for providing safety interlock continuity. Install the terminators securely in the headers, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2. Detailed pin diagrams for these terminators are provided on [page 68](#).

34934T-001 Terminal Block: 4x32 Wiring

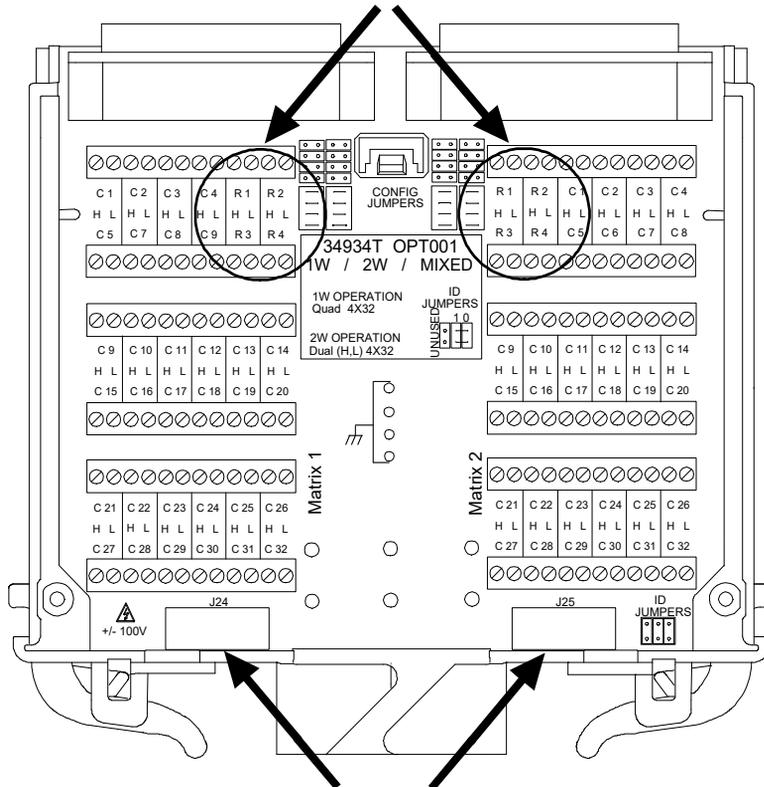
Once the required jumpers have been placed to configure the terminal block as 4x32, you are ready to make row and column connections to the terminal block.

You may connect to the module's rows and columns by wiring to the screw terminals marked C1 through C32 (H, L) and R1 through R4 (H, L) on the 4x32 *removable overlay* (see "[34934T-001 Terminal Block: 4x32 Layout](#)" on page 55). The combined weight of these wires can create significant strain on the wiring; to minimize this see "[Terminal block strain relief](#)" on page 58.

However, you may prefer to use physically separate cabling for the row connections (e.g. if you connect your DUT to the columns and your measurement device(s) to the rows). The terminal block's extension headers J24 and J25 provide access to the rows; you can make row connections to the central 16 terminals (pins 3-18) on the supplied terminators you installed in "[Placing Safety Interlock Continuity Jumpers for 4x32](#)" on page 63.

Below is an illustration of the possible locations for making row signal connections to the 34934T-001:

You can connect row signals to the screw terminals here...



...or to the central 16 pins of extension headers J24 and J25 here.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Wiring of the terminal block must be performed by qualified persons. A **MAXIMUM** of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

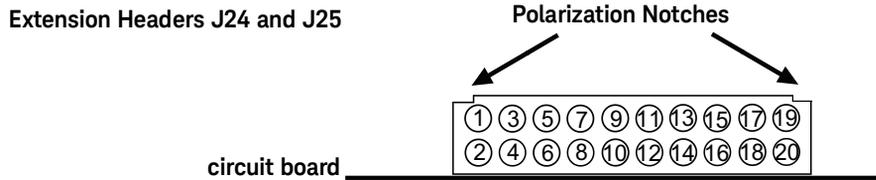
WARNING**External Wiring for Hazardous Voltages:**

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
 - are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
 - flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm^2 meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm^2 or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
 - temperature rated for the application.
 - classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)
-

The following subsection outlines how to make row connections to extension headers J24 and J25.

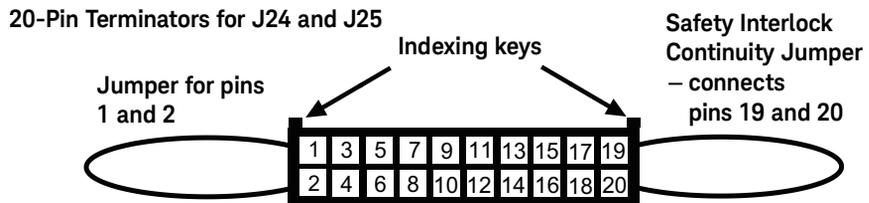
Extension Header (Row Signal) Wiring for 4x32 The position of extension headers J24 and J25 are marked on the 34934T-001's silk-screen. Oriented with the terminal side of the board up, the pin numbers for these headers are shown below.



The supplied 20-pin terminators 34934-80006 are identical, but the pin assignments for 4x32 are not (Matrix 1 rows are on J24; Matrix 2 rows are on J25). The terminal numbers—corresponding to the headers—are shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

External Wiring for Hazardous Voltages:

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
 - are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
 - flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm^2 meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm^2 or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
 - temperature rated for the application.
 - classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)
-

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934T-001: Extension Header Pin Assignments for 4x32

Header J24				Header J25			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No Connect	1	No Connect	2	No Connect	1	No Connect	2
M1 R1H	3	M1 R1H	4	M2 R1H	3	M2 R1H	4
M1 R1L	5	M1 R1L	6	M2 R1L	5	M2 R1L	6
M1 R2H	7	M1 R2H	8	M2 R2H	7	M2 R2H	8
M1 R2L	9	M1 R2L	10	M2 R2L	9	M2 R2L	10
M1 R3H	11	M1 R3H	12	M2 R3H	11	M2 R3H	12
M1 R3L	13	M1 R3L	14	M2 R3L	13	M2 R3L	14
M1 R4H	15	M1 R4H	16	M2 R4H	15	M2 R4H	16
M1 R4L	17	M1 R4L	18	M2 R4L	17	M2 R4L	18
IL1	19	IL2	20	IL3	19	IL4	20

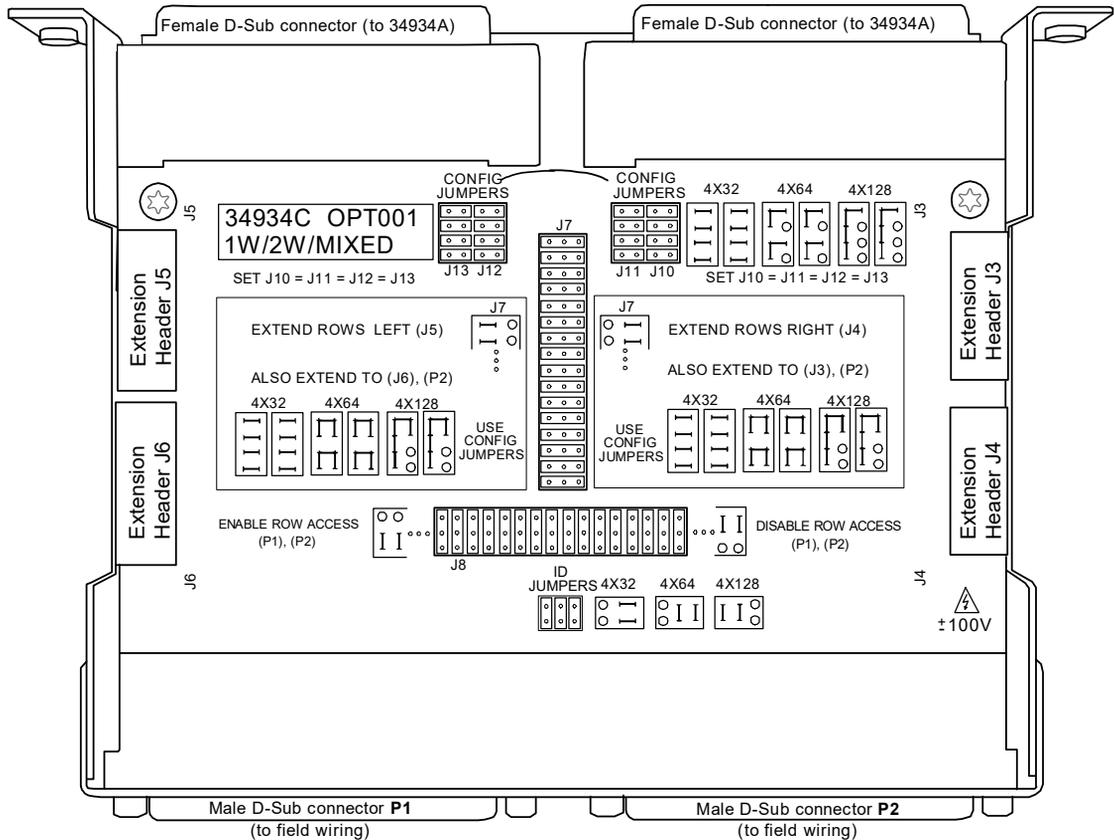
Note that each row signal (R1 through R4 (H,L) for each Matrix (1, 2) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going create an expanded-row matrix using multiple modules and terminal blocks, plan your wiring placement for each terminal block carefully, to allow for a daisy-chain interconnection between terminal blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Terminal Blocks”](#) on page 257.

Configuring a 4x32 Matrix using a Configuration Block

34934C-001 Configuration Block: Layout

This configuration block is labeled with the model number and the abbreviated module name.



Note four types of on-board jumpers you'll have to set to configure this board; some will be familiar from the configuration descriptions of the *terminal blocks*, but some are new. Instructions for setting these are provided in "[34934C-001 Configuration Block: 4x32 Jumper Configuration](#)" on page 72.

- ID Jumpers

- CONFIG Jumpers (J10, J11, J12 and J13)
- Row Extension Jumpers (J7)
- Row Access Jumpers (J8)

WARNING

Do not install substitute parts or perform any unauthorized modifications to the instrument to ensure the safety features are maintained in operational condition. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

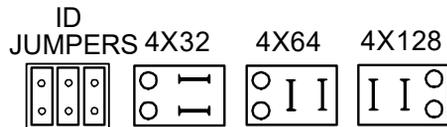
Also note the four extension headers (J3, J4, J5 and J6). Instructions for wiring to these are provided in “34934C-001 Configuration Block: Wiring” on page 78.

34934C-001 Configuration Block: 4x32 Jumper Configuration

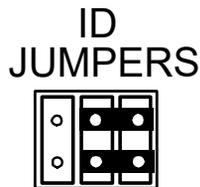
The configuration block has five types of jumpers which must be connected to configure it for use in an expanded 4-row matrix. In this section, you will place:

- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins near the bottom center of the board. See “Placing ID Jumpers on the 34934C-001 Configuration Block for 4x32” on page 73.
- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board’s female D-Sub connectors. See “Placing CONFIG Jumpers on the 34934C-001 Configuration Block for 4x32” on page 73.
- **Row Extension Jumpers:** place these 16-pin jumpers on a 16x3 block (J7) of jumper pins at the board’s center. See “Placing Row Extension Jumpers on the 34934C-001 Configuration Block” on page 75.
- **Row Access Jumpers:** place these 16-pin jumpers on a 3x16 block of jumper pins located just above the ID Jumpers. See “Placing Row Access Jumpers on the 34934C-001 Configuration Block” on page 76.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 on the four 20-pin extension headers at the sides of the configuration block. See “Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block” on page 77.

Placing ID Jumpers on the 34934C-001 Configuration Block for 4x32 The ID jumpers allow the 34980A to recognize the base matrix configuration used by the 34934C-001 upon mainframe bootup. The ID jumpers are placed in a 2x3 jumper block, located below jumper block J8. Jumper placement guidance is provided on the configuration block's silk-screen, to the right of the jumpers:



To set the correct ID code for 4x32, place two horizontally positioned jumpers in the right position, as shown below.



CAUTION

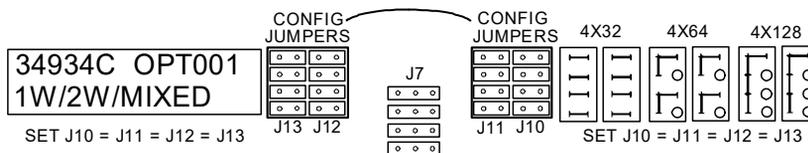
The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the following step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing CONFIG Jumpers on the 34934C-001 Configuration Block for 4x32

The CONFIG jumper area—shown below with jumpers removed— includes an illustration to its right that shows jumper placement for setting the 4x32, 4x64, and 4x128 base matrix configurations.

WARNING

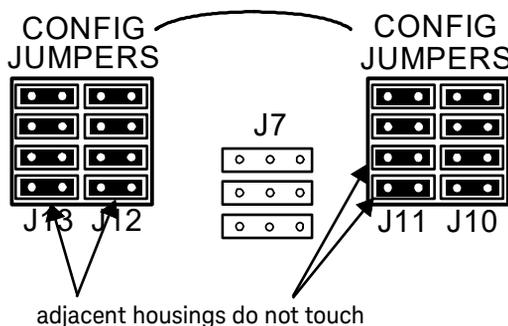
In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GW is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling. Do not use the alcohol on anywhere else of the 34980A and modules/accessories.



NOTE

The arc shown on the silk-screen—joining the two pairs of CONFIG JUMPER blocks—is a reminder that you *must* set all four blocks J10, J11, J12 and J13 identically.

For a base configuration of 4x32, place sixteen horizontally positioned jumpers as shown at right.



WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

This board configuration “extends” the four row signals either left to connectors J5 and J6 or right to connectors J3 and J4, depending on the placement of jumpers in block J7. The row signals will also be extended to male D-Sub connectors P1 and P2, if “enabled” by placement of jumpers in block J8.

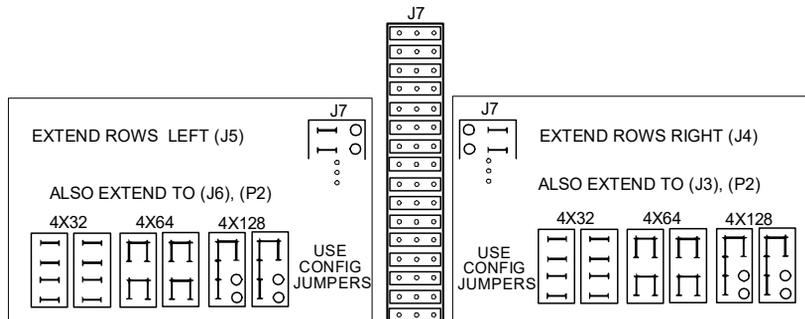
Placing Row Extension Jumpers on the 34934C-001 Configuration Block

The jumpers in 16x3 jumper block J7 (at board center) determine if row connections can be made from the left or right side of the board (i.e. they “extend” the row signal connection points out to *only one* of the two pairs of blue 20-pin connectors at the sides of the configuration block).

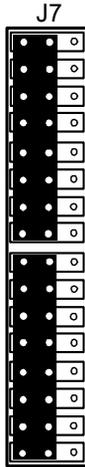
Note that “row connections” at minimum include the connections between modules in the matrix, but may also include your access from field wiring to the matrix rows.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling, depending on slot placement of the individual 34934A modules and their corresponding 34934C-001 blocks.

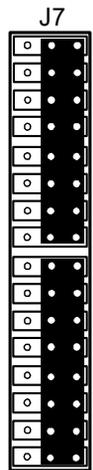
The following illustration shows the jumper block J7, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



If you plan to extend rows left, to J5 and J6, place two 2x8 jumpers as shown at right.



If you plan to extend rows right, to J3 and J4, place two 2x8 jumpers as shown at right.

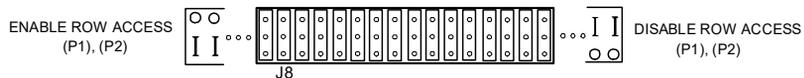


Placing Row Access Jumpers on the 34934C-001 Configuration Block The jumpers in 3x16 jumper block J8 (at lower board center) determine if row connections can be made from the two male D-Sub connectors P1 and P2 (i.e. they “enable” the row signal connection points at the D-subs).

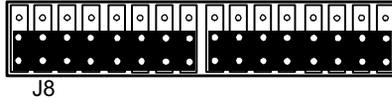
These “row connections” are the traditional access points from field wiring to the matrix rows. **In practice, your arrangement of test instrumentation and DUT test points will determine if you wish to make row connections at the D-subs, at the blue 20-pin connectors, or both.**

Setting these jumpers provides you the flexibility to plan how to route your custom cabling.

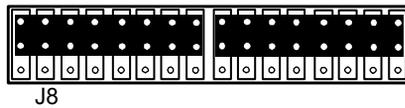
The following illustration shows the jumper block J8, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



If you plan to connect row signals to the D-sub connectors, place two 2x8 jumpers as shown below.



If you do not need to connect row signals to the D-sub connectors, place two 2x8 jumpers as shown below.



Note that by not routing signals to the D-sub connectors, module bandwidth may be preserved and capacitive loading minimized.

Placing Safety Interlock Continuity Jumpers on the 34934C-001

Configuration Block At each side of the configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. In all cases, pins 19 and 20 are the safety interlock pins.

These headers provide for row extension and row signal access, depending on the placement of the jumpers in J7. You must have set the jumpers to extend rows either to the left **or** right side of the configuration block (see [“Placing Row Extension Jumpers on the 34934C-001 Configuration Block”](#) on page 75).

To provide continuity for the module’s safety interlock function, pins 19 and 20 on *both* extension headers on the selected side (left or right) must be shorted.

- For each of these headers, install one of the supplied keyed 20-pin terminators 34934-80006 securely in that header, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2.

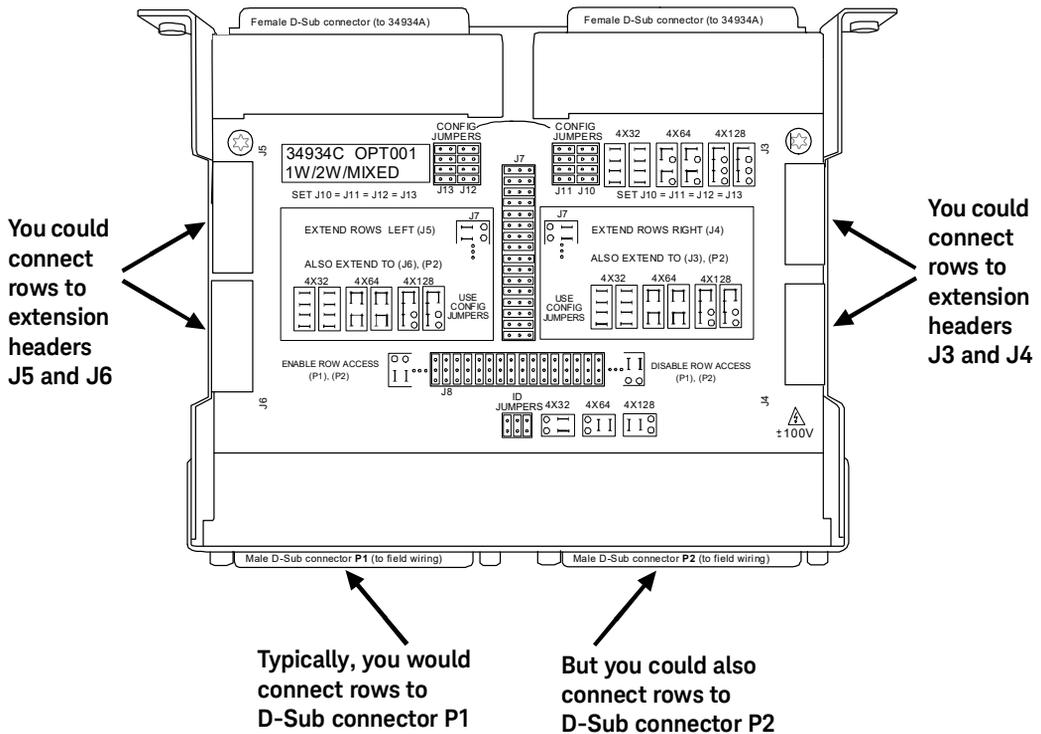
- The extension headers on the opposite (unused) side of the 34934C-001 do not require a safety interlock terminator; these will be hidden by the configuration block's cover.

34934C-001 Configuration Block: Wiring

The next two subsections outline options for making row and column wiring connections to the configuration block.

Extension Header (Row Signal) Wiring to the 34934C-001

Below is an illustration of the possible locations for making row signal connections to the 34934T-001



Typically, you could make both row and column connections to each module by fabricating custom cabling terminating in female D-Sub connectors. These attach to the configuration block's male D-Sub connectors P1 and P2.

Alternately, you may choose to connect only columns (typically test points on your DUT) to the D-Subs. There may be physical or electrical advantages in making your row connections (typically measurement devices) to the configuration block's blue extension connectors J3, J4, J5 and J6. To provide this flexibility, two extension headers are provided on each side of the configuration block.

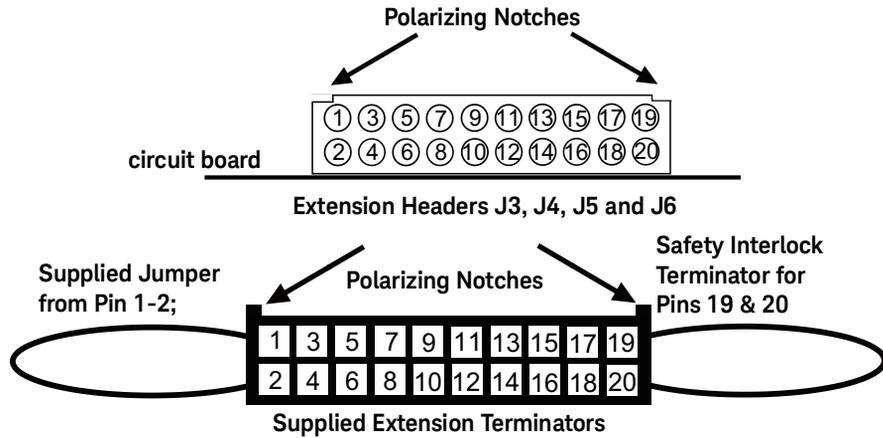
You must have set the jumpers (in ["Placing Row Extension Jumpers on the 34934C-001 Configuration Block"](#) on page 75) to extend rows to two of the four headers. We'll call these the "live" headers.

In ["Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block"](#) on page 77, you will have installed the supplied blue terminators 34934-80006 in the live headers on the configuration block.

You can now make row connections to these headers, by utilizing the central 16 pins (3-18) on the snap-in terminators. The extension header's supplied terminator is shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING**External Wiring for Hazardous Voltages:**

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
 - are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
 - flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm^2 meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm^2 or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
 - temperature rated for the application.
 - classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)
-

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

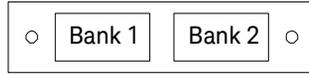
34934C-001: Extension Header Pin Assignments for 4x32

Headers J4, J5				Headers J3, J6			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No Connect	1	No Connect	2	No Connect	1	No Connect	2
M1 R1H	3	M1 R1H	4	M2 R1H	3	M2 R1H	4
M1 R1L	5	M1 R1L	6	M2 R1L	5	M2 R1L	6
M1 R2H	7	M1 R2H	8	M2 R2H	7	M2 R2H	8
M1 R2L	9	M1 R2L	10	M2 R2L	9	M2 R2L	10
M1 R3H	11	M1 R3H	12	M2 R3H	11	M2 R3H	12
M1 R3L	13	M1 R3L	14	M2 R3L	13	M2 R3L	14
M1 R4H	15	M1 R4H	16	M2 R4H	15	M2 R4H	16
M1 R4L	17	M1 R4L	18	M2 R4L	17	M2 R4L	18
IL1	19	IL2	20	IL3	19	IL4	20

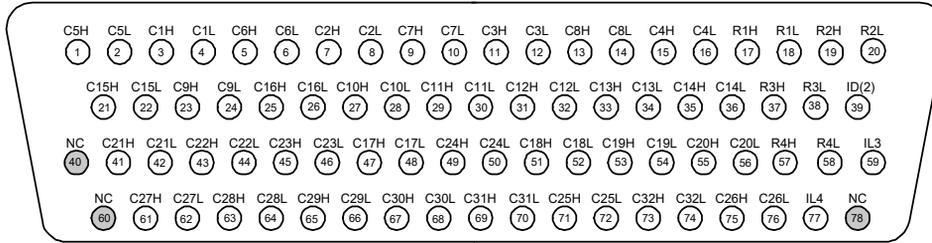
Note that each row signal (R1 through R4 (H,L) for each Matrix (1, 2) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going to create an expanded-row matrix using multiple modules and configuration blocks, plan your wiring placement for each configuration block carefully, to allow for a daisy-chain interconnection between configuration blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Configuration Blocks”](#) on page 261.

34934C-001 D-Sub Connectors: Pin Assignments for 4x32 (base configuration)



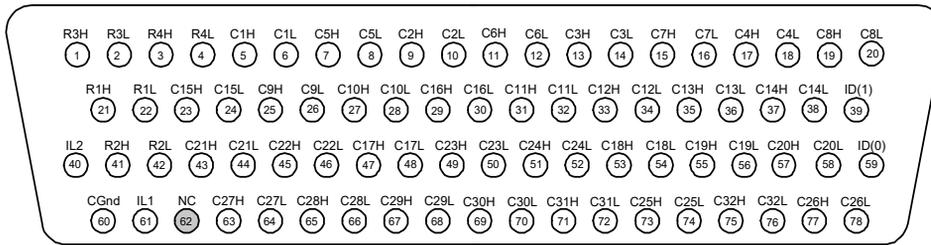
For orientation, the male D-sub connector end of the configuration block is facing you.



78-Pin D-Sub Male Connector
Bank 1 (P1)

Description	Pin										
R1H	17	C1H	3	C9H	23	C17H	47	C25H	71	ID2	39
R1L	18	C1L	4	C9L	24	C17L	48	C25L	72	IL3	59
R2H	19	C2H	7	C10H	27	C18H	51	C26H	75	IL4	77
R2L	20	C2L	8	C10L	28	C18L	52	C26L	76	No connect	40
R3H	37	C3H	11	C11H	29	C19H	53	C27H	61	No connect	60
R3L	38	C3L	12	C11L	30	C19L	54	C27L	62	No connect	78
R4H	57	C4H	15	C12H	31	C20H	55	C28H	63		
R4L	58	C4L	16	C12L	32	C20L	56	C28L	64		
		C5H	1	C13H	33	C21H	41	C29H	65		
		C5L	2	C13L	34	C21L	42	C29L	66		
		C6H	5	C14H	35	C22HL	43	C30H	67		
		C6L	6	C14L	36	C22L	44	C30L	68		
		C7H	9	C15H	21	C23H	45	C31H	69		
		C7L	10	C15L	22	C23L	46	C31L	70		
		C8H	13	C16H	25	C24H	49	C32H	73		
		C8L	14	C16L	26	C24L	50	C32L	74		

2 Base Matrix Configuration



78-Pin D-Sub
Male Connector

Bank 2 (P2)

Description	Pin										
R1H	21	C1H	5	C9H	25	C17H	47	C25H	73	ID1	39
R1L	22	C1L	6	C9L	26	C17L	48	C25L	74	ID0	59
R2H	41	C2H	9	C10H	27	C18H	53	C26H	77	IL1	61
R2L	42	C2L	10	C10L	28	C18L	54	C26L	78	IL2	40
R3H	1	C3H	13	C11H	31	C19H	55	C27H	63	Com Gnd	60
R3L	2	C3L	14	C11L	32	C19L	56	C27L	64	No connect	62
R4H	3	C4H	17	C12H	33	C20H	57	C28H	65		
R4L	4	C4L	18	C12L	34	C20L	58	C28L	66		
		C5H	7	C13H	35	C21H	43	C29H	67		
		C5L	8	C13L	36	C21L	44	C29L	68		
		C6H	11	C14H	37	C22H	45	C30H	69		
		C6L	12	C14L	38	C22L	46	C30L	70		
		C7H	15	C15H	23	C23H	49	C31H	71		
		C7L	16	C15L	24	C23L	50	C31L	72		
		C8H	19	C16H	29	C24H	51	C32H	75		
		C8L	20	C16L	30	C24L	52	C32L	76		

34934C-001 D-Sub Connectors: Safety Interlock Continuity Wiring for 4x32

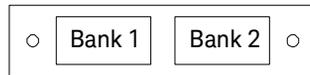
While wiring to the 34934C-001’s D-Sub connectors, you *must* make the following connections—in addition to the 4x32 row and column connections—to provide for safety interlock continuity:

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

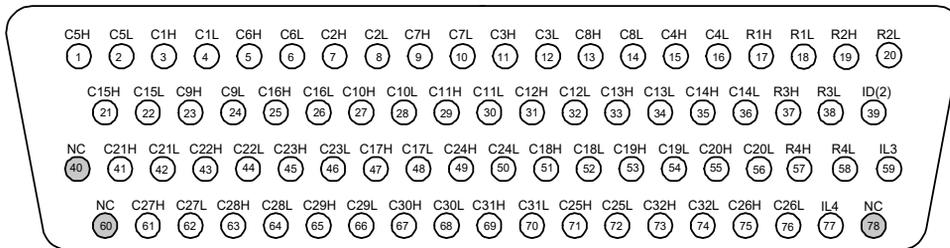
Configuring a 4x32 Matrix by Direct Wiring

34934A module D-Sub Connectors: 4x32 Pin Assignments



For orientation, the D-sub connector end of the module is facing you.

Connector and Pin Assignments for 4x32 Matrix 1H and Matrix 1L



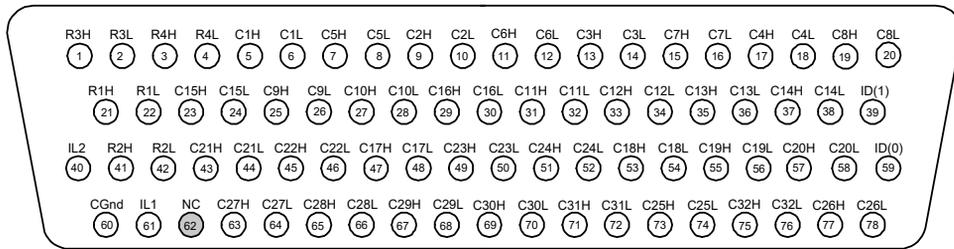
78-Pin D-Sub Male Connector
Bank 1

Description	Pin										
R1H	17	C1H	3	C9H	23	C17H	47	C25H	71	ID2	39
R1L	18	C1L	4	C9L	24	C17L	48	C25L	72	IL3	59
R2H	19	C2H	7	C10H	27	C18H	51	C26H	75	IL4	77
R2L	20	C2L	8	C10L	28	C18L	52	C26L	76	No connect	40
R3H	37	C3H	11	C11H	29	C19H	53	C27H	61	No connect	60
R3L	38	C3L	12	C11L	30	C19L	54	C27L	62	No connect	78

2 Base Matrix Configuration

Description	Pin										
R4H	57	C4H	15	C12H	31	C20H	55	C28H	63		
R4L	58	C4L	16	C12L	32	C20L	56	C28L	64		
		C5H	1	C13H	33	C21H	41	C29H	65		
		C5L	2	C13L	34	C21L	42	C29L	66		
		C6H	5	C14H	35	C22H	43	C30H	67		
		C6L	6	C14L	36	C22L	44	C30L	68		
		C7H	9	C15H	21	C23H	45	C31H	69		
		C7L	10	C15L	22	C23L	46	C31L	70		
		C8H	13	C16H	25	C24H	49	C32H	73		
		C8L	14	C16L	26	C24L	50	C32L	74		

Connector and Pin Assignments for 4x32 Matrix 2H and Matrix 2L



Description	Pin										
R1H	21	C1H	5	C9H	25	C17H	47	C25H	73	ID1	39
R1L	22	C1L	6	C9L	26	C17L	48	C25L	74	ID0	59
R2H	41	C2H	9	C10H	27	C18H	53	C26H	77	IL1	61
R2L	42	C2L	10	C10L	28	C18L	54	C26L	78	IL2	40
R3H	1	C3H	13	C11H	31	C19H	55	C27H	63	Com Gnd	60
R3L	2	C3L	14	C11L	32	C19L	56	C27L	64	No connect	62

Description	Pin										
R4H	3	C4H	17	C12H	33	C20H	57	C28H	65		
R4L	4	C4L	18	C12L	34	C20L	58	C28L	66		
		C5H	7	C13H	35	C21H	43	C29H	67		
		C5L	8	C13L	36	C21L	44	C29L	68		
		C6H	11	C14H	37	C22H	45	C30H	69		
		C6L	12	C14L	38	C22L	46	C30L	70		
		C7H	15	C15H	23	C23H	49	C31H	71		
		C7L	16	C15L	24	C23L	50	C31L	72		
		C8H	19	C16H	29	C24H	51	C32H	75		
		C8L	20	C16L	30	C24L	52	C32L	76		

34934A Module D-Sub Connectors: 4x32 Wiring

When wiring directly to the 34934A's D-Sub connectors, you *must* make the following connections—in addition to row and column connections—to configure the 4x32 matrix (matrices):

Module ID: The open pins listed below allow the 34980A mainframe to recognize the 4x32 matrix configuration.

Leave Open: Pin 39 on Bank 1 (ID bit 2)

Leave Open: Pin 39 on Bank 2 (ID bit 1)

Leave Open: Pin 59 on Bank 2 (ID bit 0)

Safety Interlock Continuity: The connections listed below provide for safety interlock continuity.

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

NOTE

If you use the 34934C-001 Configuration Block—in conjunction with the 34934A module—to make wiring connections, the additional **Module ID** connections are *not required*. Those connections are made within the hardware of the configuration block. You would need only make the **Safety Interlock Continuity** connections.

4x64 Matrix Configuration

To create a 4x64 matrix, you may connect your field wiring to the 34934A by three methods:

Terminal Block You can use terminal block 34934T-001. The terminal block connects to the 34934A's two D-Sub connectors; it provides 12 blocks of 12 screw terminals each and silk-screened guidance for field wiring the 4x64 matrices. Follow these sections in order to configure and wire the terminal block:

- “34934T-001 Terminal Block: 4x64 Layout” on page 90
- “34934T-001 Terminal Block: 4x64 Jumper Configuration” on page 95
- “34934T-001 Terminal Block: 4x64 Wiring” on page 98

Configuration Block You can use configuration block 34934C-001. Follow these sections in order to configure and wire the configuration block:

- “34934C-001 Configuration Block: Layout” on page 104
- “34934C-001 Configuration Block: 4x64 Jumper Configuration” on page 106
- “34934C-001 Configuration Block: Wiring” on page 112

WARNING

Do NOT use the 34934C-001 with the 34934T-001 terminal block for field wiring connections! This combination is not mechanically reliable and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring You can wire directly to the module's D-Sub connectors using custom cabling. You will have to short specific pins on and between the D-Sub connectors to create the 4x64 configuration. Review *both* these sections *before* wiring to the module's D-Subs:

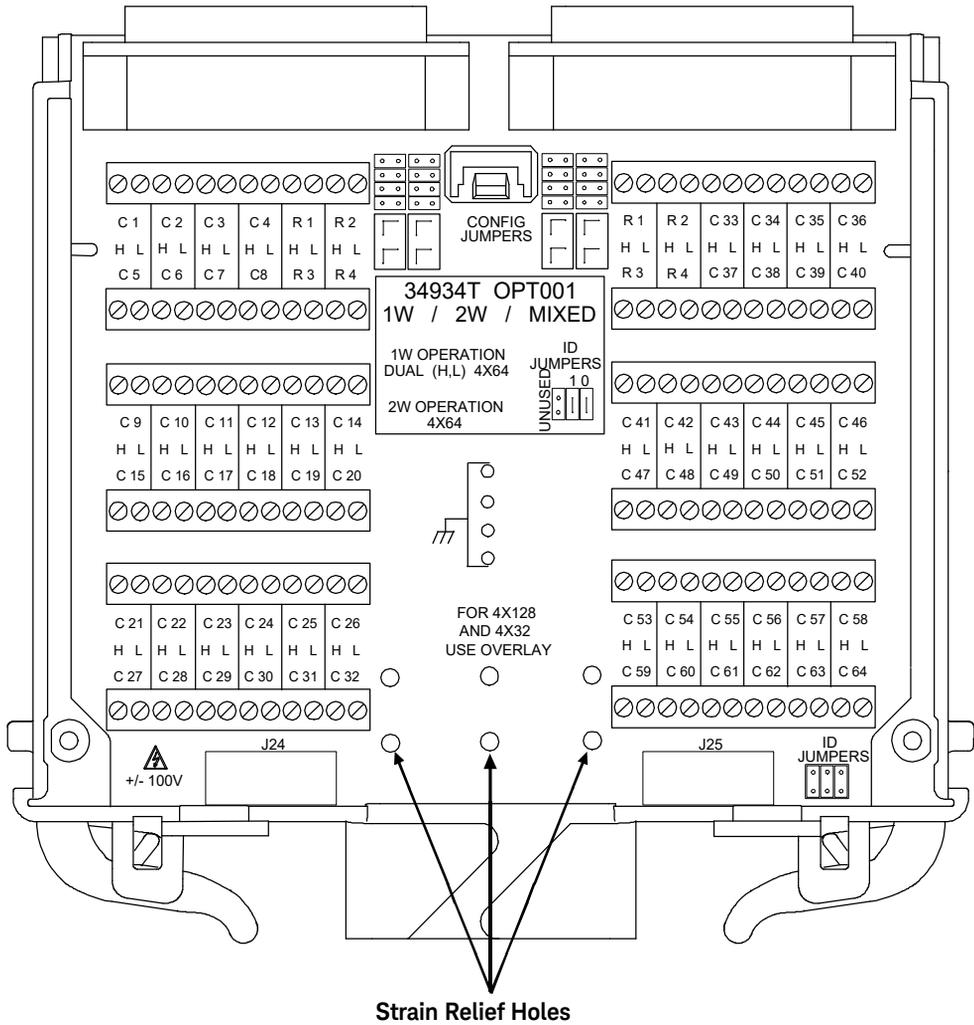
- “34934A D-Sub Connectors: 4x64 Pin Assignments” on page 119
- “34934A Module D-Sub Connectors: 4x64 Wiring” on page 121

Configuring a 4x64 Matrix using a Terminal Block

34934T-001 Terminal Block: 4x64 Layout

This terminal block with screw-type connections is labeled with the model number and the abbreviated module name. In addition, space is available on the label for you to write the slot number.

This terminal block's silk-screen shows the row and column terminals for the 4x64 matrix configuration:



Note the six strain relief holes provided in the terminal block PCB. See the following page for instructions on providing wiring strain relief.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Wiring of the terminal block must be performed by qualified persons. A **MAXIMUM** of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

WARNING**External Wiring for Hazardous Voltages:**

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
 - are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
 - flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm² meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm² or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
 - temperature rated for the application.
 - classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)
-

Terminal block strain relief

To reduce strain on the wiring at the terminals and maintain connection reliability, a strain relief feature has been incorporated into the terminal block design. Six holes have been placed near the lower edge of the circuit board, lined up with holes in the terminal block's bottom cover, as seen on [“34934T-001 Terminal Block: 4x64 Layout”](#) on page 90.

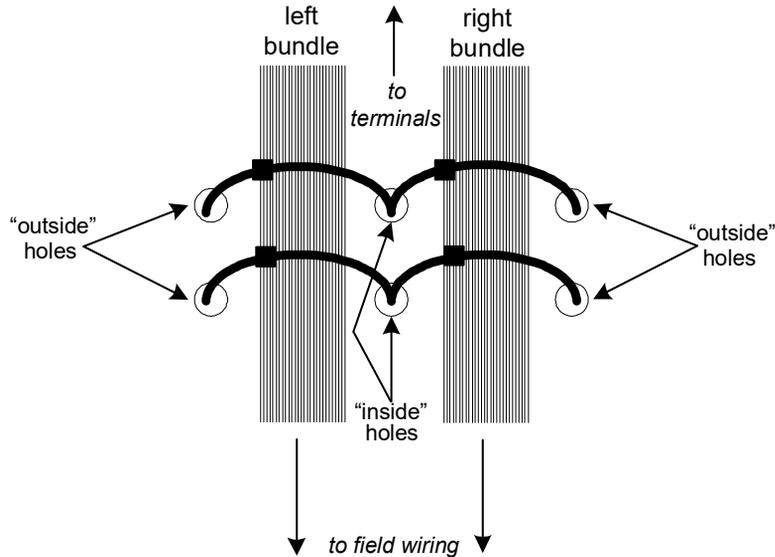
WARNING

Always include wiring strain relief to reduce strain on the connections. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

To use this feature:

- 1** Once all jumpers are installed and wiring is connected to the terminals, arrange the wires leading away from the terminal block in two bundles. This is easiest if the left bundle leads to the terminals on the left side of the block, and conversely for the right side.
- 2** Insert a cable tie (tie-wrap, zip-tie) down through each of the four outside holes in the PCB, continuing down through the holes in the terminal block's bottom cover.
- 3** Route these cable ties up through the center holes in the bottom cover and PCB.
- 4** Close and tighten the cable ties as required, and remove any excess so that it does not interfere with cover closure.

The completed bundles are shown below.



34934T-001 Terminal Block: 4x64 Jumper Configuration

This terminal block has three types of jumpers which must be connected to configure the 4x64 matrix (matrices):

- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four 4x2 blocks* of jumper pins located near the board's female D-Sub connectors, as shown.
- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins on the lower right corner of the board, as shown.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 or equivalent custom wiring on the two 20-pin extension headers at the bottom of the terminal block, as shown.

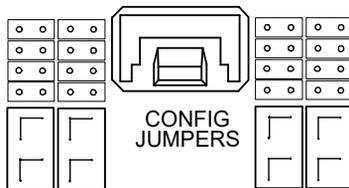
NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation $> 10\text{ G}\Omega$ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

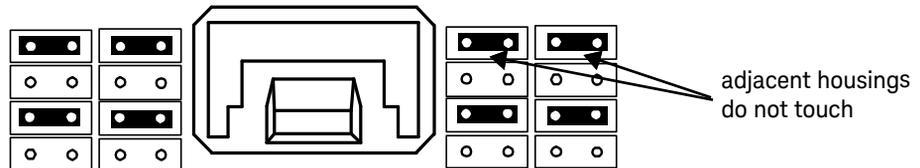
Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

Placing CONFIG Jumpers for 4x64 The CONFIG jumper area—shown below with jumpers removed— includes an illustration (on the silk-screen) that shows jumper placement for setting a 4x64 matrix configuration.



There are two 4x2 blocks of CONFIG JUMPER pins on each side of the board's center—you *must* set all four blocks identically.

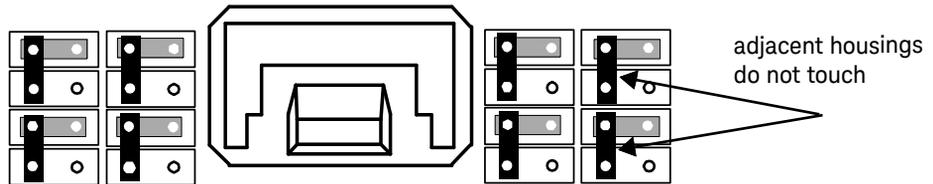
- 1 Place eight horizontally positioned jumpers as shown below.



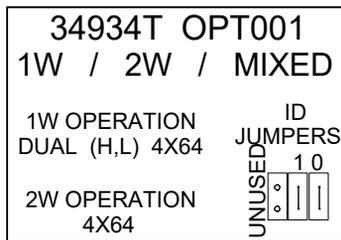
WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

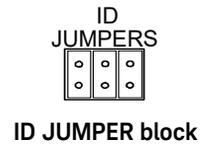
- Then *stack* eight vertically positioned jumpers *above* the first set of eight, as shown below. Ensure that adjacent jumpers (housings) do not touch.



Placing ID Jumpers for 4x64 The ID jumpers allow the 34980A to recognize the 4x64 matrix configuration upon mainframe bootup. Jumper placement guidance is provided on the silk-screen (below left); the ID jumper block is shown at right with jumpers removed.



Location of placement guidance for ID Jumpers



To set the correct ID code for 4x64, place two vertically positioned jumpers as shown below.

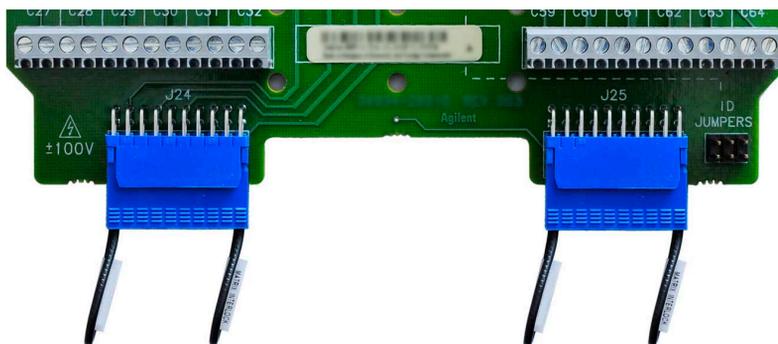


CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing Safety Interlock Continuity Jumpers for 4x64 At the bottom (end opposite the D-Sub connectors) of the terminal block are two 20-pin extension headers—marked J24 and J25 on the circuit board. These headers provide access to the module’s rows. Pins 19 and 20 on each header must be shorted to provide continuity for the module’s safety interlock function.

Two keyed 20-pin terminators 34934-80006 are supplied for providing safety interlock continuity. Install the terminators securely in the headers, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2. Detailed pin diagrams for these terminators are provided on [page 104](#).

34934T-001 Terminal Block: 4x64 Wiring

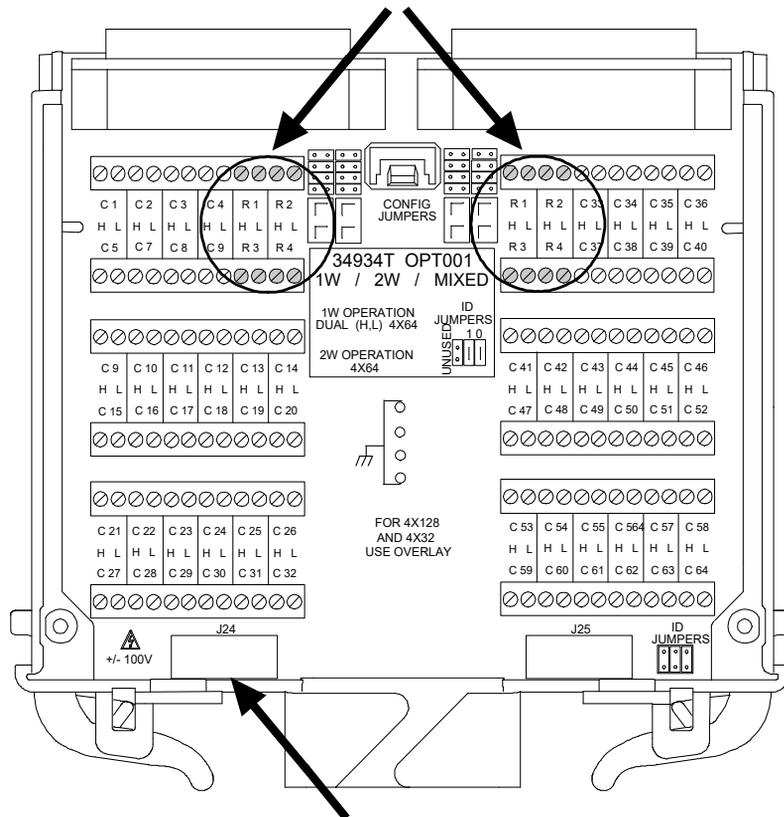
Once the required jumpers have been placed to configure the terminal block as 4x64, you are ready to make row and column connections to the terminal block.

You may connect to the module’s rows and columns by wiring to the screw terminals marked C1 through C64 (H, L) and R1 through R4 (H, L) on the silk-screen. The combined weight of these wires can create significant strain on the wiring; to minimize this see [“Terminal block strain relief”](#) on page 93.

However, you may prefer to use physically separate cabling for the row connections (e.g. if you connect your DUT to the columns and your measurement device(s) to the rows). The terminal block's extension headers J24 and J25 provide access to the rows; you can make row connections to the central 16 terminals (pins 3-18) on the supplied terminators you installed in **“Placing Safety Interlock Continuity Jumpers for 4x64”** on page 98.

Below is an illustration of the possible locations for making row signal connections to the 34934T-001:

You can connect row signals to the screw terminals here...



...or to the central 16 pins of extension header J24 here.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Wiring of the terminal block must be performed by qualified persons. A **MAXIMUM** of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

WARNING**External Wiring for Hazardous Voltages:**

To ensure minimum safety insulation when wiring with hazardous voltages, ensure all wiring (both internal and external to the instruments):

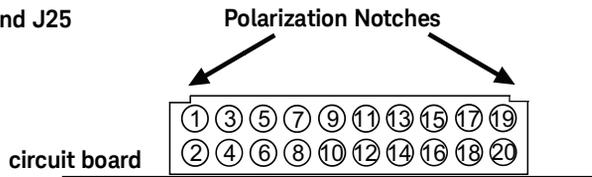
- follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.
 - are rated for at least 2 x the maximum applied voltage of the external source or any interconnecting system modules or external connections.
 - flame rated minimum:
 - Wires and cables with overall cross-sectional area of the conductors exceeding 0.5mm^2 meet test of IEC 60332-1-2 (IEC); or
 - Wires and cables with overall cross-sectional area of the conductors of 0.5mm^2 or less, the test of IEC 60332-2-2 (IEC); or
 - FT-1 of CSA C22.2 No. 0.3 (Canada); or
 - VW-1 of UL 1581 (US).
 - temperature rated for the application.
 - classified and suitable to be used external to the enclosure:
 - AWM Class II B or A/B – external/interconnecting wires (single- or multiple-conductor constructions with a jacket) and potentially subject to mechanical abuse (Canada)
 - AWM Style Use – external interconnection of electronic equipment or appliances (US)
-

The following subsection outlines how to make row connections to extension header J24.

Extension Header (Row Signal) Wiring for 4x64

The position of extension headers J24 and J25 are marked on the 34934T-001's silk-screen. Oriented with the terminal side of the board up, the pin numbers for these headers are shown below.

Extension Headers J24 and J25

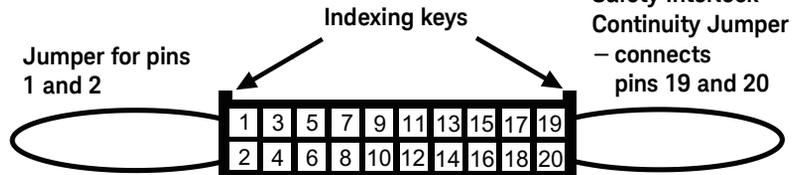


The supplied 20-pin terminators 34934-80006 are identical. For 4x64, you can only make row connections to header J24. The terminal numbers—corresponding to the headers—are shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

20-Pin Terminators for J24 and J25



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of header J24, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934T-001: Extension Header Pin Assignments for 4x64

Header J24		Header J25					
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1H	3	R1H	4	No connect	3	No connect	4
R1L	5	R1L	6	No connect	5	No connect	6
R2H	7	R2H	8	No connect	7	No connect	8
R2L	9	R2L	10	No connect	9	No connect	10
R3H	11	R3H	12	No connect	11	No connect	12
R3L	13	R3L	14	No connect	13	No connect	14
R4H	15	R4H	16	No connect	15	No connect	16
R4L	17	R4L	18	No connect	17	No connect	18
IL1	19	IL2	20	IL3	19	IL4	20

2 Base Matrix Configuration

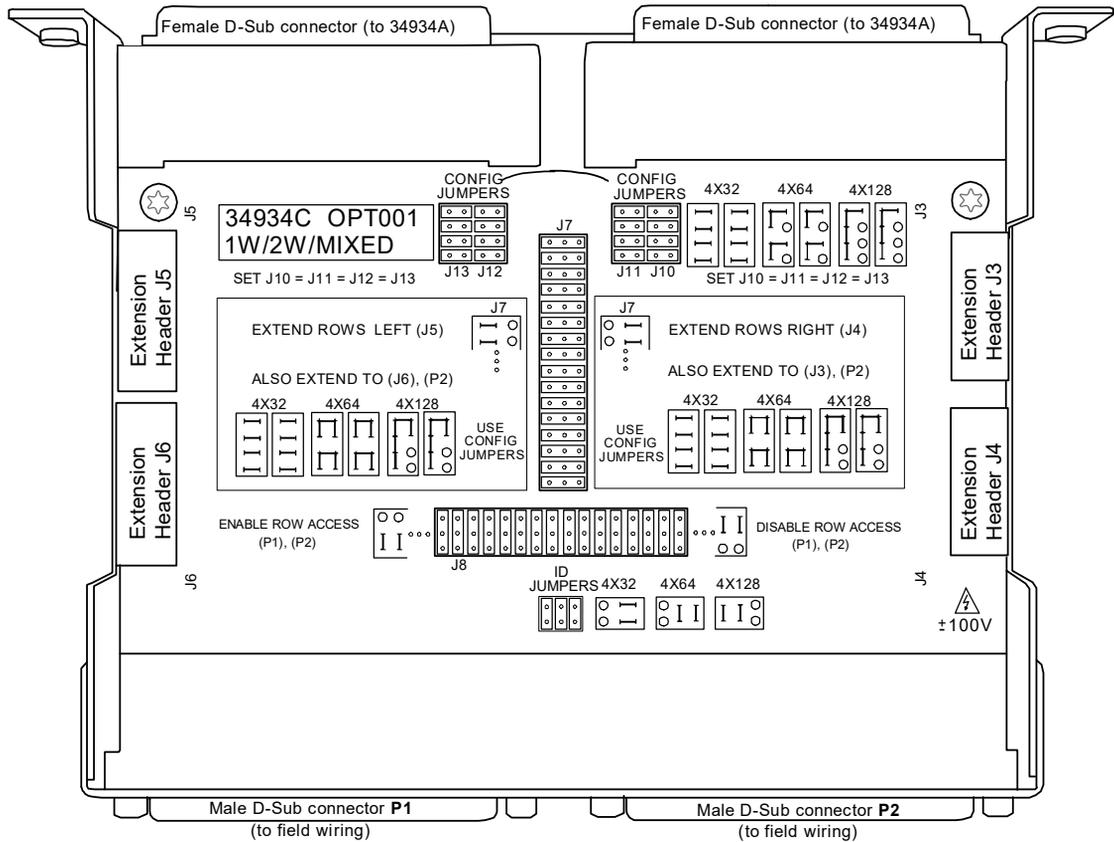
Note that each row signal (R1 through R4 (H,L) is routed out on two pins on J24. You can make row signal connections to either row (upper, lower) of the connector.

If you are going create an expanded-row matrix using multiple modules and terminal blocks, plan your wiring placement for each terminal block carefully, to allow for a daisy-chain interconnection between terminal blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Terminal Blocks”](#) on page 257.

Configuring a 4x64 Matrix using a Configuration Block

34934C-001 Configuration Block: Layout

This configuration block is labeled with the model number and the abbreviated module name.



Note four types of on-board jumpers you'll have to set to configure this board; some will be familiar from the configuration descriptions of the *terminal blocks*, but some are new. Instructions for setting these are provided in ["34934C-001 Configuration Block: 4x64 Jumper Configuration"](#) on page 106.

- ID Jumpers
- CONFIG Jumpers (J10, J11, J12 and J13)
- Row Extension Jumpers (J7)
- Row Access Jumpers (J8)

WARNING

Do not install substitute parts or perform any unauthorized modifications to the instrument to ensure the safety features are maintained in operational condition. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

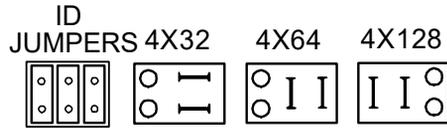
Also note the four extension headers (J3, J4, J5 and J6). Instructions for wiring to these are provided in [“34934C-001 Configuration Block: Wiring”](#) on page 112.

34934C-001 Configuration Block: 4x64 Jumper Configuration

The configuration block has five types of jumpers which must be connected to configure it for use in an expanded 4-row matrix. In this section, you will place:

- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins near the bottom center of the board. See [“Placing ID Jumpers on the 34934C-001 Configuration Block for 4x64”](#) on page 106.
- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board’s female D-Sub connectors. See [“Placing CONFIG Jumpers on the 34934C-001 Configuration Block for 4x64”](#) on page 107.
- **Row Extension Jumpers:** place these 16-pin jumpers on a 16x3 block (J7) of jumper pins at the board’s center. See [“Placing Row Extension Jumpers on the 34934C-001 Configuration Block”](#) on page 109.
- **Row Access Jumpers:** place these 16-pin jumpers on a 3x16 block of jumper pins located just above the ID Jumpers. See [“Placing Row Access Jumpers on the 34934C-001 Configuration Block”](#) on page 110.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 on the four 20-pin extension headers at the sides of the configuration block. See [“Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block”](#) on page 111.

Placing ID Jumpers on the 34934C-001 Configuration Block for 4x64 The ID jumpers allow the 34980A to recognize the base matrix configuration used by the 34934C-001, upon mainframe bootup. The ID jumpers are placed in a 2x3 jumper block, located below jumper block J8. Jumper placement guidance is provided on the configuration block’s silk-screen, to the right of the jumpers:



To set the correct ID code for 4x64, place two vertically positioned jumpers in the right position, as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the following step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing CONFIG Jumpers on the 34934C-001 Configuration Block for 4x64

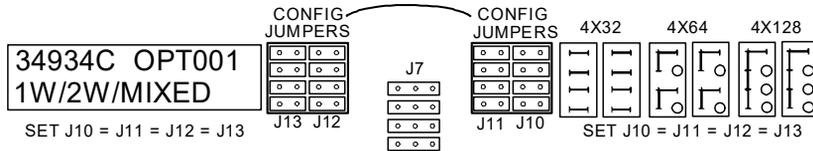
The CONFIG jumper area—shown below with jumpers removed— includes an illustration to its right that shows jumper placement for setting the 4x32, 4x64, and 4x128 base matrix configurations.

NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

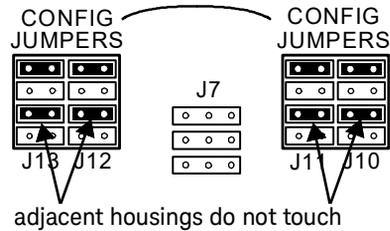
WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

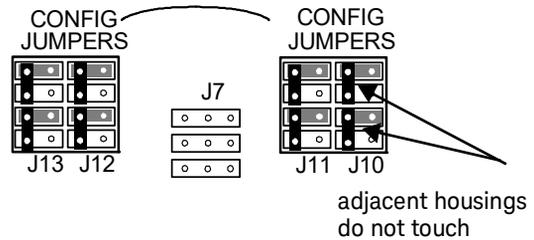


To set the CONFIG jumpers for 4x64:

- 1 Place eight horizontally positioned jumpers as shown at right.



- 2 Then stack eight vertically positioned jumpers above the first set, as shown at right.



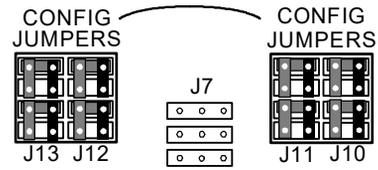
WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

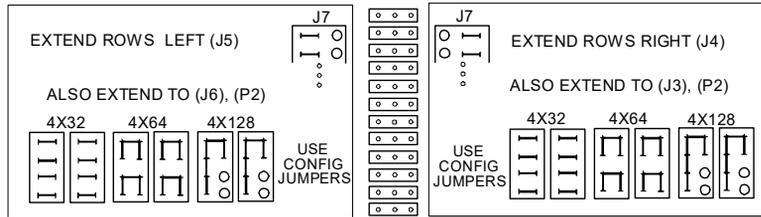
This board configuration (through step **2**) “extends” the four row signals either left to connector J5 or right to connector J4, depending on the placement of jumpers in block J7. The row signals will also be extended to male D-Sub connector P1, if “enabled” by placement of jumpers in block J8. *This provides typical row connectivity options.*

- 3 Optional Step:** For the 4x64 base configuration, *additional* jumpers must be placed to provide the following additional options for row connectivity:
- extend the row signals either left to connector J6 or right to connector J3, depending on the placement of jumpers in block J7.
 - extend the row signals to male D-Sub connector P2, if “enabled” by placement of jumpers in block J8.

If these additional row connectivity options are desired, stack eight additional vertically positioned jumpers as shown at right.



This optional step is implied by the silk-screened guidance provided on the board at either side of jumper block J7, illustrated below.



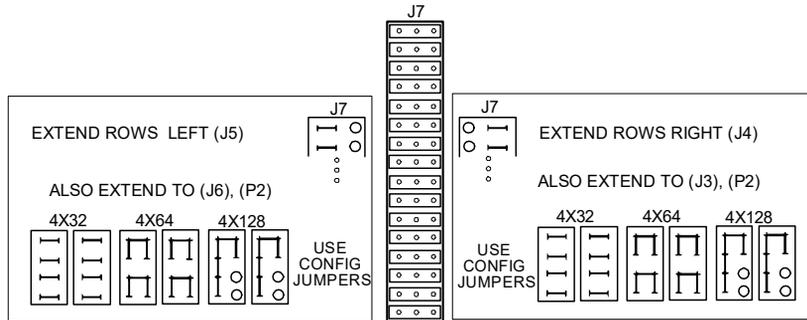
Placing Row Extension Jumpers on the 34934C-001 Configuration Block

The jumpers in 16x3 jumper block J7 (at board center) determine if row connections can be made from the left or right side of the board (i.e. they “extend” the row signal connection points out to *only one* of the two pairs of blue 20-pin connectors at the sides of the configuration block).

Note that “row connections” at minimum include the connections between modules in the matrix, but may also include your access from field wiring to the matrix rows.

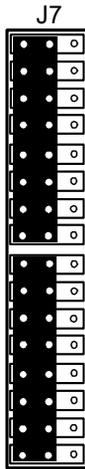
Setting these jumpers provides you the flexibility to plan how to route your custom cabling, depending on slot placement of the individual 34934A modules and their corresponding 34934C-001 blocks.

The following illustration shows the jumper block J7, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



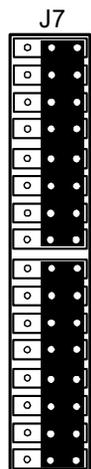
If you plan to extend rows *left*, to J5 and J6, place two 2x8 jumpers as shown at right.

The row signals will also be present on J6 *if* you placed the optional CONFIG jumpers in step 3 on [page 109](#).



If you plan to extend rows *right*, to J3 and J4, place two 2x8 jumpers as shown at right.

The row signals will also be present on J3 *if* you placed the optional CONFIG jumpers in step 3 on [page 109](#).

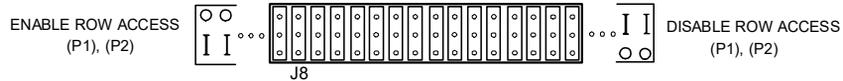


Placing Row Access Jumpers on the 34934C-001 Configuration Block The jumpers in 3x16 jumper block J8 (at lower board center) determine if row connections can be made from the two male D-Sub connectors P1 and P2 (i.e. they “enable” the row signal connection points at the D-subs).

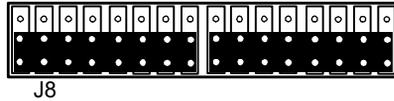
These “row connections” are the traditional access points from field wiring to the matrix rows. In practice, your arrangement of test instrumentation and DUT test points will determine if you wish to make row connections at the D-subs, at the blue 20-pin connectors, or both.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling.

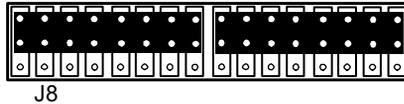
The following illustration shows the jumper block J8, along with the relevant guidance provided on the board's silk-screen for jumper placement:



If you plan to connect row signals to the D-sub connectors, place two 2x8 jumpers as shown below.



If you do not need to connect row signals to the D-sub connectors, place two 2x8 jumpers as shown below.



Note that by not routing signals to the D-sub connectors, module bandwidth may be preserved and capacitive loading minimized.

Placing Safety Interlock Continuity Jumpers on the 34934C-001

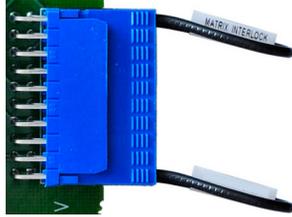
Configuration Block At each side of the configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. In all cases, pins 19 and 20 are the safety interlock pins.

These headers provide for row extension and row signal access, depending on the placement of the jumpers in J7. You must have set the jumpers to extend rows either to the left or right side (of the configuration block) (see [“Placing Row Extension Jumpers on the 34934C-001 Configuration Block”](#) on page 109).

To provide continuity for the module's safety interlock function, pins 19 and 20 on *both* extension headers on the selected side (left or right) must be shorted.

2 Base Matrix Configuration

- For each of these headers, install one of the supplied keyed 20-pin terminators 34934-80006 securely in that header, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2.

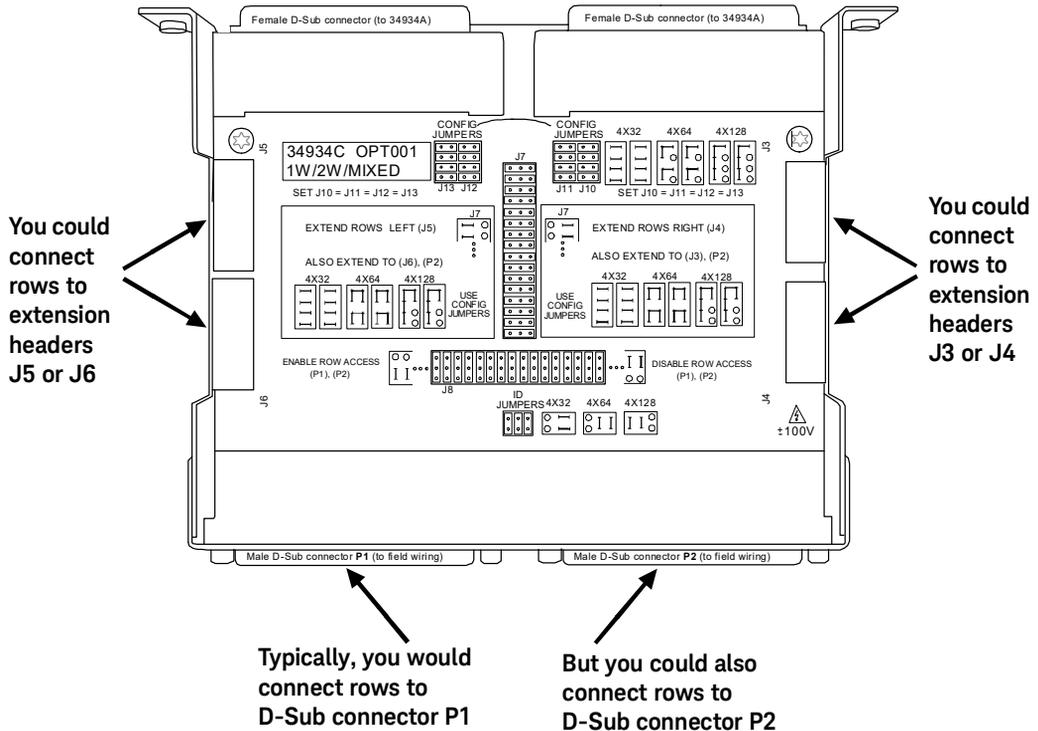
- The extension headers on the opposite (unused) side of the 34934C-001 do not require a safety interlock terminator; these will be hidden by the configuration block's cover.

34934C-001 Configuration Block: Wiring

The next two subsections outline options for making row and column wiring connections to the configuration block.

Extension Header (Row Signal) Wiring to the 34934C-001

Below is an illustration of the possible locations for making row signal connections to the 34934T-001:



Typically, you could make both row and column connections to each module by fabricating custom cabling terminating in female D-Sub connectors. These attach to the configuration block's male D-Sub connectors P1 and P2.

Alternately, you may choose to connect only columns (typically test points on your DUT) to the D-Subs. There may be physical or electrical advantages in making your *row* connections (typically measurement devices) to the configuration block's blue extension connectors J3, J4, J5 and J6. To provide this flexibility, two extension headers are provided on each side of the configuration block.

You must have set the jumpers (in "Placing Row Extension Jumpers on the 34934C-001 Configuration Block" on page 109) to extend rows to two of the four headers. We'll call these the "live" headers.

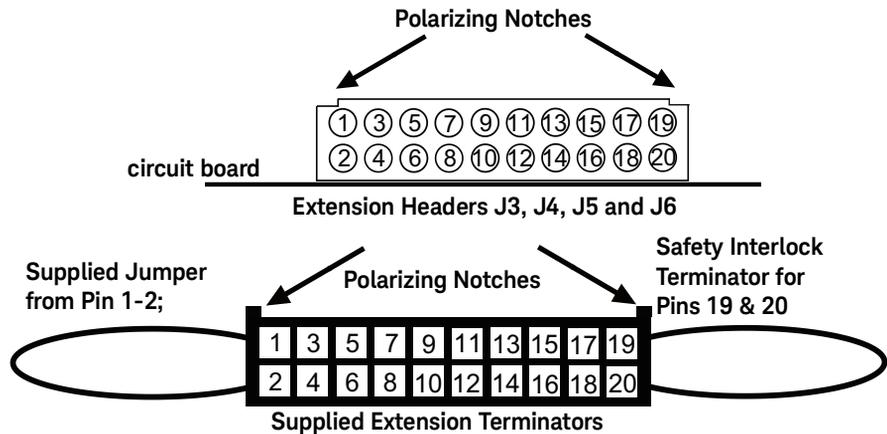
Had you created two live headers, by performing step 3 on [page 107](#); then the second header (J3 or J6) on the side selected by jumpers J7 will also be a live header.

In “[Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block](#)” on page 111, you will have installed the supplied blue terminators in the live headers on the configuration block.

You can now make row connections to these headers, by utilizing the central 16 pins (3-18) on the snap-in terminators. The extension header’s supplied terminator is shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

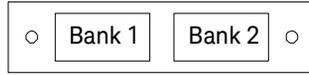
34934C-001: Extension Header Pin Assignments for 4x64

Description	Headers J4, J5		Headers J3, J6				
	Pin	Description	Pin	Description	Pin	Description	Pin
No Connect	1	No Connect	2	No Connect	1	No Connect	2
R1H	3	R1H	4	R1H	3	R1H	4
R1L	5	R1L	6	R1L	5	R1L	6
R2H	7	R2H	8	R2H	7	R2H	8
R2L	9	R2L	10	R2L	9	R2L	10
R3H	11	R3H	12	R3H	11	R3H	12
R3L	13	R3L	14	R3L	13	R3L	14
R4H	15	R4H	16	R4H	15	R4H	16
R4L	17	R4L	18	R4L	17	R4L	18
IL1	19	IL2	20	IL3	19	IL4	20

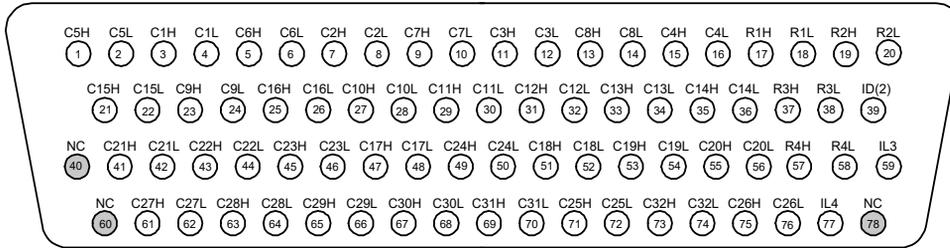
2 Base Matrix Configuration

If you are going to create an expanded-row matrix using multiple modules and configuration blocks, plan your wiring placement for each configuration block carefully, to allow for a daisy-chain interconnection between configuration blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Configuration Blocks”](#) on page 261.

34934C-001 D-Sub Connectors: Pin Assignments for 4x64 (base configuration)



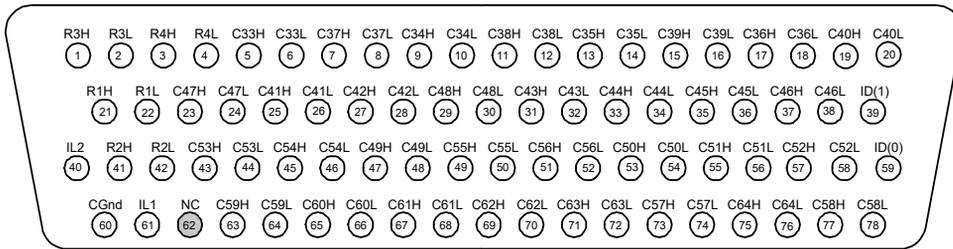
For orientation, the male D-sub connector end of the configuration block is facing you.



78-Pin D-Sub Male Connector
Bank 1 (P1)

Description	Pin										
R1H	17	C1H	3	C9H	23	C17H	47	C25H	71	ID2	39
R1L	18	C1L	4	C9L	24	C17L	48	C25L	72	IL3	59
R2H	19	C2H	7	C10H	27	C18H	51	C26H	75	IL4	77
R2L	20	C2L	8	C10L	28	C18L	52	C26L	76	No connect	40
R3H	37	C3H	11	C11H	29	C19H	53	C27H	61	No connect	60
R3L	38	C3L	12	C11L	30	C19L	54	C27L	62	No connect	78
R4H	57	C4H	15	C12H	31	C20H	55	C28H	63		
R4L	58	C4L	16	C12L	32	C20L	56	C28L	64		
		C5H	1	C13H	33	C21H	41	C29H	65		
		C5L	2	C13	34	C21L	42	C29L	66		
		C6H	5	C14H	35	C22H	43	C30H	67		
		C6L	6	C14L	36	C22L	44	C30L	68		
		C7H	9	C15H	21	C23H	45	C31H	69		
		C7L	10	C15L	22	C23L	46	C31L	70		
		C8H	13	C16H	25	C24H	49	C32H	73		
		C8L	14	C16L	26	C24L	50	C32L	74		

2 Base Matrix Configuration



**78-Pin D-Sub
Male Connector
Bank 2 (P2)**

Description	Pin										
R1H	21	C33H	5	C41H	25	C49H	47	C57H	73	ID1	39
R1L	22	C33L	6	C41L	26	C49L	48	C57L	74	ID0	59
R2H	41	C34H	9	C42H	27	C50H	53	C58H	77	IL1	61
R2L	42	C34L	10	C42L	28	C50L	54	C58L	78	IL2	40
R3H	1	C35H	13	C43H	31	C51H	55	C59H	63	Com Gnd	60
R3L	2	C35L	14	C43L	32	C51L	56	C59L	64	No connect	62
R4H	3	C36H	17	C44H	33	C52H	57	C60H	65		
R4L	4	C36L	18	C44L	34	C52L	58	C60L	66		
		C37H	7	C45H	35	C53H	43	C61H	67		
		C37L	8	C45L	36	C53L	44	C61L	68		
		C38H	11	C46H	37	C54H	45	C62H	69		
		C38L	12	C46L	38	C54L	46	C62L	70		
		C39H	15	C47H	23	C55H	49	C63H	71		
		C39L	16	C47L	24	C55L	50	C63L	72		
		C40H	19	C48H	29	C56H	51	C64H	75		
		C40L	20	C48L	30	C56L	52	C64L	76		

34934C-001 D-Sub Connectors: Safety Interlock Continuity Wiring for 4x64

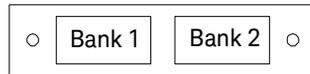
While wiring to the 34934C-001’s D-Sub connectors, you *must* make the following connections—in addition to the 4x64 row and column connections—to provide for safety interlock continuity:

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

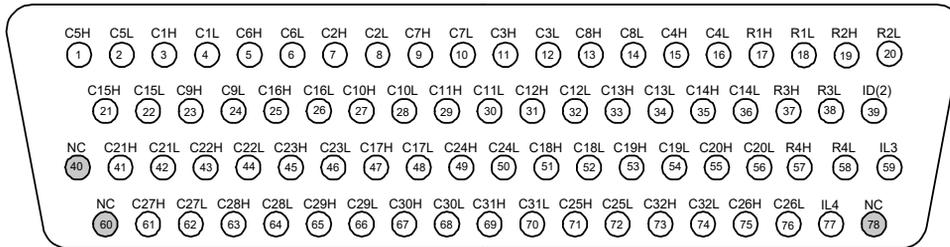
Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

Configuring a 4x64 Matrix by Direct Wiring

34934A D-Sub Connectors: 4x64 Pin Assignments



For orientation, the D-sub connector end of the module is facing you.

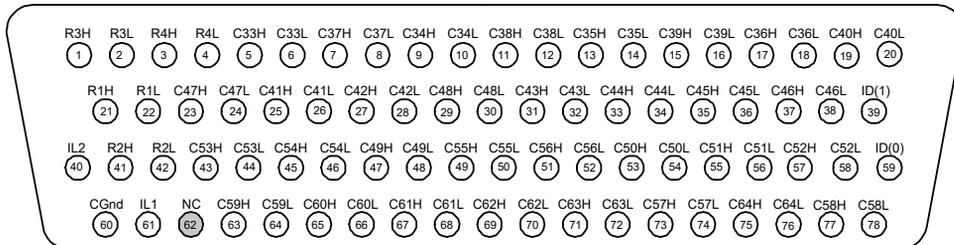


78-Pin D-Sub Male Connector
Bank 1

Description	Pin										
R1H	17	C1H	3	C9H	23	C17H	47	C25H	71	ID2	39
R1L	18	C1L	4	C9L	24	C17L	48	C25L	72	IL3	59
R2H	19	C2H	7	C10H	27	C18H	51	C26H	75	IL4	77
R2L	20	C2L	8	C10L	28	C18L	52	C26L	76	No connect	40
R3H	37	C3H	11	C11H	29	C19H	53	C27H	61	No connect	60
R3L	38	C3L	12	C11L	30	C19L	54	C27L	62	No connect	78

2 Base Matrix Configuration

Description	Pin										
R4H	57	C4H	15	C12H	31	C20H	55	C28H	63		
R4L	58	C4L	16	C12L	32	C20L	56	C28L	64		
		C5H	1	C13H	33	C21H	41	C29H	65		
		C5L	2	C13L	34	C21L	42	C29L	66		
		C6H	5	C14H	35	C22H	43	C30H	67		
		C6L	6	C14L	36	C22L	44	C30L	68		
		C7H	9	C15H	21	C23H	45	C31H	69		
		C7L	10	C15L	22	C23L	46	C31L	70		
		C8H	13	C16H	25	C24H	49	C32H	73		
		C8L	14	C16L	26	C24L	50	C32L	74		



78-Pin D-Sub
Male Connector
Bank 2

Description	Pin										
R1H	21	C33H	5	C41H	25	C49H	47	C57H	73	ID1	39
R1L	22	C33L	6	C41L	26	C49L	48	C57L	74	ID0	59
R2H	41	C34H	9	C42H	27	C50H	53	C58H	77	IL1	61
R2L	42	C34L	10	C42L	28	C50L	54	C58L	78	IL2	40
R3H	1	C35H	13	C43H	31	C51H	55	C59H	63	Com Gnd	60
R3L	2	C35L	14	C43L	32	C51L	56	C59L	64	No connect	62

Description	Pin										
R4H	3	C36H	17	C44H	33	C52H	57	C60H	65		
R4L	4	C36L	18	C44L	34	C52L	58	C60L	66		
		C37H	7	C45H	35	C53H	43	C61H	67		
		C37L	8	C45L	36	C53L	44	C61L	68		
		C38H	11	C46H	37	C54H	45	C62H	69		
		C38L	12	C46L	38	C54L	46	C62L	70		
		C39H	15	C47H	23	C55H	49	C63H	71		
		C39L	16	C47L	24	C55L	50	C63L	72		
		C40H	19	C48H	29	C56H	51	C64H	75		
		C40L	20	C48L	30	C56L	52	C64L	76		

34934A Module D-Sub Connectors: 4x64 Wiring

When wiring directly to the 34934A's D-Sub connectors, you *must* make the following connections—in addition to row and column connections—to configure the 4x64 matrix (matrices):

Module Configuration: The connections listed below short the pairs of rows in the 34934A's native 4x4x32 configuration to create the 4x64 MH and 4x64 ML matrices.

Short: Pin 17 on Bank 1 to Pin 21 on Bank 2
 Short: Pin 18 on Bank 1 to Pin 22 on Bank 2
 Short: Pin 19 on Bank 1 to Pin 41 on Bank 2
 Short: Pin 20 on Bank 1 to Pin 42 on Bank 2
 Short: Pin 37 on Bank 1 to Pin 1 on Bank 2
 Short: Pin 38 on Bank 1 to Pin 2 on Bank 2
 Short: Pin 57 on Bank 1 to Pin 3 on Bank 2
 Short: Pin 58 on Bank 1 to Pin 4 on Bank 2

Note that these connections require wires run *between* the two D-Sub connectors. You may make these connections near your test instrumentation or DUT, or near the D-Sub ends of your custom cabling.

Module ID: The connections listed below allow the 34980A mainframe to recognize the 4x64 matrix configuration.

Leave Open: Pin 39 on Bank 1 (ID bit 2)

Ground: Pin 39 on Bank 2 (ID bit 1)

Ground: Pin 59 on Bank 2 (ID bit 0)

Safety Interlock Continuity: The connections listed below provide for safety interlock continuity.

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

NOTE

If you use the 34934C-001 Configuration Block—in conjunction with the 34934A module—to make wiring connections, the additional **Module Configuration** and **Module ID** connections are *not required*. Those connections are made within the hardware of the configuration block. You would need only make the **Safety Interlock Continuity** connections.

4x128 Matrix Configuration

To create a 4x128 matrix, you may connect your field wiring to the 34934A by three methods:

Terminal Block You can use terminal block 34934T-001. The terminal block connects to the 34934A's two D-Sub connectors; it provides 12 blocks of 12 screw terminals each and a *4x128 removable overlay* (supplied with the 34934T-001) which provides guidance for field wiring the 4x128 matrices. Follow these sections in order to configure and wire the terminal block:

- “34934T-001 Terminal Block: 4x128 Layout” on page 124
- “34934T-001 Terminal Block: 4x128 Jumper Configuration” on page 127
- “34934T-001 Terminal Block: 4x128 Wiring” on page 131

Configuration Block You can use configuration block 34934C-001. Follow these sections in order to configure and wire the configuration block:

- “34934C-001 Configuration Block: Layout” on page 135
- “34934C-001 Configuration Block: 4x128 Jumper Configuration” on page 137
- “34934C-001 Configuration Block: Wiring” on page 143

WARNING

Do NOT use the 34934C-001 with the 34934T-001 terminal block for field wiring connections! This combination is not mechanically reliable and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring You can wire directly to the module's D-Sub connectors using custom cabling. You will have to short specific pins on and between the D-Sub connectors to create the 4x128 configuration. Review *both* these sections *before* wiring to the module's D-Subs:

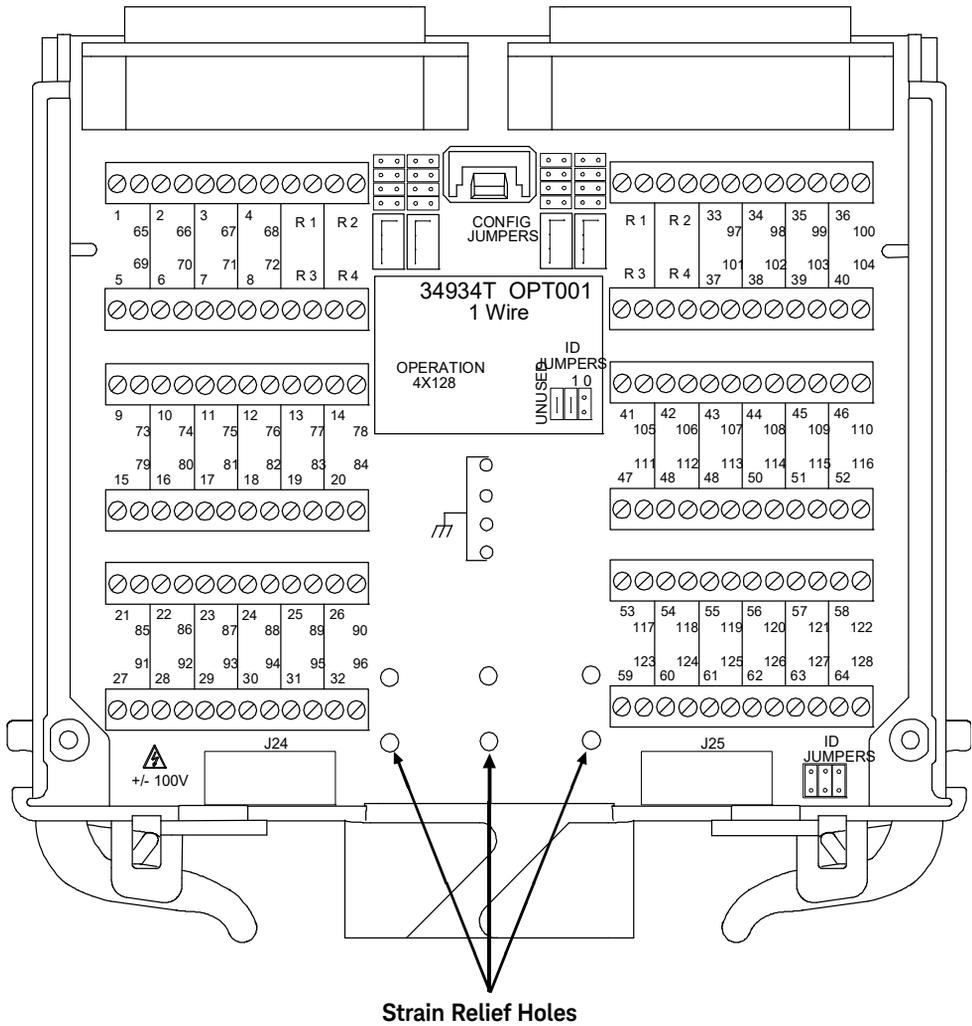
- “34934A D-Sub Connectors: 4x128 Pin Assignments” on page 150
- “34934A Module D-Sub Connectors: 4x128 Wiring” on page 152

Configuring a 4x128 Matrix using a Terminal Block

34934T-001 Terminal Block: 4x128 Layout

This terminal block with screw-type connections is labeled with the model number and the abbreviated module name. In addition, space is available on the label for you to write the slot number.

This terminal block's *4x128 removable overlay* shows the row and column terminals for the 4x128 matrix configuration:



Note the six strain relief holes provided in the terminal block PCB. See the following page for instructions on providing wiring strain relief.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

Terminal block strain relief

To reduce strain on the wiring at the terminals and maintain connection reliability, a strain relief feature has been incorporated into the terminal block design. Six holes have been placed near the lower edge of the circuit board, lined up with holes in the terminal block's bottom cover, as seen on "[34934T-001 Terminal Block: 4x128 Layout](#)" on page 124.

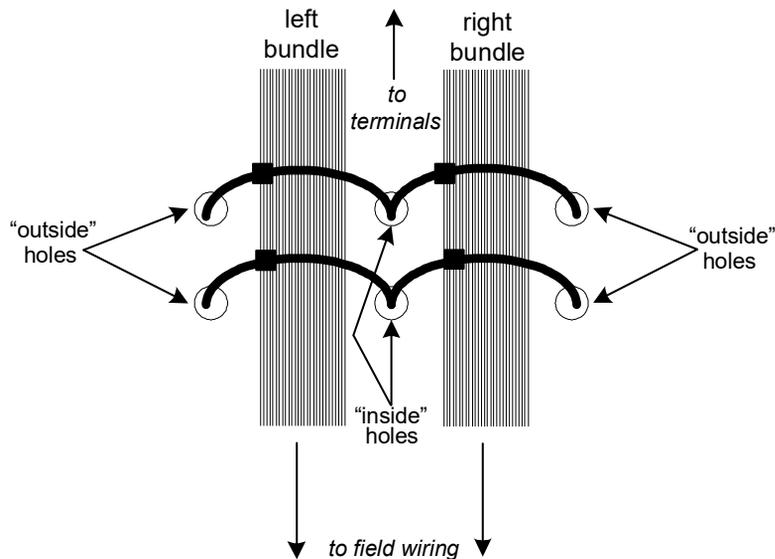
WARNING

Always include wiring strain relief to reduce strain on the connections. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

To use this feature:

- 1 Once all jumpers are installed and wiring is connected to the terminals, arrange the wires leading away from the terminal block in two bundles. This is easiest if the left bundle leads to the terminals on the left side of the block, and conversely for the right side.
- 2 Insert a cable tie (tie-wrap, zip-tie) down through each of the four outside holes in the PCB, continuing down through the holes in the terminal block's bottom cover.
- 3 Route these cable ties up through the center holes in the bottom cover and PCB.
- 4 Close and tighten the cable ties as required, and remove any excess so that it does not interfere with cover closure.

The completed bundles are shown below.



34934T-001 Terminal Block: 4x128 Jumper Configuration

This terminal block has three types of jumpers which must be set to configure the 4x128 matrix (matrices):

- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board's female D-Sub connectors, as shown.
- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins on the lower right corner of the board, as shown.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 or equivalent custom wiring on the two 20-pin extension headers at the bottom of the terminal block, as shown.

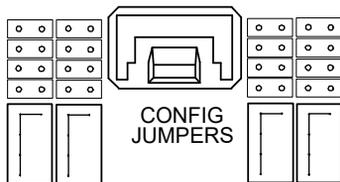
NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

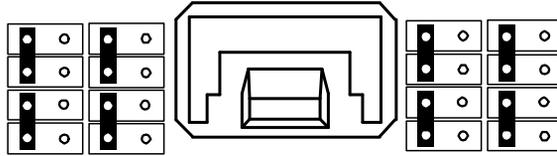
Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

Placing CONFIG Jumpers for 4x128 The CONFIG jumper area—shown below with jumpers removed— includes an illustration (on the *4x128 removable overlay*) that shows jumper placement for setting a 4x128 matrix configuration.

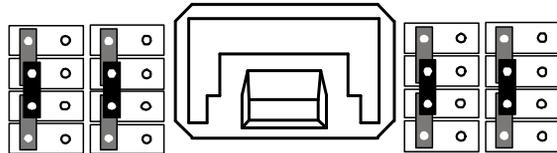


There are two 4x2 blocks of CONFIG JUMPER pins on each side of the board's center—you *must* set all four blocks identically.

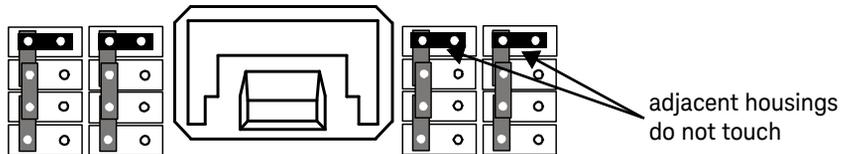
- 1 Place eight vertically positioned jumpers as shown below.



- 2 Then stack four more vertically positioned jumpers as shown below. **Note:** these stack *above* the jumpers you placed in step 1, connecting the second and third rows of pins in each 4x4 block of jumper pins.



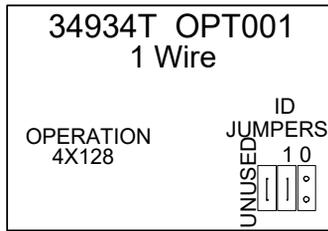
- 3 Then stack four horizontally positioned jumpers as shown below.



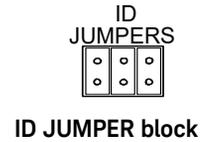
WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Placing ID Jumpers for 4x128 The ID jumpers allow the 34980A to recognize the 4x128 matrix configuration upon mainframe bootup. Jumper placement guidance is provided on the *4x128 removable overlay* (below left); the ID jumper block is shown at right with jumpers removed.



Location of placement guidance for ID Jumpers



To set the correct ID code for 4x128, place two vertically positioned jumpers as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing Safety Interlock Continuity Jumpers for 4x128 At the bottom (end opposite the D-Sub connectors) of the terminal block are two 20-pin extension headers—marked J24 and J25 on the circuit board. These headers provide access to the module’s rows. Pins 19 and 20 on each header must be shorted to provide continuity for the module’s safety interlock function.

Two keyed 20-pin terminators 34934-80006 are supplied for providing safety interlock continuity. Install the terminators securely in the headers, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2. Detailed pin diagrams for these terminators are provided on [page 135](#).

34934T-001 Terminal Block: 4x128 Wiring

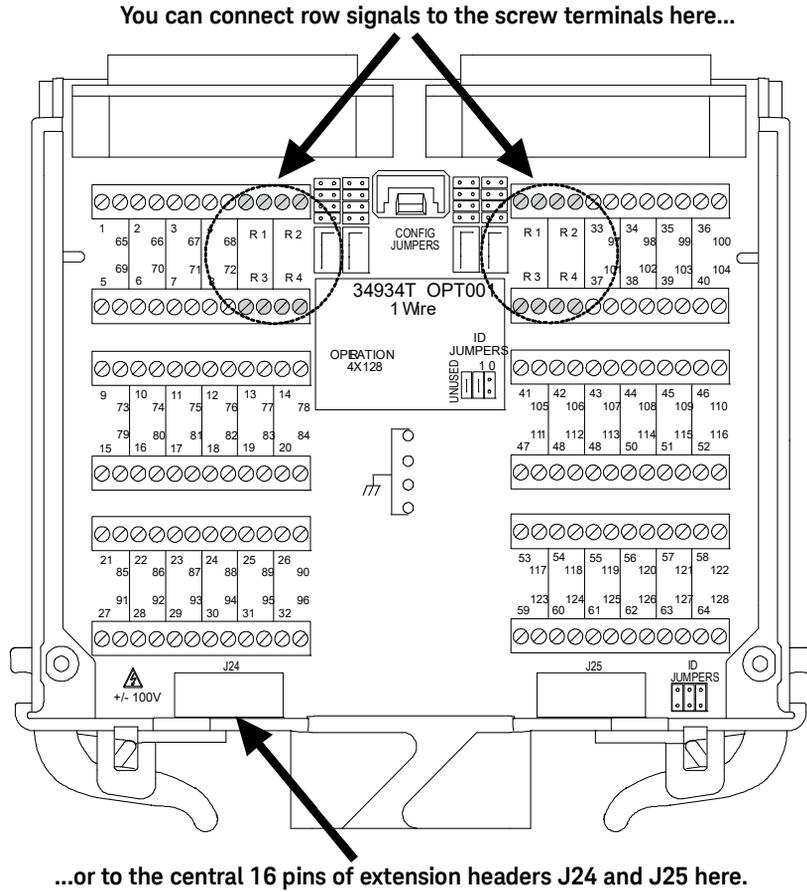
Once the required jumpers have been placed to configure the terminal block as 4x128, you are ready to make row and column connections to the terminal block.

You may connect to the module's rows and columns by wiring to the screw terminals marked C1 through C128 (H, L) and R1 through R4 (H, L) on the *4x128 removable overlay*. The combined weight of these wires can create significant strain on the wiring; to minimize this see ["Terminal block strain relief"](#) on page 126.

However, you may prefer to use physically separate cabling for the row connections (e.g. if you connect your DUT to the columns and your measurement device(s) to the rows). The terminal block's extension headers J24 and J25 provide access to the rows; you can make row connections to the central 16 terminals (pins 3-18) on the supplied terminators you installed in ["Placing Safety Interlock Continuity Jumpers for 4x128"](#) on page 130.

2 Base Matrix Configuration

Below is an illustration of the possible locations for making row signal connections to the 34934T-001:

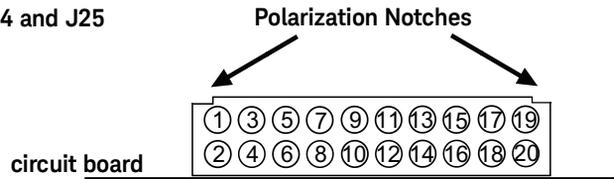


The following subsection outlines how to make row connections to extension header J24.

Extension Header (Row Signal) Wiring for 4x128

The position of extension headers J24 and J25 are marked on the 34934T-001's silk-screen. Oriented with the terminal side of the board up, the pin assignments for these headers are shown below.

Extension Headers J24 and J25

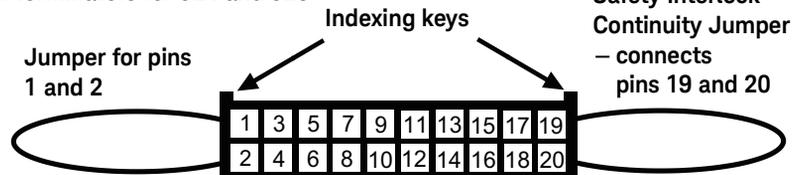


The supplied 20-pin terminators 34934-80006 are identical. For 4x128, you can only make row connections to header J24. The terminal numbers—corresponding to the headers—are shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

20-Pin Terminators for J24 and J25



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of header J24, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to "crimp-to-wire" contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

As no row access is provided for J25, keep the supplied safety interlock terminator installed in J25.

Note that each row signal (R1 through R4 (H,L) is routed out on two pins on J24. You can make row signal connections to either row (upper, lower) of the connector.

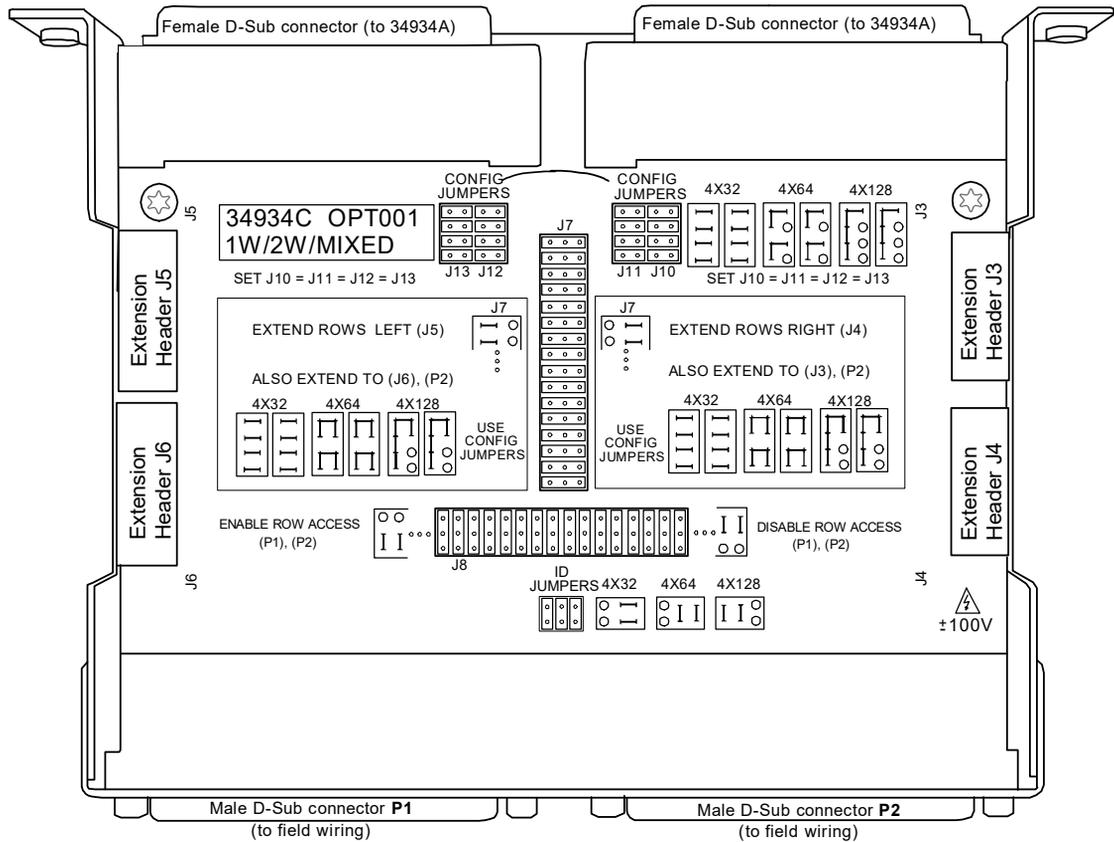
34934T-001: Extension Header Pin Assignments for 4x128

Header J24				Header J25			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1	3	R1	4	No connect	3	No connect	4
No connect	5	No connect	6	No connect	5	No connect	6
R2	7	R2	8	No connect	7	No connect	8
No connect	9	No connect	10	No connect	9	No connect	10
R3	11	R3	12	No connect	11	No connect	12
No connect	13	No connect	14	No connect	13	No connect	14
R4	15	R4	16	No connect	15	No connect	16
No connect	17	No connect	18	No connect	17	No connect	18
IL1	19	IL2	20	IL3	19	IL4	20

Configuring a 4x128 Matrix using a Configuration Block

34934C-001 Configuration Block: Layout

This configuration block is labeled with the model number and the abbreviated module name.



Note four types of on-board jumpers you'll have to set to configure this board; some will be familiar from the configuration descriptions of the *terminal blocks*, but some are new. Instructions for setting these are provided in ["34934C-001 Configuration Block: 4x128 Jumper Configuration"](#) on page 137.

- ID Jumpers
- CONFIG Jumpers (J10, J11, J12 and J13)
- Row Extension Jumpers (J7)
- Row Access Jumpers (J8)

WARNING

Do not install substitute parts or perform any unauthorized modifications to the instrument to ensure the safety features are maintained in operational condition. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

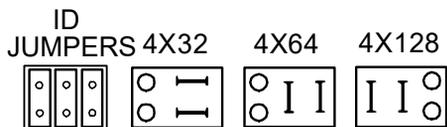
Also note the four extension headers (J3, J4, J5 and J6). Instructions for wiring to these are provided in [“34934C-001 Configuration Block: Wiring”](#) on page 143.

34934C-001 Configuration Block: 4x128 Jumper Configuration

The configuration block has five types of jumpers which must be connected to configure it for use in an expanded 4-row matrix. In this section, you will place:

- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins near the bottom center of the board. See [“Placing ID Jumpers on the 34934C-001 Configuration Block for 4x128”](#) on page 137.
- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board’s female D-Sub connectors. See [“Placing CONFIG Jumpers on the 34934C-001 for 4x128”](#) on page 138.
- **Row Extension Jumpers:** place these 16-pin jumpers on a 16x3 block (J7) of jumper pins at the board’s center. See [“Placing Row Extension Jumpers on the 34934C-001 Configuration Block”](#) on page 140.
- **Row Access Jumpers:** place these 16-pin jumpers on a 3x16 block of jumper pins located just above the ID Jumpers. See [“Placing Row Access Jumpers on the 34934C-001 Configuration Block”](#) on page 141.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 on the four 20-pin extension headers at the sides of the configuration block. See [“Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block”](#) on page 142.

Placing ID Jumpers on the 34934C-001 Configuration Block for 4x128 The ID jumpers allow the 34980A to recognize the base matrix configuration used by the 34934C-001, upon mainframe bootup. The ID jumpers are placed in a 2x3 jumper block, located below jumper block J8. Jumper placement guidance is provided on the configuration block’s silk-screen, to the right of the jumpers:



To set the correct ID code for 4x128, place two horizontally positioned jumpers in the right position, as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the following step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

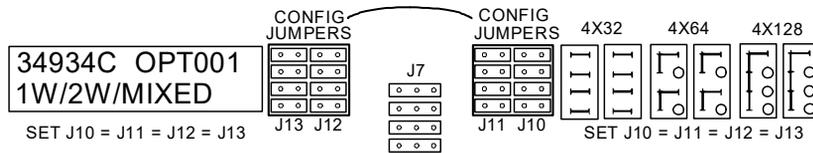
Placing CONFIG Jumpers on the 34934C-001 for 4x128 The CONFIG jumper area—shown below with jumpers removed— includes an illustration to its right that shows jumper placement for setting the 4x32, 4x64, and 4x128 base matrix configurations.

NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

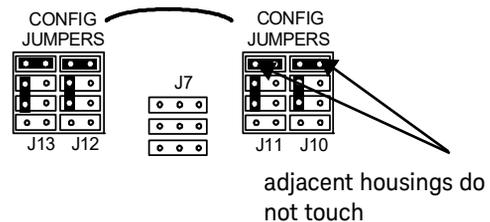
WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

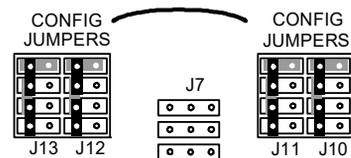


To set the CONFIG jumpers for 4x128:

- 1 Place eight horizontally and vertically positioned jumpers as shown at right.



- 2 Then stack eight vertically positioned jumpers *above* the other jumpers, as shown at right.

**WARNING**

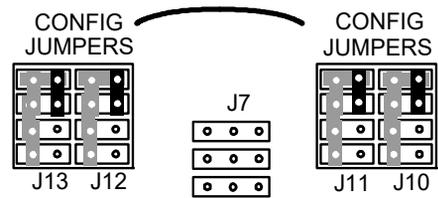
When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

This board configuration (through step **2**) “extends” the four row signals either left to connector J5 or right to connector J4, depending on the placement of jumpers in block J7. The row signals will also be extended to male D-Sub connector P1, if “enabled” by placement of jumpers in block J8. *This provides typical row connectivity options.*

3 Optional Step: For the 4x128 base configuration, *additional* jumpers must be placed to provide these additional options for row connectivity:

- extend the row signals either left to connector J6 or right to connector J3, depending on the placement of jumpers in block J7.
- extend the row signals to male D-Sub connector P2, if “enabled” by placement of jumpers in block J8.

If these additional row connectivity options are desired, stack four additional vertically positioned jumpers as shown at right.



This optional step is implied by the silk-screened guidance provided on the board at either side of jumper block J7.

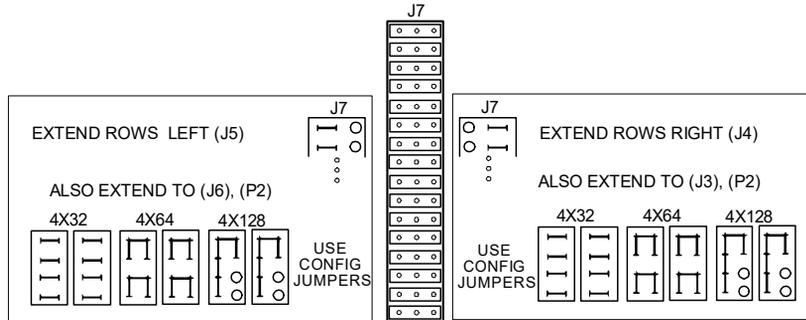
Placing Row Extension Jumpers on the 34934C-001 Configuration Block

The jumpers in 16x3 jumper block J7 (at board center) determine if row connections can be made from the left or right side of the board (i.e. they “extend” the row signal connection points out to *only one* of the two pairs of blue 20-pin connectors at the sides of the configuration block).

Note that “row connections” at minimum include the connections between modules in the matrix, but may also include your access from field wiring to the matrix rows.

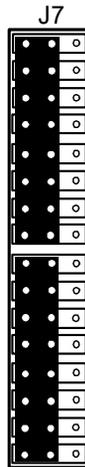
Setting these jumpers provides you the flexibility to plan how to route your custom cabling, depending on slot placement of the individual 34934A modules and their corresponding 34934C-001 blocks.

The following illustration shows the jumper block J7, along with the relevant guidance provided on the board's silk-screen for jumper placement:



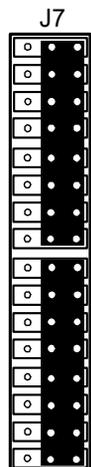
If you plan to extend rows left, to J5 and J6, place two 2x8 jumpers as shown at right.

The row signals will also be present on J6 if you placed the optional CONFIG jumpers in step 3 on [page 140](#).



If you plan to extend rows right, to J3 and J4, place two 2x8 jumpers as shown at right.

The row signals will also be present on J3 if you placed the optional CONFIG jumpers in step 3 on [page 140](#).

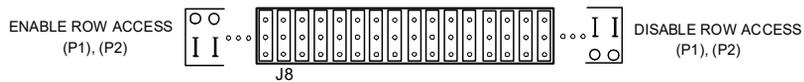


Placing Row Access Jumpers on the 34934C-001 Configuration Block The jumpers in 3x16 jumper block J8 (at lower board center) determine if row connections can be made from the two male D-Sub connectors P1 and P2 (i.e. they “enable” the row signal connection points at the D-subs).

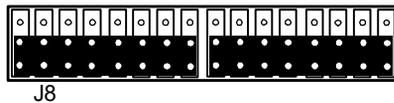
These “row connections” are the traditional access points from field wiring to the matrix rows. In practice, your arrangement of test instrumentation and DUT test points will determine if you wish to make row connections at the D-sub, at the blue 20-pin connectors, or both.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling.

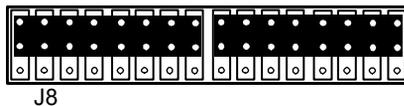
The following illustration shows the jumper block J8, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



If you plan to connect row signals to the D-sub connectors, place two 2x8 jumpers as shown below.



If you do not need to connect row signals to the D-sub connectors, place two 2x8 jumpers as shown below.



Note that by not routing signals to the D-sub connectors, module bandwidth may be preserved and capacitive loading minimized.

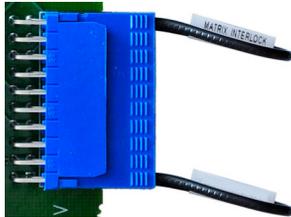
Placing Safety Interlock Continuity Jumpers on the 34934C-001

Configuration Block At each side of the configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. In all cases, pins 19 and 20 are the safety interlock pins.

These headers provide for row extension and row signal access, depending on the placement of the jumpers in J7. You must have set the jumpers to extend rows either to the left or right side (of the configuration block) (see [“Placing Row Extension Jumpers on the 34934C-001 Configuration Block”](#) on page 140).

To provide continuity for the module's safety interlock function, pins 19 and 20 on *both* extension headers on the selected side (left or right) must be shorted.

- For each of these headers, install one of the supplied keyed 20-pin terminators 34934-80006 securely in that header, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2.

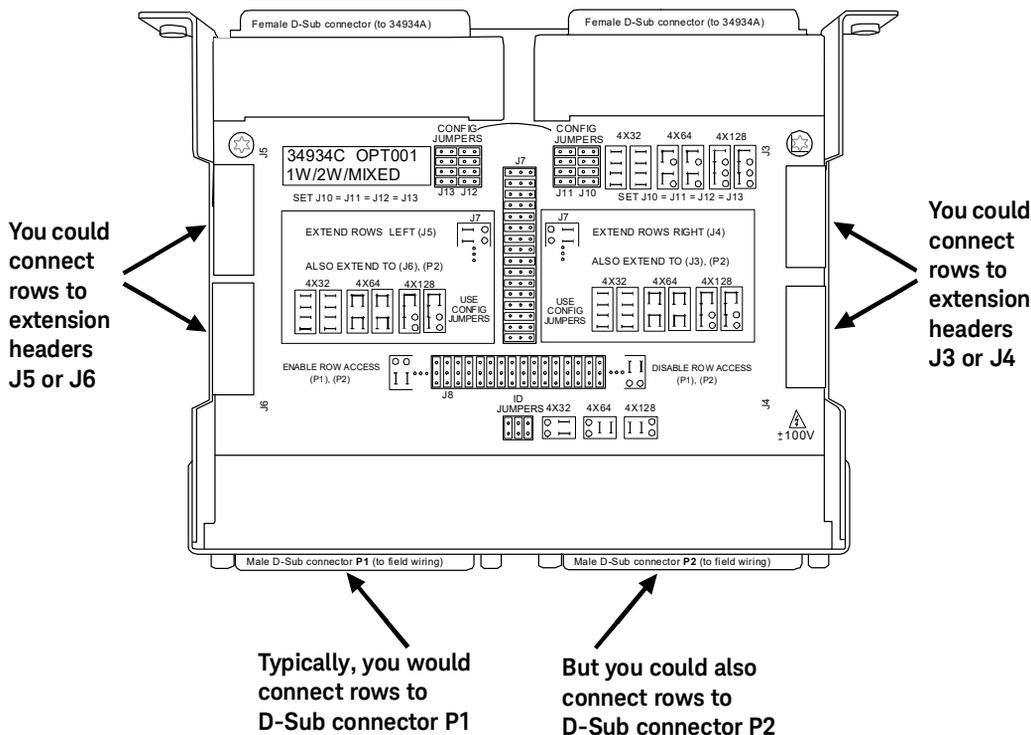
- The extension headers on the opposite (unused) side of the 34934C-001 do not require a safety interlock terminator; these will be hidden by the configuration block's cover.

34934C-001 Configuration Block: Wiring

The next two subsections outline options for making row and column wiring connections to the configuration block.

Extension Header (Row Signal) Wiring to the 34934C-001

Below is an illustration of the possible locations for making row signal connections to the 34934T-001:



Typically, you could make both row and column connections to each module by fabricating custom cabling terminating in female D-Sub connectors. These attach to the configuration block’s male D-Sub connectors P1 and P2.

Alternately, you may choose to connect only columns (typically test points on your DUT) to the D-Subs. There may be physical or electrical advantages in making your *row* connections (typically measurement devices) to the configuration block’s blue extension connectors J3, J4, J5 and J6. To provide this flexibility, two extension headers are provided on each side of the configuration block.

You must have set the jumpers (in [“Placing Row Extension Jumpers on the 34934C-001 Configuration Block”](#) on page 140) to extend rows to two of the four headers. We’ll call these the “live” headers.

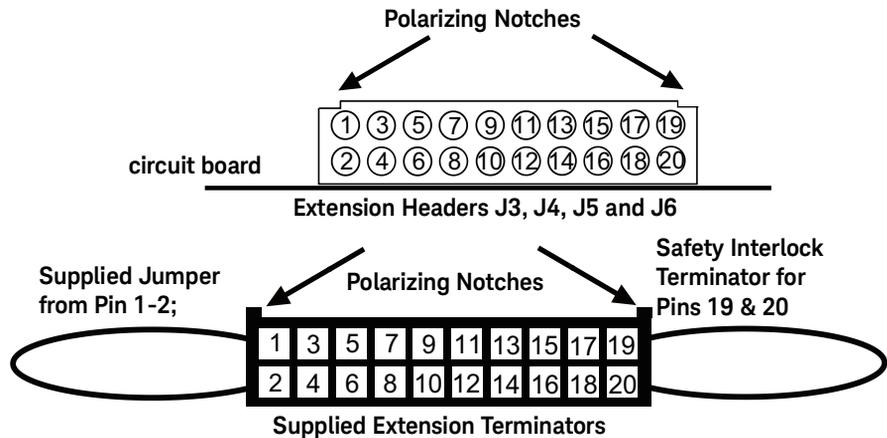
Had you created *two* live headers, by performing step 3 on [page 140](#); then the second header (J3 or J6) on the side selected by jumpers J7 will also be a live header.

In “[Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block](#)” on page 142, you will have installed the supplied blue terminators 34934-80006 in the live headers on the configuration block.

You can now make row connections to these headers, by utilizing the central 16 pins (3-18) on the snap-in terminators. The extension header’s supplied terminator is shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934C-001: Extension Header Pin Assignments for 4x128

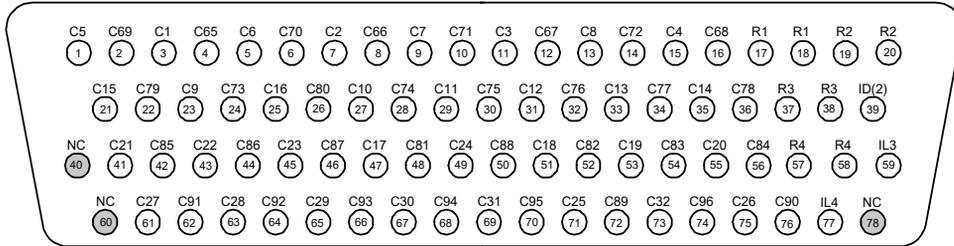
Description	Headers J4, J5		Headers J3, J6				
	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1	3	R1	4	R1	3	R1	4
No connect	5	No connect	6	No connect	5	No connect	6
R2	7	R2	8	R2	7	R2	8
No connect	9	No connect	10	No connect	9	No connect	10
R3	11	R3	12	R3	11	R3	12
No connect	13	No connect	14	No connect	13	No connect	14
R4	15	R4	16	R4	15	R4	16
No connect	17	No connect	18	No connect	17	No connect	18
IL1	19	IL2	20	IL3	19	IL4	20

If you are going to create an expanded-row matrix using multiple modules and configuration blocks, plan your wiring placement for each configuration block carefully, to allow for a daisy-chain interconnection between configuration blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Configuration Blocks”](#) on page 261.

34934C-001 D-Sub Connectors: Pin Assignments for 4x128 (base configuration)

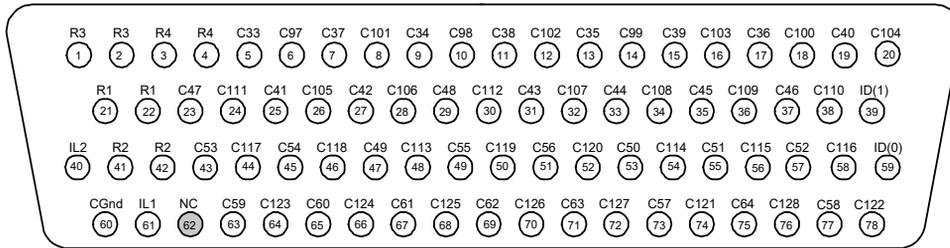


For orientation, the male D-sub connector end of the configuration block is facing you.



78-Pin D-Sub Male Connector
Bank 1 (P1)

Description	Pin										
R1	17	C1	3	C17	47	C65	4	C81	48	ID2	39
R1	18	C2	7	C18	51	C66	8	C82	52	IL3	59
R2	19	C3	11	C19	53	C67	12	C83	54	IL4	77
R2	20	C4	15	C20	55	C68	16	C84	56	No connect	40
R3	37	C5	1	C21	41	C69	2	C85	42	No connect	60
R3	38	C6	5	C22	43	C70	6	C86	44	No connect	78
R4	57	C7	9	C23	45	C71	10	C87	46		
R4	58	C8	13	C24	49	C72	14	C88	50		
		C9	23	C25	71	C73	24	C89	72		
		C10	27	C26	75	C74	28	C90	76		
		C11	29	C27	61	C75	30	C91	62		
		C12	31	C28	63	C76	32	C92	64		
		C13	33	C29	65	C77	34	C93	66		
		C14	35	C30	67	C78	36	C94	68		
		C15	21	C31	69	C79	22	C95	70		
		C16	25	C32	73	C80	26	C96	74		



78-Pin D-Sub Male Connector
Bank 2 (P2)

Description	Pin										
R1	21	C33	5	C49	47	C97	6	C113	48	ID1	39
R1	22	C34	9	C50	53	C98	10	C114	54	ID0	59
R2	41	C35	13	C51	55	C99	14	C115	56	IL1	61
R2	42	C36	17	C52	57	C100	18	C116	58	IL2	40
R3	1	C37	7	C53	43	C101	8	C117	44	Com Gnd	60
R3	2	C38	11	C54	45	C102	12	C118	46	No connect	62
R4	3	C39	15	C55	49	C103	16	C119	50		
R4	4	C40	19	C56	51	C104	20	C120	52		
		C41	25	C57	73	C105	26	C121	74		
		C42	27	C58	77	C106	28	C122	78		
		C43	31	C59	63	C107	32	C123	64		
		C44	33	C60	65	C108	34	C124	66		
		C45	35	C61	67	C109	36	C125	68		
		C46	37	C62	69	C110	38	C126	70		
		C47	23	C63	71	C111	24	C127	72		
		C48	29	C64	75	C112	30	C128	76		

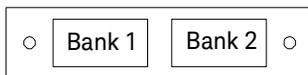
34934C-001 D-Sub Connectors: Safety Interlock Continuity Wiring for 4x128 While wiring to the 34934C-001's D-Sub connectors, you *must* make the following connections—in addition to the 4x128 row and column connections—to provide for safety interlock continuity:

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

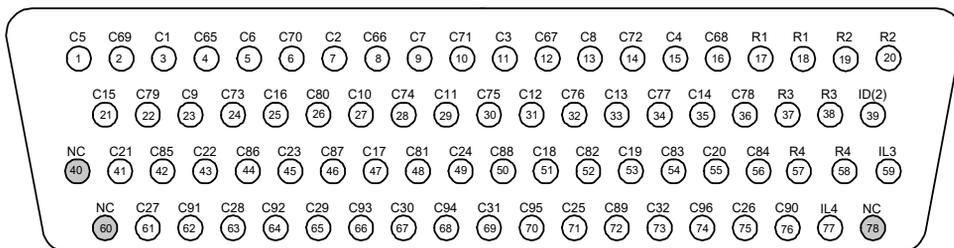
Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

Configuring a 4x128 Matrix by Direct Wiring

34934A D-Sub Connectors: 4x128 Pin Assignments



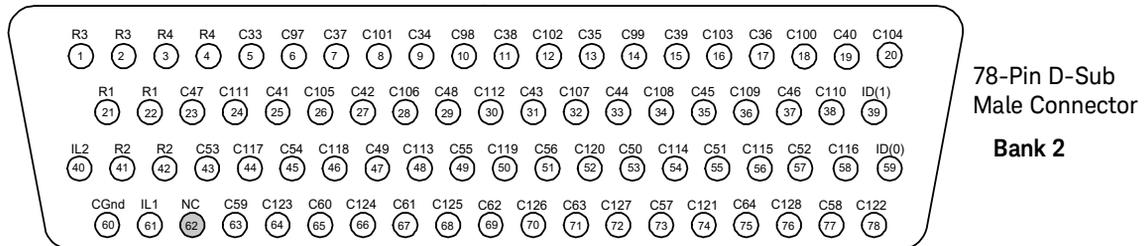
For orientation, the D-sub connector end of the module is facing you.



78-Pin D-Sub Male Connector
Bank 1

Description	Pin										
R1	17	C1	3	C17	47	C65	4	C81	48	ID2	39
R1	18	C2	7	C18	51	C66	8	C82	52	IL3	59
R2	19	C3	11	C19	53	C67	12	C83	54	IL4	77
R2	20	C4	15	C20	55	C68	16	C84	56	No connect	40
R3	37	C5	1	C21	41	C69	2	C85	42	No connect	60
R3	38	C6	5	C22	43	C70	6	C86	44	No connect	78

Description	Pin										
R4	57	C7	9	C23	45	C71	10	C87	46		
R4	58	C8	13	C24	49	C72	14	C88	50		
		C9	23	C25	71	C73	24	C89	72		
		C10	27	C26	75	C74	28	C90	76		
		C11	29	C27	61	C75	30	C91	62		
		C12	31	C28	63	C76	32	C92	64		
		C13	33	C29	65	C77	34	C93	66		
		C14	35	C30	67	C78	36	C94	68		
		C15	21	C31	69	C79	22	C95	70		
		C16	25	C32	73	C80	26	C96	74		



78-Pin D-Sub Male Connector
Bank 2

Description	Pin										
R1	21	C33	5	C49	47	C97	6	C113	48	ID1	39
R1	22	C34	9	C50	53	C98	10	C114	54	ID0	59
R2	41	C35	13	C51	55	C99	14	C115	56	IL1	61
R2	42	C36	17	C52	57	C100	18	C116	58	IL2	40
R3	1	C37	7	C53	43	C101	8	C117	44	Com Gnd	60
R3	2	C38	11	C54	45	C102	12	C118	46	No connect	62

Description	Pin										
R4	3	C39	15	C55	49	C103	16	C119	50		
R4	4	C40	19	C56	51	C104	20	C120	52		
		C41	25	C57	73	C105	26	C121	74		
		C42	27	C58	77	C106	28	C122	78		
		C43	31	C59	63	C107	32	C123	64		
		C44	33	C60	65	C108	34	C124	66		
		C45	35	C61	67	C109	36	C125	68		
		C46	37	C62	69	C110	38	C126	70		
		C47	23	C63	71	C111	24	C127	72		
		C48	29	C64	75	C112	30	C128	76		

34934A Module D-Sub Connectors: 4x128 Wiring

When wiring directly to the 34934A's D-Sub connectors, you *must* make the following connections—in addition to row and column connections—to configure the 4x128 matrix (matrices):

Module Configuration: The connections listed below short the pairs of rows in the 34934A's native 4x4x32 configuration to create the 4x128 matrix.

- Short: Pin 17 on Bank 1 to Pin 21 on Bank 2
- Short: Pin 18 on Bank 1 to Pin 22 on Bank 2
- Short: Pin 19 on Bank 1 to Pin 41 on Bank 2
- Short: Pin 20 on Bank 1 to Pin 42 on Bank 2
- Short: Pin 37 on Bank 1 to Pin 1 on Bank 2
- Short: Pin 38 on Bank 1 to Pin 2 on Bank 2
- Short: Pin 57 on Bank 1 to Pin 3 on Bank 2
- Short: Pin 58 on Bank 1 to Pin 4 on Bank 2
- Short: Pin 17 to Pin 18 on Bank 1
- Short: Pin 19 to Pin 20 on Bank 1
- Short: Pin 37 to Pin 38 on Bank 1
- Short: Pin 57 to Pin 58 on Bank 1

Note that eight of these connections require wires run *between* the two D-Sub connectors. You may make these connections near your test instrumentation or DUT, or near the D-Sub ends of your custom cabling.

Module ID: The connections listed below allow the 34980A mainframe to recognize the 4x128 matrix configuration.

Leave Open: Pin 39 on Bank 1 (ID bit 2)

Ground: Pin 39 on Bank 2 (ID bit 1)

Leave Open: Pin 59 on Bank 2 (ID bit 0)

Safety Interlock Continuity: The connections listed below provide for safety interlock continuity.

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

NOTE

If you use the 34934C-001 Configuration Block—in conjunction with the 34934A module—to make wiring connections, the additional **Module Configuration** and **Module ID** connections are *not required*. Those connections are made within the hardware of the configuration block. You would need only make the **Safety Interlock Continuity** connections.

8x32 Matrix Configuration

To create an 8x32 matrix, you may connect your field wiring to the 34934A by three methods:

Terminal Block You can use terminal block 34934T-002. The terminal block connects to the 34934A's two D-Sub connectors; it provides 12 blocks of 12 screw terminals each, and silk-screened guidance for field wiring the 8x32 matrices. Follow these sections in order to configure and wire the terminal block:

- “34934T-002 Terminal Block: 8x32 Layout” on page 155
- “34934T-002 Terminal Block: 8x32 Jumper Configuration” on page 159
- “34934T-002 Terminal Block: 8x32 Wiring” on page 162

Configuration Block You can use configuration block 34934C-001. Follow these sections in order to configure and wire the configuration block:

- “34934C-002 Configuration Block: Layout” on page 166
- “34934C-002 Configuration Block: 8x32 Jumper Configuration” on page 168
- “34934C-002 Configuration Block: Wiring” on page 174

WARNING

Do NOT use the 34934C-001 with the 34934T-001 terminal block for field wiring connections! This combination is not mechanically reliable and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring You can wire directly to the module's D-Sub connectors using custom cabling. You will have to short specific pins on and between the D-Sub connectors to create the 8x32 configuration. Review *both* these sections *before* wiring to the module's D-Subs:

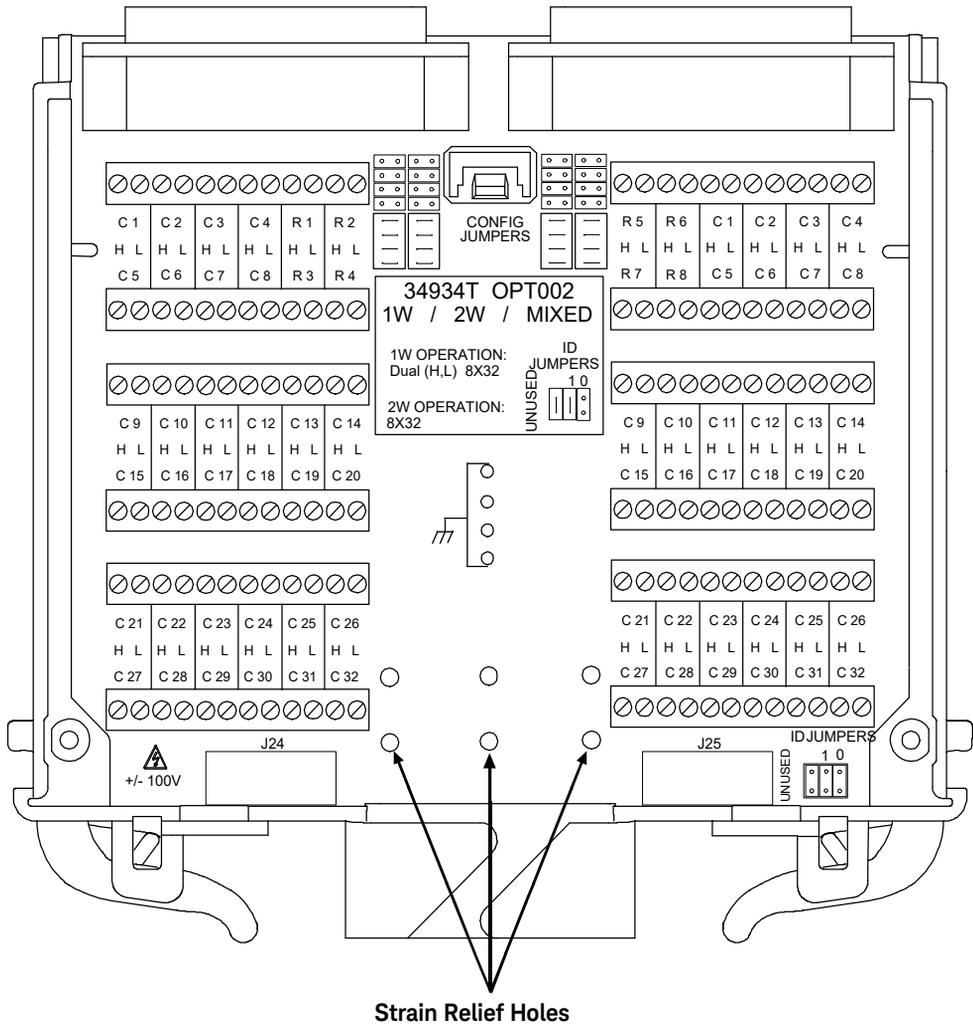
- “34934A module D-Sub Connectors: 8x32 Pin Assignments” on page 180
- “34934A Module D-Sub Connectors: 8x32 Wiring” on page 182

Configuring an 8x32 Matrix using a Terminal Block

34934T-002 Terminal Block: 8x32 Layout

This terminal block with screw-type connections is labeled with the model number and the abbreviated module name. In addition, space is available on the label for you to write the slot number.

This terminal block's silk-screen shows the row and column terminals for the 8x32 matrix configuration:



Note the six strain relief holes provided in the terminal block PCB. See the following page for instructions on providing wiring strain relief.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

34934T-002 Terminal Block: 8x32 Terminal Wiring Options

On the 34934T-002 terminal block, there are redundant sets of terminals provided for columns 1-32, one set on each side of the block. The column terminals on each side are shorted by traces in the block to their like-labeled counterparts on the other; you may make any column connection to the appropriate terminal on either side.

Terminal block strain relief

To reduce strain on the wiring at the terminals and maintain connection reliability, a strain relief feature has been incorporated into the terminal block design. Six holes have been placed near the lower edge of the circuit board, lined up with holes in the terminal block's bottom cover, as seen on ["34934T-002 Terminal Block: 8x32 Layout"](#) on page 155.

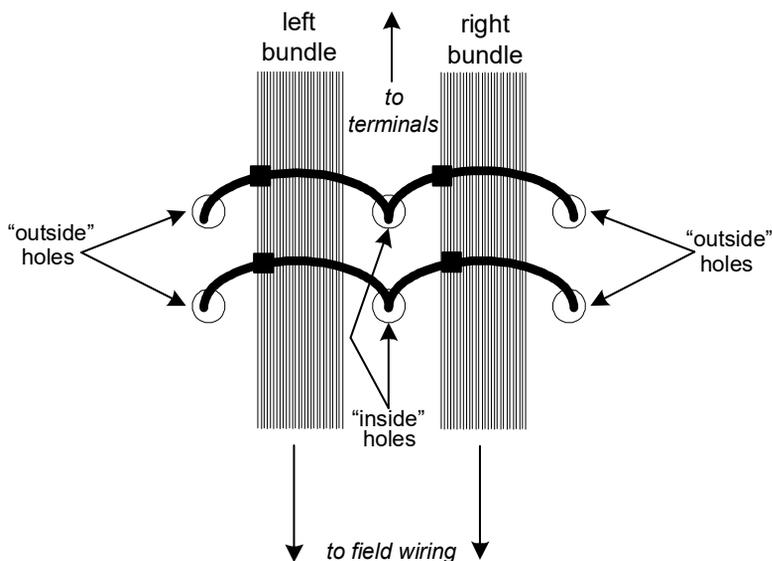
WARNING

Always include wiring strain relief to reduce strain on the connections. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

To use this feature:

- 1 Once all jumpers are installed and wiring is connected to the terminals, arrange the wires leading away from the terminal block in two bundles. This is easiest if the left bundle leads to the terminals on the left side of the block, and conversely for the right side.
- 2 Insert a cable tie (tie-wrap, zip-tie) down through each of the four outside holes in the PCB, continuing down through the holes in the terminal block's bottom cover.
- 3 Route these cable ties up through the center holes in the bottom cover and PCB.
- 4 Close and tighten the cable ties as required, and remove any excess so that it does not interfere with cover closure.

The completed bundles are shown below.



34934T-002 Terminal Block: 8x32 Jumper Configuration

This terminal block has three types of jumpers which must be connected to configure the 8x32 matrix (matrices):

- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four 4x2* blocks of jumper pins located near the board's female D-Sub connectors, as shown.
- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins on the lower right corner of the board, as shown.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 or equivalent custom wiring on the two 20-pin extension headers at the bottom of the terminal block, as shown.

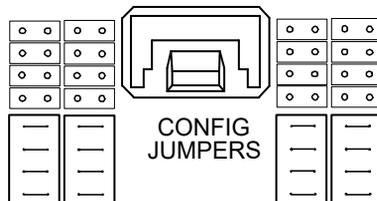
NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

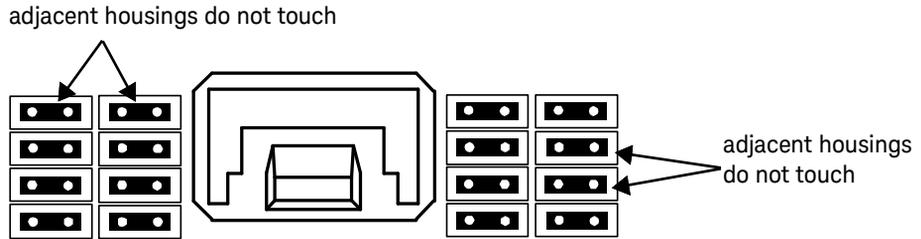
Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

Placing CONFIG Jumpers for 8x32 The CONFIG jumper area—shown below with jumpers removed— includes an illustration below it on the silk-screen that shows jumper placement for setting a 8x32 matrix configuration.



There are two 4x2 blocks of CONFIG JUMPER pins on each side of the board's center—you *must* set all four blocks identically.

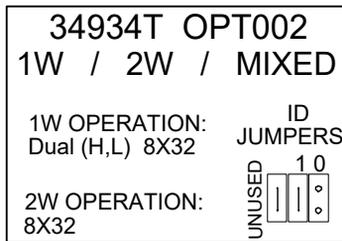
- 1 Place sixteen horizontally positioned jumpers as shown below.



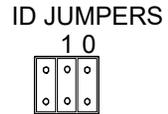
WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Placing ID Jumpers for 8x32 The ID jumpers allow the 34980A to recognize the 8x32 matrix configuration upon mainframe bootup. Jumper placement guidance is provided on the silk-screen (below left); the ID jumper block is shown at right with jumpers removed.



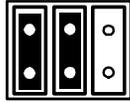
Location of placement guidance for ID Jumpers



ID JUMPER block

To set the correct ID code for 8x32, place two vertically positioned jumpers as shown below.

ID JUMPERS

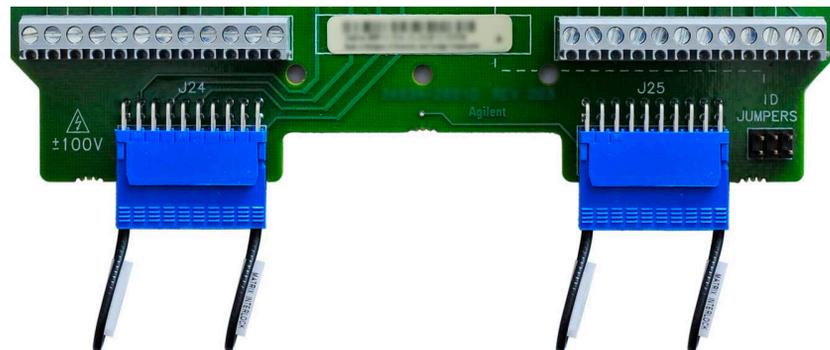


CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing Safety Interlock Continuity Jumpers for 8x32 At the bottom (end opposite the D-Sub connectors) of the terminal block are two 20-pin extension headers—marked J24 and J25 on the circuit board. These headers provide access to the module's rows. Pins 19 and 20 on each header must be shorted to provide continuity for the module's safety interlock function.

Two keyed 20-pin terminators 34934-80006 are supplied for providing safety interlock continuity. Install the terminators securely in the headers, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2. Detailed pin diagrams for these terminators are provided on [page 164](#).

34934T-002 Terminal Block: 8x32 Wiring

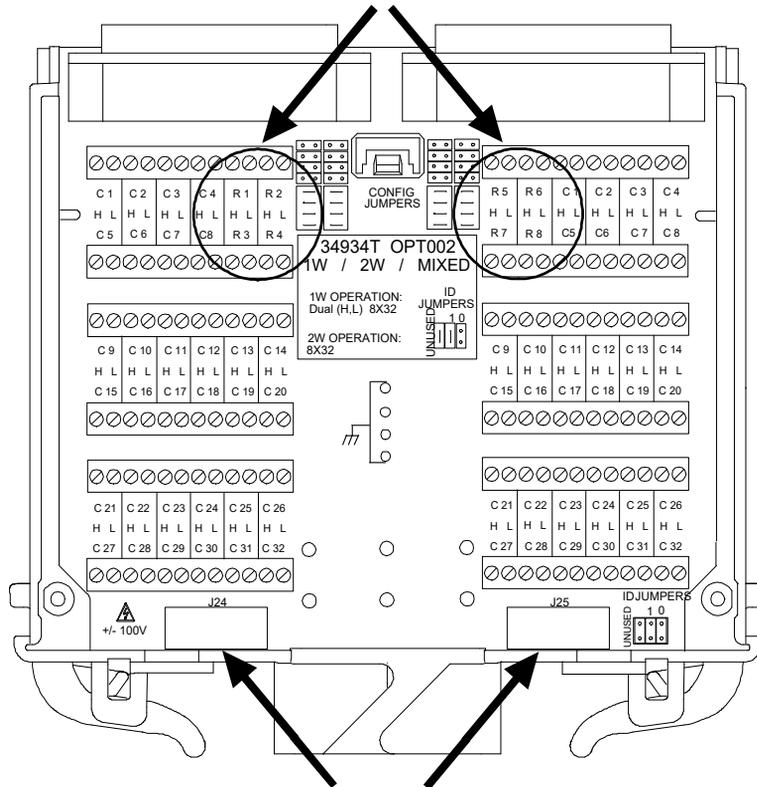
Once the required jumpers have been placed to configure the terminal block as 8x32, you are ready to make row and column connections to the terminal block.

You may connect to the module's rows and columns by wiring to the screw terminals marked C1 through C32 (H, L) and R1 through R8 (H, L) on the silk-screen (see ["34934T-002 Terminal Block: 8x32 Layout"](#) on page 155). The combined weight of these wires can create significant strain on the wiring; to minimize this see ["Terminal block strain relief"](#) on page 157.

However, you may prefer to use physically separate cabling for the row connections (e.g. if you connect your DUT to the columns and your measurement device(s) to the rows). The terminal block's extension headers J24 and J25 provide access to the rows; you can make row connections to the central 16 terminals (pins 3-18) on the supplied terminators you installed in ["Placing Safety Interlock Continuity Jumpers for 8x32"](#) on page 161.

Below is an illustration of the possible locations for making row signal connections to the 34934T-002:

You can connect row signals to the screw terminals here...



...or to the central 16 pins of extension headers J24 and J25 here.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

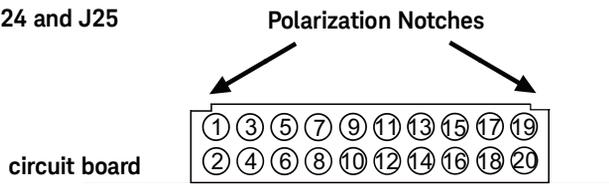
When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A **MAXIMUM** of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

The following subsection outlines how to make row connections to extension headers J24 and J25.

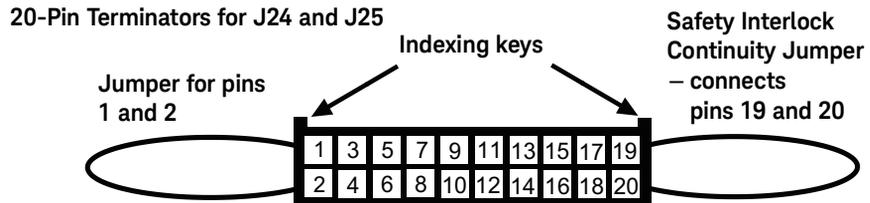
Extension Header (Row Signal) Wiring for 8x32 The position of extension headers J24 and J25 are marked on the 34934T-002's silk-screen. Oriented with the terminal side of the board up, the pin numbers for these headers are shown below.

Extension Headers J24 and J25

The supplied 20-pin terminators 34934-80006 are identical, but the pin assignments for 8x32 are not (rows 1-4 are on J24; rows 5-8 are on J25). The terminal numbers—corresponding to the headers—are shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934T-002: Extension Header Pin Assignments for 8x32

Header J24				Header J25			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No Connect	1	No Connect	2	No Connect	1	No Connect	2
R1H	3	R1H	4	R5H	3	R5H	4
R1L	5	R1L	6	R5L	5	R5L	6
R2H	7	R2H	8	R6H	7	R6H	8
R2L	9	R2L	10	R6L	9	R6L	10
R3H	11	R3H	12	R7H	11	R7H	12
R3L	13	R3L	14	R7L	13	R7L	14
R4H	15	R4H	16	R8H	15	R8H	16
R4L	17	R4L	18	R8L	17	R8L	18
IL1	19	IL2	20	IL3	19	IL4	20

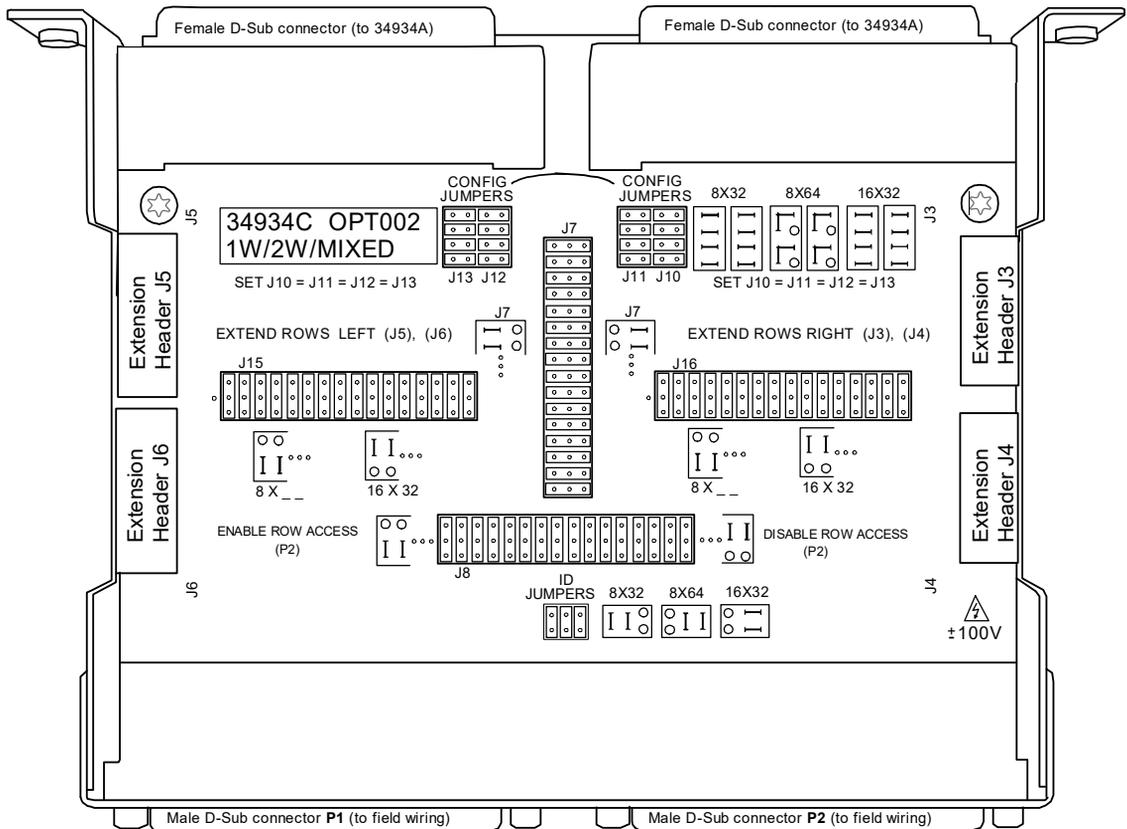
Note that each row signal (R1 through R8 (H,L) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going to create an expanded-row matrix using multiple modules and terminal blocks, plan your wiring placement for each terminal block carefully, to allow for a daisy-chain interconnection between terminal blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Terminal Blocks”](#) on page 257.

Configuring an 8x32 Matrix using a Configuration Block

34934C-002 Configuration Block: Layout

This configuration block is labeled with the model number and the abbreviated module name.



Note five types of on-board jumpers you'll have to set to configure this board; some will be familiar from the configuration descriptions of the *terminal blocks*, but some are new. Instructions for setting these are provided in ["34934C-002 Configuration Block: 8x32 Jumper Configuration"](#) on page 168.

- ID Jumpers
- CONFIG Jumpers (J10, J11, J12 and J13)
- Row Extension Jumpers (J7)
- Row Access Jumpers (J8)
- Row Setting Jumpers (J15 and J16)

WARNING

Do not install substitute parts or perform any unauthorized modifications to the instrument to ensure the safety features are maintained in operational condition. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

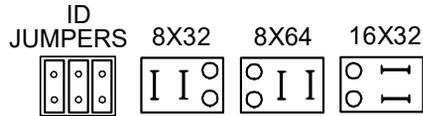
Also note the four extension headers (J3, J4, J5 and J6). Instructions for wiring to these are provided in [“34934C-002 Configuration Block: Wiring”](#) on page 174.

34934C-002 Configuration Block: 8x32 Jumper Configuration

The configuration block has five types of jumpers which must be connected to configure it for use in an expanded 4-row matrix. In this section, you will place:

- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins near the bottom center of the board. See [“Placing ID Jumpers on the 34934C-002 Configuration Block for 8x32”](#) on page 168.
- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board’s female D-Sub connectors. See [“Placing CONFIG Jumpers on the 34934C-002 Configuration Block for 8x32”](#) on page 169.
- **Row Extension Jumpers:** place these 16-pin jumpers on a 16x3 block (J7) of jumper pins at the board’s center. See [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 170.
- **Row Access Jumpers:** place these 16-pin jumpers on a 3x16 block of jumper pins located just above the ID Jumpers. See [“Placing Row Access Jumpers on the 34934C-002 Configuration Block”](#) on page 172.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 on the four 20-pin extension headers at the sides of the configuration block. See [“Placing Row Setting Jumpers on the 34934C-002 Configuration Block”](#) on page 172.

Placing ID Jumpers on the 34934C-002 Configuration Block for 8x32 The ID jumpers allow the 34980A to recognize the base matrix configuration used by the 34934C-002, upon mainframe bootup. The ID jumpers are placed in a 2x3 jumper block, located below jumper block J8. Jumper placement guidance is provided on the configuration block’s silk-screen, to the right of the jumpers:



To set the correct ID code for 8x32, place two vertically positioned jumpers in the left position, as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing CONFIG Jumpers on the 34934C-002 Configuration Block for 8x32

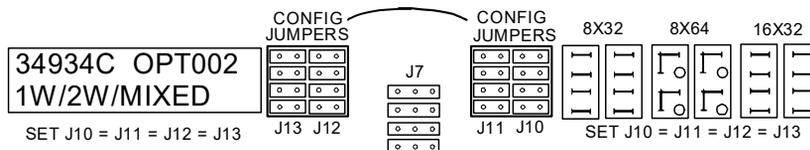
The CONFIG jumper area—shown below with jumpers removed— includes an illustration to its right that shows jumper placement for setting the 8x32, 8x64, and 16x32 base matrix configurations.

NOTE

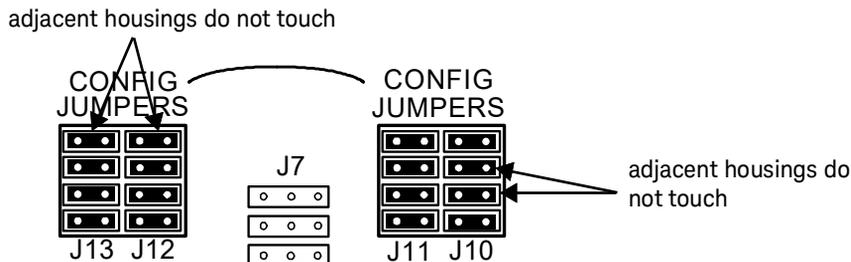
In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.



To set the CONFIG jumpers for 8x32, place 16 horizontally positioned jumpers as shown below.



WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

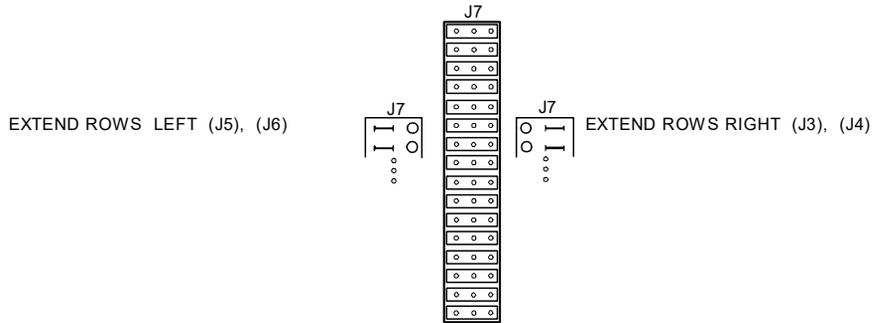
Placing Row Extension Jumpers on the 34934C-002 Configuration Block

The jumpers in 16x3 jumper block J7 (at board center) determine if row connections can be made from the left or right side of the board (i.e. they “extend” the row signal connection points out to *only one* of the two pairs of blue 20-pin connectors at the sides of the configuration block).

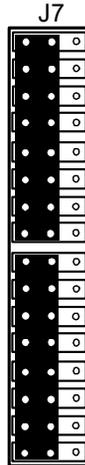
Note that “row connections” at minimum include the connections between modules in the matrix, but may also include your access from field wiring to the matrix rows.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling, depending on slot placement of the individual 34934A modules and their corresponding 34934C-002 blocks.

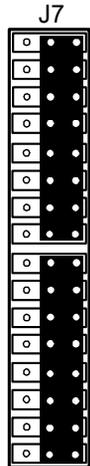
The following illustration shows the jumper block J7, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



If you plan to extend rows left, to J5 and J6, place two 2x8 jumpers as shown at right.



If you plan to extend rows right, to J3 and J4, place two 2x8 jumpers as shown at right.

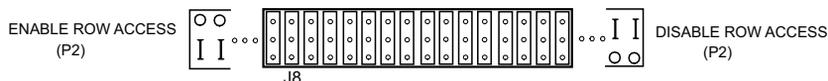


Placing Row Access Jumpers on the 34934C-002 Configuration Block The jumpers in 3x16 jumper block J8 (at lower board center) determine if row connections can be made from the male D-Sub connector P2 (i.e. they “enable” the row signal connection points at this D-sub).

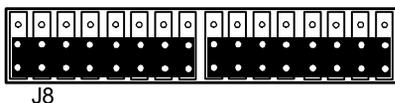
These “row connections” are the traditional access points from field wiring to the matrix rows. In practice, your arrangement of test instrumentation and DUT test points will determine if you wish to make row connections at the D-sub, at the blue 20-pin extension connectors, or both.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling.

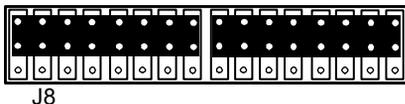
The following illustration shows the jumper block J8, along with the relevant guidance provided on the board’s silk-screen for jumper placement.



If you plan to connect row signals to D-sub connector P2, place two 2x8 jumpers on block J8, as shown below.



If you do not need to connect row signals to D-sub connector P2, place two 2x8 jumpers on block J8, as shown below.

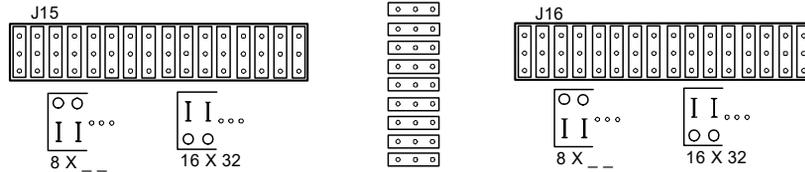


Note that by not routing signals to the D-sub connectors, module bandwidth may be preserved and capacitive loading minimized.

Placing Row Setting Jumpers on the 34934C-002 Configuration Block

The jumpers in 3x16 jumper blocks J 15 and J16 (left and right of lower board center) set the row depth (i.e. 8-rows or 16-rows) for the expanded matrix. In conjunction with the CONFIG jumpers, these jumpers set the matrix configuration.

The following illustration shows jumper blocks J15 and J16, along with the relevant guidance provided on the board's silk-screen for jumper placement.



For a base configuration of 8x32, place two 2x8 jumpers *on each of* blocks J15 and J16, as shown below.



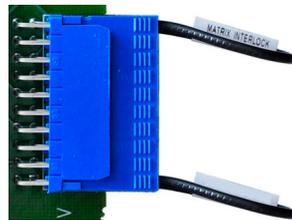
Placing Safety Interlock Continuity Jumpers on the 34934C-002 Configuration Block

At each side of the configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. In all cases, pins 19 and 20 are the safety interlock pins.

These headers provide for row extension and row signal access, depending on the placement of the jumpers in J7. You must have set the jumpers to extend rows either to the left or right side of the configuration block (see [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 170).

To provide continuity for the module's safety interlock function, pins 19 and 20 on *both* extension headers on the selected side (left or right) must be shorted.

- For each of these headers, install one of the supplied keyed 20-pin terminators 34934-80006 securely in that header, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2.

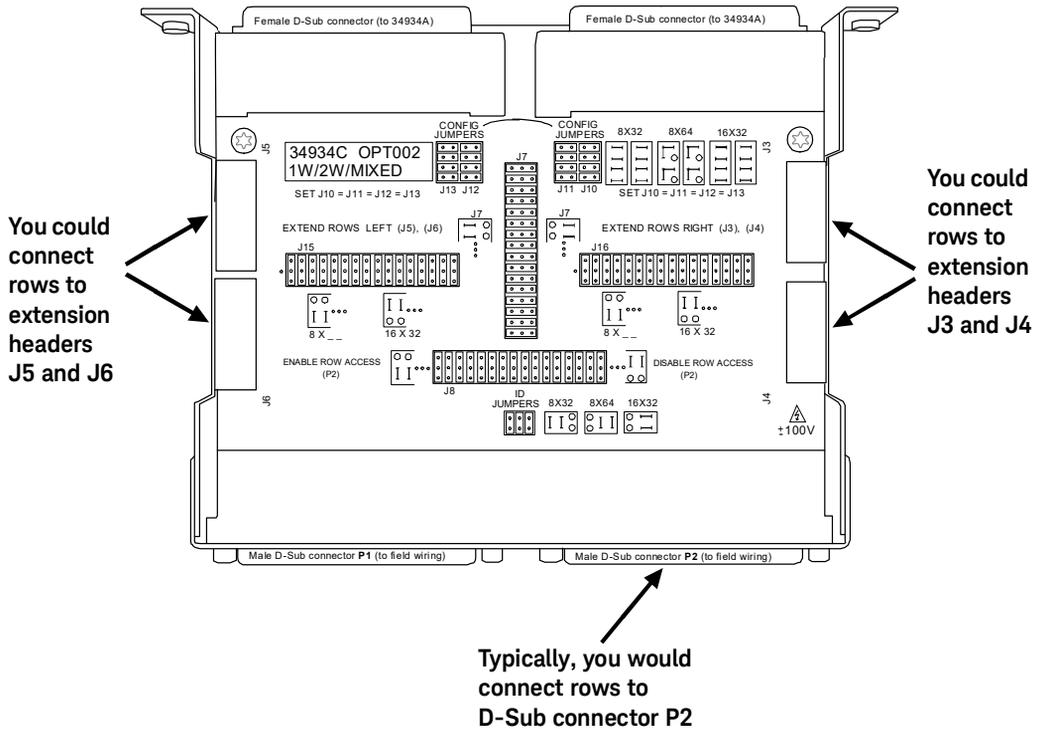
- The extension headers on the opposite (unused) side of the 34934C-002 do not require a safety interlock terminator; these will be hidden by the configuration block's cover.

34934C-002 Configuration Block: Wiring

The next two subsections outline options for making row and column wiring connections to the configuration block.

Extension Header (Row Signal) Wiring to the 34934C-002

Below is an illustration of the possible locations for making row signal connections to the 34934T-002:



Typically, you could make both row and column connections to each module by fabricating custom cabling terminating in female D-Sub connectors. These attach to the configuration block's male D-Sub connectors P1 and P2. For 8x32, all rows connect to P2.

Alternately, you may choose to connect only columns (typically test points on your DUT) to the D-Subs. There may be physical or electrical advantages in making your *row* connections (typically measurement devices) to the configuration block's blue extension connectors J3, J4, J5 and J6. To provide this flexibility, two extension headers are provided on each side of the configuration block.

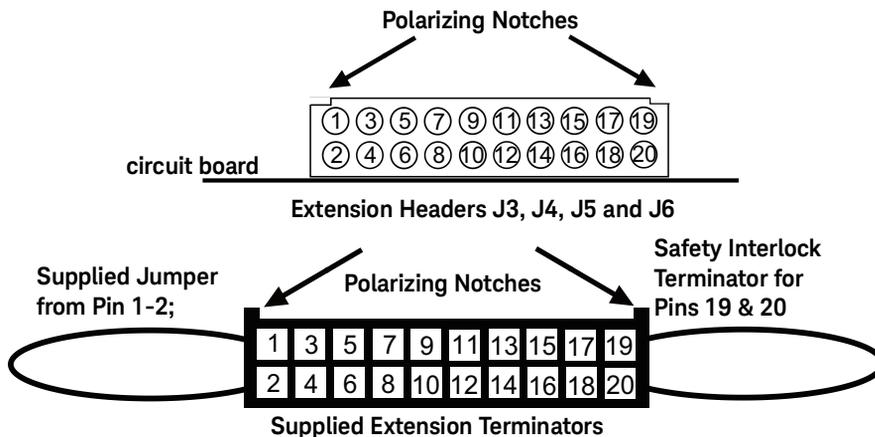
You must have set the jumpers (in [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 170) to extend rows to two of the four headers. We'll call these the “live” headers.

In [“Placing Safety Interlock Continuity Jumpers on the 34934C-001 Configuration Block”](#) on page 77, you will have installed the supplied blue terminators 34934-80006 in the live headers on the configuration block.

You can now make row connections to these headers, by utilizing the central 16 pins (3-18) on the snap-in terminators. The extension header's supplied terminator is shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934C-002: Extension Header Pin Assignments for 8x32

Headers J4, J5				Headers J3, J6			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1H	3	R1H	4	R5H	3	R5H	4
R1L	5	R1L	6	R5L	5	R5L	6
R2H	7	R2H	8	R6H	7	R6H	8
R2L	9	R2L	10	R6L	9	R6L	10
R3H	11	R3H	12	R7H	11	R7H	12
R3L	13	R3L	14	R7L	13	R7L	14
R4H	15	R4H	16	R8H	15	R8H	16
R4L	17	R4L	18	R8L	17	R8L	18
IL1	19	IL2	20	IL3	19	IL4	20

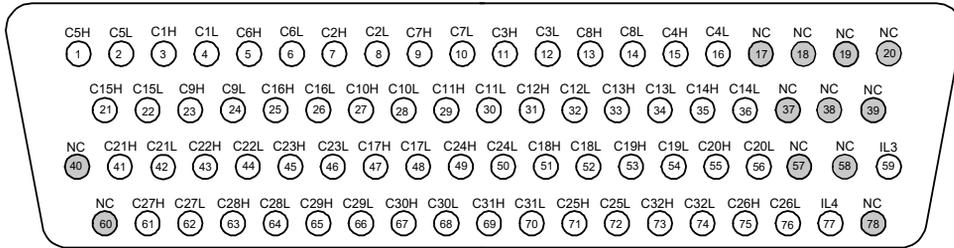
Note that each row signal (R1 through R8 (H,L) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going to create an expanded-row matrix using multiple modules and configuration blocks, plan your wiring placement for each configuration block carefully, to allow for a daisy-chain interconnection between configuration blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Configuration Blocks”](#) on page 261.

34934C-002 D-Sub Connectors: Pin Assignments for 8x32 (base configuration)



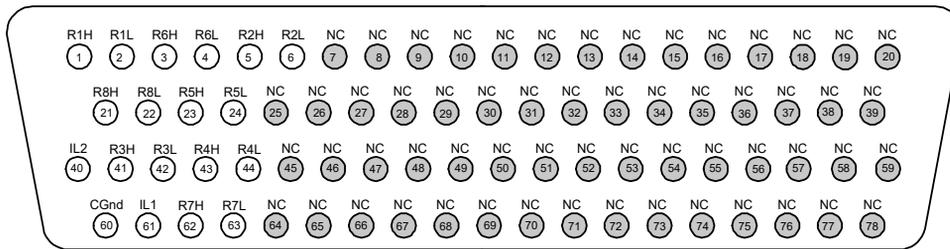
For orientation, the male D-sub connector end of the configuration block is facing you.



78-Pin D-Sub Male Connector

Bank 1 (P1)

Description	Pin										
IL3	59	C1H	3	C9H	23	C17H	47	C25H	71	No connect	17
IL4	77	C1L	4	C9L	24	C17L	48	C25L	72	No connect	18
		C2H	7	C10H	27	C18H	51	C26H	75	No connect	19
		C2L	8	C10L	28	C18L	52	C26L	76	No connect	20
		C3H	11	C11H	29	C19H	53	C27H	61	No connect	37
		C3L	12	C11L	30	C19L	54	C27L	62	No connect	38
		C4H	15	C12H	31	C20H	55	C28H	63	No connect	39
		C4L	16	C12L	32	C20L	56	C28L	64	No connect	40
		C5H	1	C13H	33	C21H	41	C29H	65	No connect	57
		C5L	2	C13L	34	C21L	42	C29L	66	No connect	58
		C6H	5	C14H	35	C22H	43	C30H	67	No connect	60
		C6L	6	C14L	36	C22L	44	C30L	68	No connect	78
		C7H	9	C15H	21	C23H	45	C31H	69		
		C7L	10	C15L	22	C23L	46	C31L	70		
		C8H	13	C16H	25	C24H	49	C32H	73		
		C8L	14	C16L	26	C24L	50	C32L	74		



78-Pin D-Sub
Male Connector
Bank 2 (P2)

Description	Pin										
R1H	1	No connect	7	No connect	25	No connect	45	No connect	64	IL1	61
R1L	2	No connect	8	No connect	26	No connect	46	No connect	65	IL2	40
R2H	5	No connect	9	No connect	27	No connect	47	No connect	66	Com Gnd	60
R2L	6	No connect	10	No connect	28	No connect	48	No connect	67		
R3H	41	No connect	11	No connect	29	No connect	49	No connect	68		
R3L	42	No connect	12	No connect	30	No connect	50	No connect	69		
R4H	43	No connect	13	No connect	31	No connect	51	No connect	70		
R4L	44	No connect	14	No connect	32	No connect	52	No connect	71		
R5H	23	No connect	15	No connect	33	No connect	53	No connect	72		
R5L	24	No connect	16	No connect	34	No connect	54	No connect	73		
R6H	3	No connect	17	No connect	35	No connect	55	No connect	74		
R6L	4	No connect	18	No connect	36	No connect	56	No connect	75		
R7H	62	No connect	19	No connect	37	No connect	57	No connect	76		
R7L	63	No connect	20	No connect	38	No connect	58	No connect	77		
R8H	21			No connect	39	No connect	59	No connect	78		
R8L	22										

34934C-001 D-Sub Connectors: Wiring for 8x32

WARNING

For the 3434C-001, the D-Sub connector pin assignments are identical to the D-Sub connectors on the 34934A module. For the 34934C-002, the D-Sub connector pin assignments are not identical to the D-Sub connectors on the 34934A module. Pay careful attention to the wiring requirements found in the instructions when making D-Sub connections! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

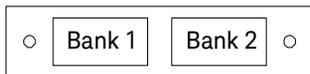
Safety Interlock Continuity While wiring to the 34934C-002's D-Sub connectors, you *must* make the following connections—in addition to the 8x32 row and column connections—to provide for safety interlock continuity:

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

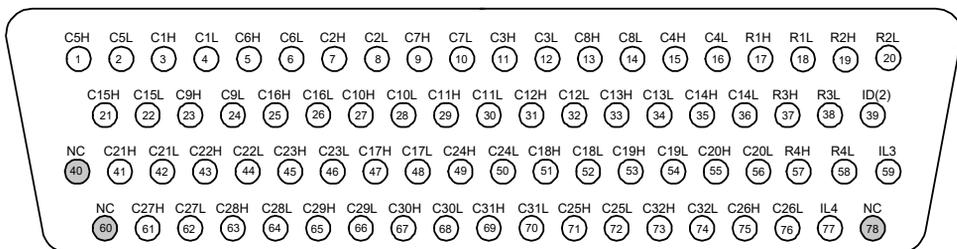
Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

Configuring an 8x32 Matrix by Direct Wiring

34934A module D-Sub Connectors: 8x32 Pin Assignments

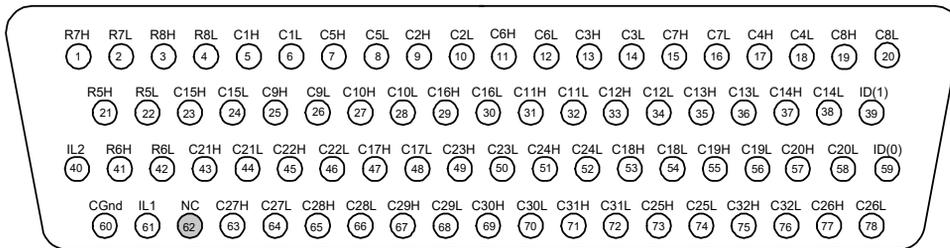


For orientation, the D-sub connector end of the module is facing you.



78-Pin D-Sub Male Connector
Bank 1

Description	Pin										
R1H	17	C1H	3	C9H	23	C17H	47	C25H	71	ID2	39
R1L	18	C1L	4	C9L	24	C17L	48	C25L	72	IL3	59
R2H	19	C2H	7	C10H	27	C18H	51	C26H	75	IL4	77
R2L	20	C2L	8	C10L	28	C18L	52	C26L	76	No connect	40
R3H	37	C3H	11	C11H	29	C19H	53	C27H	61	No connect	60
R3L	38	C3L	12	C11L	30	C19L	54	C27L	62	No connect	78
R4H	57	C4H	15	C12H	31	C20H	55	C28H	63		
R4L	58	C4L	16	C12L	32	C20L	56	C28L	64		
		C5H	1	C13H	33	C21H	41	C29H	65		
		C5L	2	C13L	34	C21L	42	C29L	66		
		C6H	5	C14H	35	C22H	43	C30H	67		
		C6L	6	C14L	36	C22L	44	C30L	68		
		C7H	9	C15H	21	C23H	45	C31H	69		
		C7L	10	C15L	22	C23L	46	C31L	70		
		C8H	13	C16H	25	C24H	49	C32H	73		
		C8L	14	C16L	26	C24L	50	C32L	74		



78-Pin D-Sub Male Connector
Bank 2

Description	Pin										
R5H	21	C1H	5	C9H	25	C17H	47	C25H	73	ID1	39
R5L	22	C1L	6	C9L	26	C17L	48	C25L	74	ID0	59
R6H	41	C2H	9	C10H	27	C18H	53	C26H	77	IL1	61
R6L	42	C2L	10	C10L	28	C18L	54	C26L	78	IL2	40
R7H	1	C3H	13	C11H	31	C19H	55	C27H	63	Com Gnd	60
R7L	2	C3L	14	C11L	32	C19L	56	C27L	64	No connect	62
R8H	3	C4H	17	C12H	33	C20H	57	C28H	65		
R8L	4	C4L	18	C12L	34	C20L	58	C28L	66		
		C5H	7	C13H	35	C21H	43	C29H	67		
		C5L	8	C13L	36	C21L	44	C29L	68		
		C6H	11	C14H	37	C22H	45	C30H	69		
		C6L	12	C14L	38	C22L	46	C30L	70		
		C7H	15	C15H	23	C23H	49	C31H	71		
		C7L	16	C15L	24	C23L	50	C31L	72		
		C8H	19	C16H	29	C24H	51	C32H	75		
		C8L	20	C16L	30	C24L	52	C32L	76		

34934A Module D-Sub Connectors: 8x32 Wiring

When wiring directly to the 34934A's D-Sub connectors, you *must* make the following connections—in addition to row and column connections—to configure the 8x32 matrix (matrices):

Module Configuration: The connections listed below short the rows in the 34934A's native 4x4x32 configuration to create the 8x32 MH and 8x32 ML matrices.

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2	To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2
C1H	3	5	C17H	47	47
C1L	4	6	C17L	48	48
C2H	7	9	C18H	51	53
C2L	8	10	C18L	52	54
C3H	11	13	C19H	53	55
C3L	12	14	C19L	54	56
C4H	15	17	C20H	55	57
C4L	16	18	C20L	56	58
C5H	1	7	C21H	41	43
C5L	2	8	C21L	42	44
C6H	5	11	C22H	43	45
C6L	6	12	C22L	44	46
C7H	9	15	C23H	45	49
C7L	10	16	C23L	46	50
C8H	13	19	C24H	49	51
C8L	14	20	C24L	50	52
C9H	23	25	C25H	71	73
C9L	24	26	C25L	72	74
C10H	27	27	C26H	75	77
C10L	28	28	C26L	76	78
C11H	29	31	C27H	61	63
C11L	30	32	C27L	62	64
C12H	31	33	C28H	63	65
C12L	32	34	C28L	64	66
C13H	33	35	C29H	65	67

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2	To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2
C13L	34	36	C29L	66	68
C14H	35	37	C30H	67	69
C14L	36	38	C30L	68	70
C15H	21	23	C31H	69	71
C15L	22	24	C31L	70	72
C16H	25	29	C32H	73	75
C16L	26	30	C32L	74	76

Note that these connections require wires run *between* the two D-Sub connectors. You may make these connections near your test instrumentation or DUT, or near the D-Sub ends of your custom cabling.

Module ID: The open pins listed below allow the 34980A mainframe to recognize the 8x32 matrix configuration.

Ground: Pin 39 on Bank 1 (ID bit 2)
 Ground: Pin 39 on Bank 2 (ID bit 1)
 Leave Open: Pin 59 on Bank 2 (ID bit 0)

Safety Interlock Continuity: The connections listed below provide for safety interlock continuity.

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1
 Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

NOTE

If you use the 34934C-002 Configuration Block—in conjunction with the 34934A module—to make wiring connections, the additional **Module Configuration** and **Module ID** connections are *not required*. Those connections are made within the hardware of the configuration block. You would need only make the **Safety Interlock Continuity** connections.

8x64 Matrix Configuration

To create an 8x64 matrix, you may connect your field wiring to the 34934A by three methods:

Terminal Block You can use terminal block 34934T-002. The terminal block connects to the 34934A's two D-Sub connectors; it provides 12 blocks of 12 screw terminals each, and an *8x64 removable overlay* (supplied with the 34934T-002) which provides guidance for field wiring the 8x64 matrices. Follow these sections in order to configure and wire the terminal block:

- “34934T-002 Terminal Block: 8x64 Layout” on page 186
- “34934T-002 Terminal Block: 8x64 Jumper Configuration” on page 190
- “34934T-002 Terminal Block: 8x64 Wiring” on page 193

Configuration Block You can use configuration block 34934C-001. Follow these sections in order to configure and wire the configuration block:

- “34934C-002 Configuration Block: Layout” on page 197
- “34934C-002 Configuration Block: 8x64 Jumper Configuration” on page 199
- “34934C-002 Configuration Block: Wiring” on page 205

WARNING

Do NOT use the 34934C-001 with the 34934T-001 terminal block for field wiring connections! This combination is not mechanically reliable and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring You can wire directly to the module's D-Sub connectors using custom cabling. You will have to short specific pins on and between the D-Sub connectors to create the 8x64 configuration. Review *both* these sections *before* wiring to the module's D-Subs:

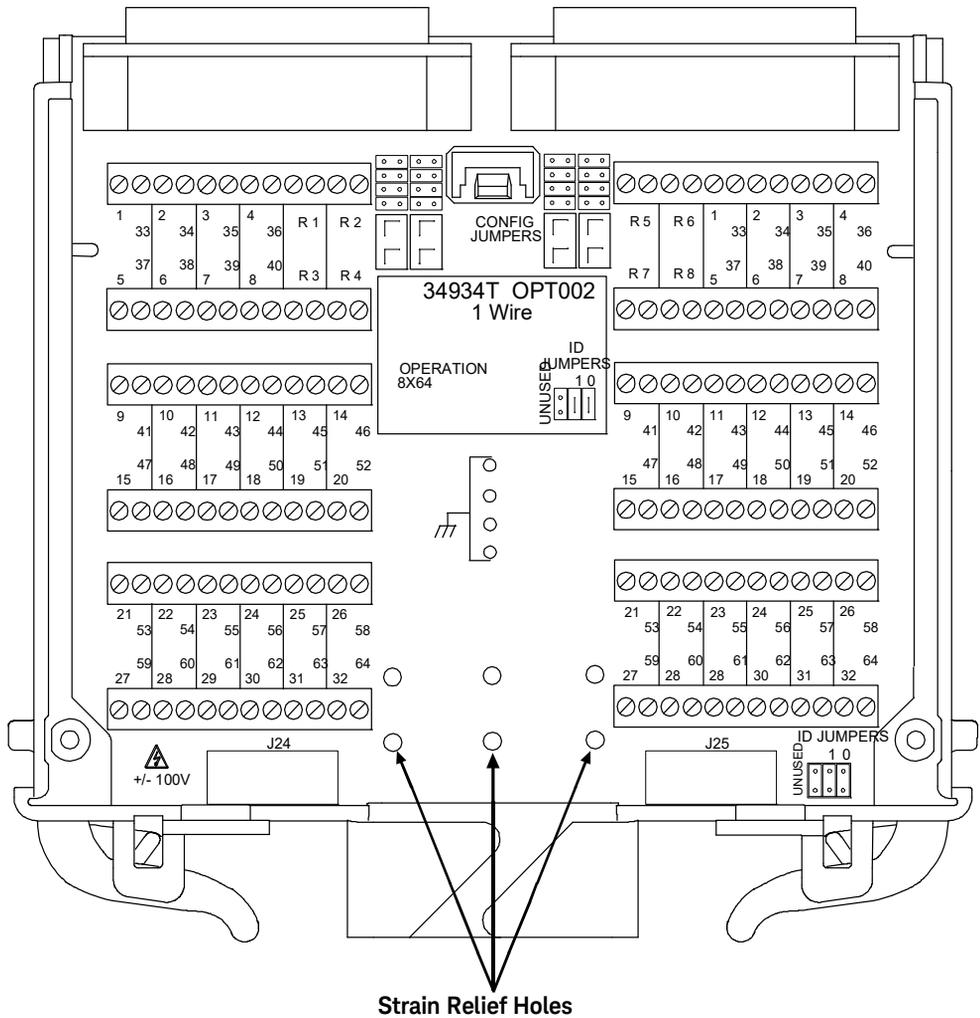
- “34934A module D-Sub Connectors: 8x64 Pin Assignments” on page 212
- “34934A Module D-Sub Connectors: 8x64 Wiring” on page 214

Configuring an 8x64 Matrix using a Terminal Block

34934T-002 Terminal Block: 8x64 Layout

This terminal block with screw-type connections is labeled with the model number and the abbreviated module name. In addition, space is available on the label for you to write the slot number.

This terminal block's *8x64 removable overlay* shows the row and column terminals for the 8x64 matrix configuration:



Note the six strain relief holes provided in the terminal block PCB. See the following page for instructions on providing wiring strain relief.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

34934T-002 Terminal Block: 8x64 Terminal Wiring Options

On the 34934T-002 terminal block, there are redundant sets of terminals provided for columns 1-64, one set on each side of the block. The column terminals on each side are shorted by traces in the block to their like-labeled counterparts on the other; you may make any column connection to the appropriate terminal on either side.

Terminal block strain relief

To reduce strain on the wiring at the terminals and maintain connection reliability, a strain relief feature has been incorporated into the terminal block design. Six holes have been placed near the lower edge of the circuit board, lined up with holes in the terminal block's bottom cover, as seen on "[34934T-002 Terminal Block: 8x64 Layout](#)" on page 186.

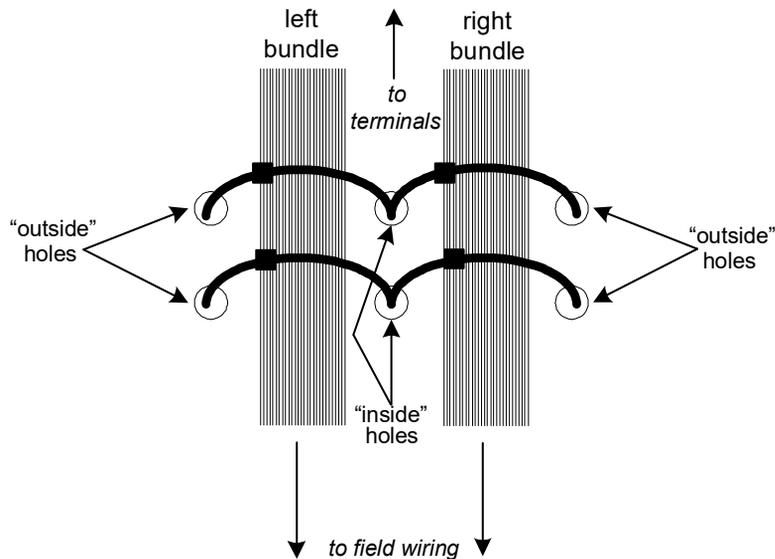
WARNING

Always include wiring strain relief to reduce strain on the connections. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

To use this feature:

- 1 Once all jumpers are installed and wiring is connected to the terminals, arrange the wires leading away from the terminal block in two bundles. This is easiest if the left bundle leads to the terminals on the left side of the block, and conversely for the right side.
- 2 Insert a cable tie (tie-wrap, zip-tie) down through each of the four outside holes in the PCB, continuing down through the holes in the terminal block's bottom cover.
- 3 Route these cable ties up through the center holes in the bottom cover and PCB.
- 4 Close and tighten the cable ties as required, and remove any excess so that it does not interfere with cover closure.

The completed bundles are shown below.



34934T-002 Terminal Block: 8x64 Jumper Configuration

This terminal block has three types of jumpers which must be connected to configure the 8x64 matrix (matrices):

- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board's female D-Sub connectors, as shown.
- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins on the lower right corner of the board, as shown.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 or equivalent custom wiring on the two 20-pin extension headers at the bottom of the terminal block, as shown.

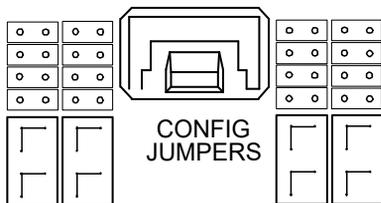
NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

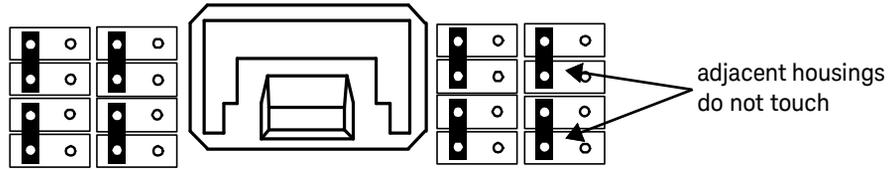
WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

Placing CONFIG Jumpers for 8x64 The CONFIG jumper area—shown below with jumpers removed— includes an illustration (on the 8x64 *removable overlay*) that shows jumper placement for setting a 8x64 matrix configuration.

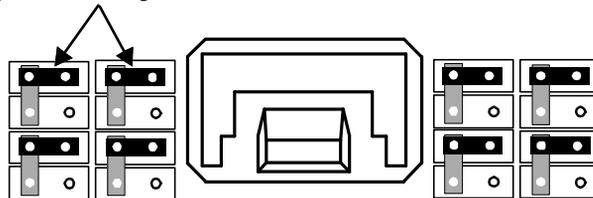


- 1 Place eight vertically positioned jumpers as shown below.



- 2 Then *stack* eight horizontally positioned jumpers *above* the first set of eight, as shown below.

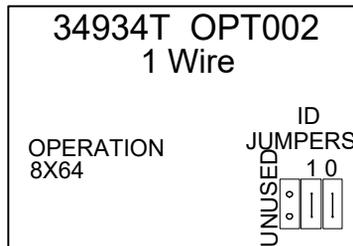
adjacent housings do not touch



WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Placing ID Jumpers for 8x64 The ID jumpers allow the 34980A to recognize the 8x64 matrix configuration upon mainframe bootup. Jumper placement guidance is provided (on the *8x64 removable overlay*) as shown below left; the ID jumper block is shown at right with jumpers removed.



Location of placement guidance for ID Jumpers

ID JUMPERS

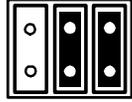
1 0



ID JUMPER block

To set the correct ID code for 8x64, place two vertically positioned jumpers as shown below.

ID JUMPERS



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing Safety Interlock Continuity Jumpers for 8x64 At the bottom (end opposite the D-Sub connectors) of the terminal block are two 20-pin extension headers—marked J24 and J25 on the circuit board. These headers provide access to the module's rows. Pins 19 and 20 on each header must be shorted to provide continuity for the module's safety interlock function.

Two keyed 20-pin terminators 34934-80006 are supplied for providing safety interlock continuity. Install the terminators securely in the headers, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2. Detailed pin diagrams for these terminators are provided on [page 195](#).

34934T-002 Terminal Block: 8x64 Wiring

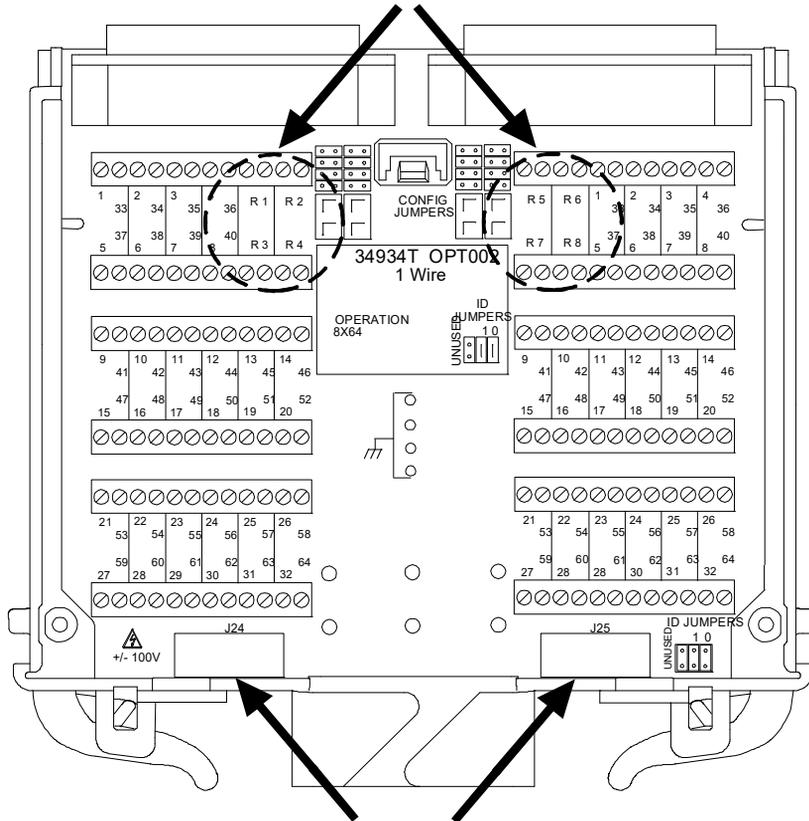
Once the required jumpers have been placed to configure the terminal block as 8x64, you are ready to make row and column connections to the terminal block.

You may connect to the module's rows and columns by wiring to the screw terminals marked C1 through C64 (H, L) and R1 through R8 (H, L) on the silk-screen (see "[34934T-002 Terminal Block: 8x64 Layout](#)" on page 186). The combined weight of these wires can create significant strain on the wiring; to minimize this see "[Terminal block strain relief](#)" on page 188.

However, you may prefer to use physically separate cabling for the row connections (e.g. if you connect your DUT to the columns and your measurement device(s) to the rows). The terminal block's extension headers J24 and J25 provide access to the rows; you can make row connections to the central 16 terminals (pins 3-18) on the supplied terminators you installed in "[Placing Safety Interlock Continuity Jumpers for 8x64](#)" on page 192.

Below is an illustration of the possible locations for making row signal connections to the 34934T-002:

You can connect row signals to the screw terminals here...



...or to the central 16 pins of extension headers J24 and J25 here.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

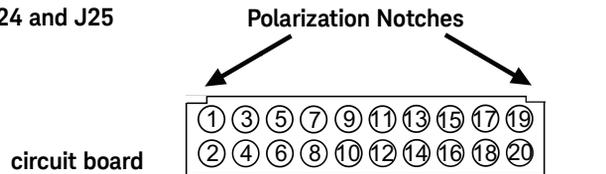
When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

The following subsection outlines how to make row connections to extension headers J24 and J25.

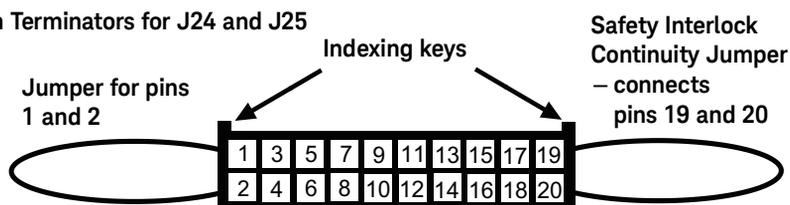
Extension Header (Row Signal) Wiring for 8x64 The position of extension headers J24 and J25 are marked on the 34934T-002's silk-screen. Oriented with the terminal side of the board up, the pin numbers for these headers are shown below.

Extension Headers J24 and J25

The supplied 20-pin terminators 34934-80006 are identical, but the pin assignments for 8x64 are not (rows 1-4 are on J24; rows 5-8 are on J25). The terminal numbers—corresponding to the headers—are shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934T-002: Extension Header Pin Assignments for 8x64

Headers J24				Headers J25			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1	3	R1	4	R5	3	R5	4
No connect	5	No connect	6	No connect	5	No connect	6
R2	7	R2	8	R6	7	R6	8
No connect	9	No connect	10	No connect	9	No connect	10
R3	11	R3	12	R7	11	R7	12
No connect	13	No connect	14	No connect	13	No connect	14
R4	15	R4	16	R8	15	R8	16
No connect	17	No connect	18	No connect	17	No connect	18
IL1	19	IL2	20	IL3	19	IL4	20

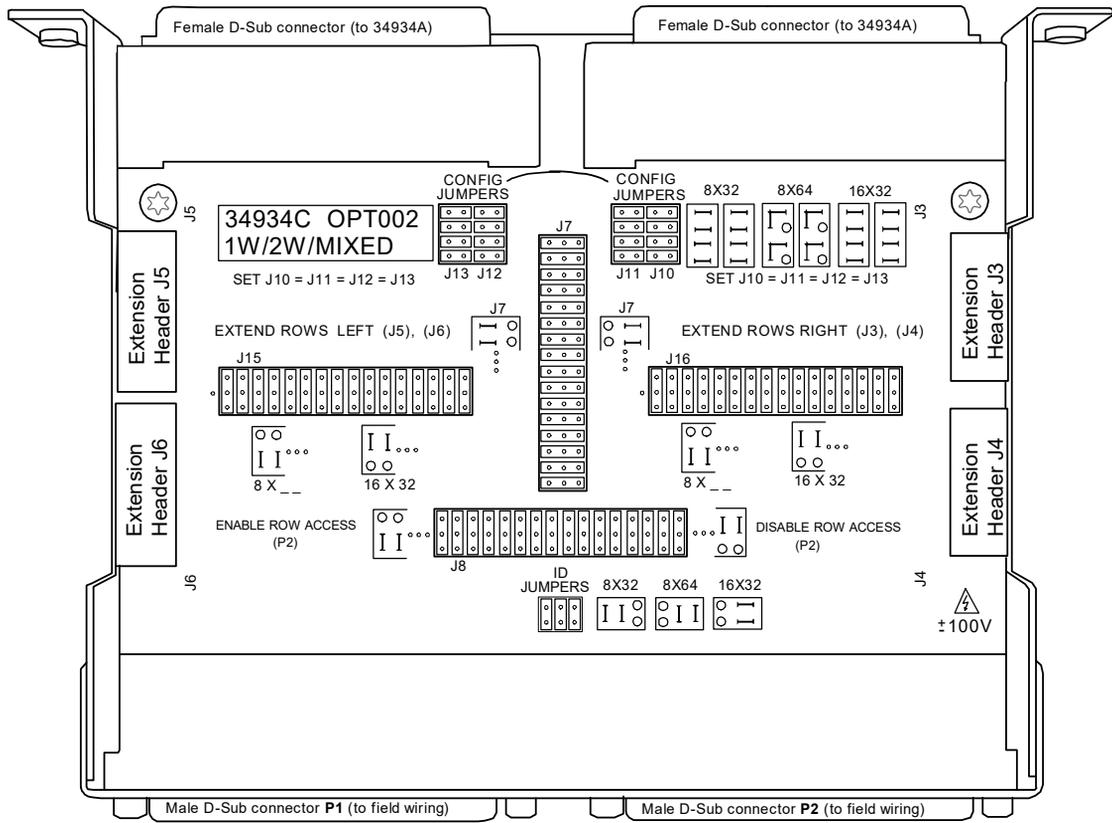
Note that each row signal (R1 through R8 (H,L) is routed out on four pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going to create an expanded-row matrix using multiple modules and terminal blocks, plan your wiring placement for each terminal block carefully, to allow for a daisy-chain interconnection between terminal blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Terminal Blocks”](#) on page 257.

Configuring an 8x64 Matrix using a Configuration Block

34934C-002 Configuration Block: Layout

This configuration block is labeled with the model number and the abbreviated module name.



Note five types of on-board jumpers you'll have to set to configure this board; some will be familiar from the configuration descriptions of the *terminal blocks*, but some are new. Instructions for setting these are provided in ["34934C-002 Configuration Block: 8x64 Jumper Configuration"](#) on page 199.

- ID Jumpers
- CONFIG Jumpers (J10, J11, J12 and J13)
- Row Extension Jumpers (J7)
- Row Access Jumpers (J8)
- Row Setting Jumpers (J15 and J16)

WARNING

Do not install substitute parts or perform any unauthorized modifications to the instrument to ensure the safety features are maintained in operational condition. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

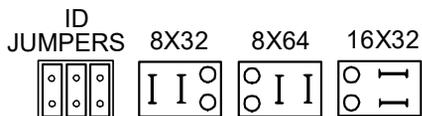
Also note the four extension headers (J3, J4, J5 and J6). Instructions for wiring to these are provided in [“34934C-002 Configuration Block: Wiring”](#) on page 205.

34934C-002 Configuration Block: 8x64 Jumper Configuration

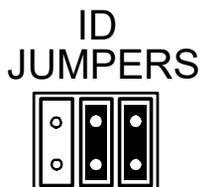
The configuration block has five types of jumpers which must be connected to configure it for use in an expanded 4-row matrix. In this section, you will place:

- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins near the bottom center of the board. See [“Placing ID Jumpers on the 34934C-002 Configuration Block for 8x64”](#) on page 199.
- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board’s female D-Sub connectors. See [“Placing CONFIG Jumpers on the 34934C-002 Configuration Block for 8x64”](#) on page 200.
- **Row Extension Jumpers:** place these 16-pin jumpers on a 16x3 block (J7) of jumper pins at the board’s center. See [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 202.
- **Row Access Jumpers:** place these 16-pin jumpers on a 3x16 block of jumper pins located just above the ID Jumpers. See [“Placing Row Access Jumpers on the 34934C-002 Configuration Block”](#) on page 203.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 on the four 20-pin extension headers at the sides of the configuration block. See [“Placing Safety Interlock Continuity Jumpers on the 34934C-002 Configuration Block”](#) on page 204.

Placing ID Jumpers on the 34934C-002 Configuration Block for 8x64 The ID jumpers allow the 34980A to recognize the base matrix configuration used by the 34934C-002, upon mainframe bootup. The ID jumpers are placed in a 2x3 jumper block, located below jumper block J8. Jumper placement guidance is provided on the configuration block’s silk-screen, to the right of the jumpers:



To set the correct ID code for 8x64, place two vertically positioned jumpers in the right position, as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing CONFIG Jumpers on the 34934C-002 Configuration Block for 8x64

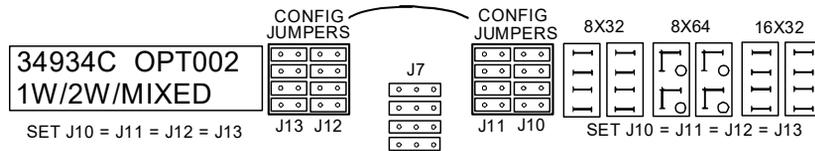
The CONFIG jumper area—shown below with jumpers removed— includes an illustration to its right that shows jumper placement for setting the 8x32, 8x64, and 16x32 base matrix configurations.

NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation > 10 GΩ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

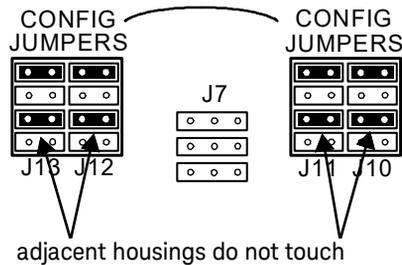
WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

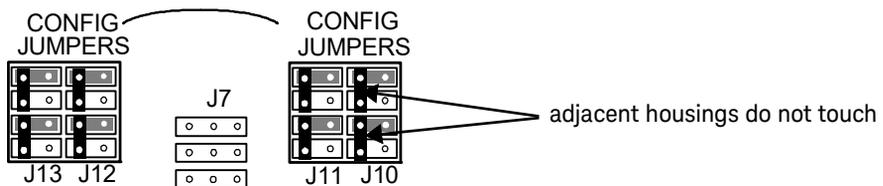


To set the CONFIG jumpers for 8x64:

- 1 Place eight horizontally positioned jumpers as shown below.



- 2 Then stack eight vertically positioned jumpers as shown below.

**WARNING**

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

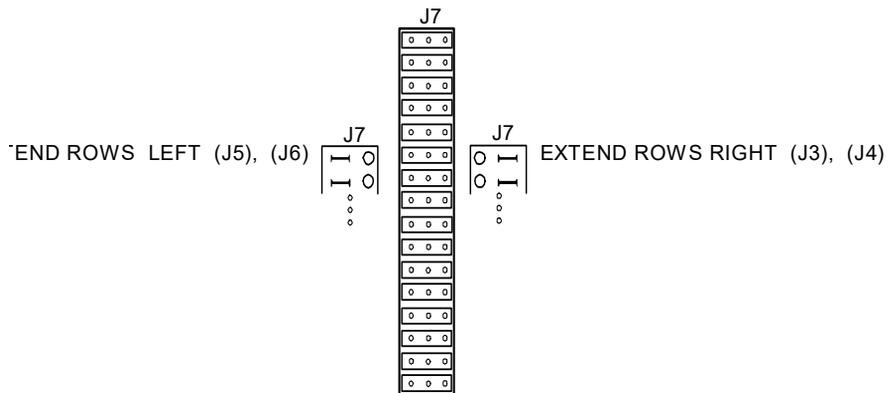
Placing Row Extension Jumpers on the 34934C-002 Configuration Block

The jumpers in 16x3 jumper block J7 (at board center) determine if row connections can be made from the left or right side of the board (i.e. they “extend” the row signal connection points out to *only one* of the two pairs of blue 20-pin connectors at the sides of the configuration block).

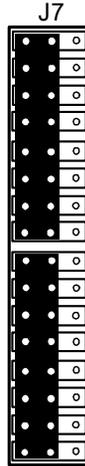
Note that “row connections” at minimum include the connections between modules in the matrix, but may also include your access from field wiring to the matrix rows.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling, depending on slot placement of the individual 34934A modules and their corresponding 34934C-002 blocks.

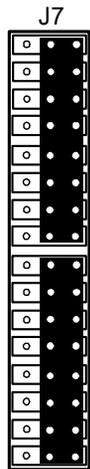
The following illustration shows the jumper block J7, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



If you plan to extend rows left, to J5 and J6, place two 2x8 jumpers as shown at right.



If you plan to extend rows right, to J3 and J4, place two 2x8 jumpers as shown at right.



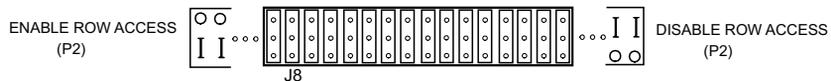
Placing Row Access Jumpers on the 34934C-002 Configuration Block

The jumpers in 3x16 jumper block J8 (at lower board center) determine if row connections can be made from the male D-Sub connector P2 (i.e. they “enable” the row signal connection points at this D-sub).

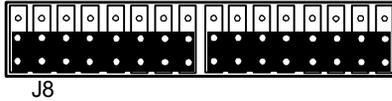
These “row connections” are the traditional access points from field wiring to the matrix rows. In practice, your arrangement of test instrumentation and DUT test points will determine if you wish to make row connections at the D-sub, at the blue 20-pin extension connectors, or both.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling.

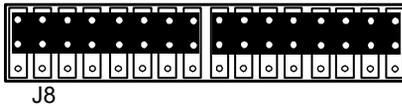
The following illustration shows the jumper block J8, along with the relevant guidance provided on the board’s silk-screen for jumper placement.



If you plan to connect row signals to D-sub connector P2, place two 2x8 jumpers on block J8, as shown below.



If you do not need to connect row signals to D-sub connector P2, place two 2x8 jumpers on block J8, as shown below.

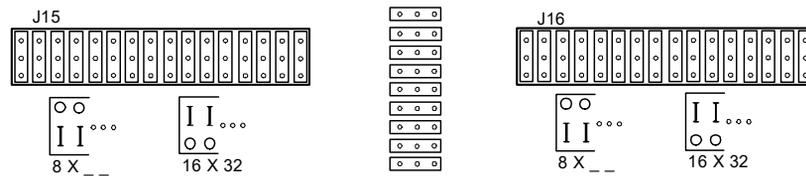


Note that by not routing signals to the D-sub connectors, module bandwidth may be preserved and capacitive loading minimized.

Placing Row Setting Jumpers on the 34934C-002 Configuration Block

The jumpers in 3x16 jumper blocks J 15 and J16 (left and right of lower board center) set the row depth (i.e. 8-rows or 16-rows) for the expanded matrix. In conjunction with the CONFIG jumpers, these jumpers set the matrix configuration.

The following illustration shows jumper blocks J15 and J16, along with the relevant guidance provided on the board's silk-screen for jumper placement.



For a base configuration of 8x64, place two 2x8 jumpers *on each of* blocks J15 and J16, as shown below.



Placing Safety Interlock Continuity Jumpers on the 34934C-002 Configuration Block

Configuration Block At each side of the configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. In all cases, pins 19 and 20 are the safety interlock pins.

These headers provide for row extension and row signal access, depending on the placement of the jumpers in J7. You must have set the jumpers to extend rows either to the left or right side of the configuration block (see [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 202).

To provide continuity for the module’s safety interlock function, pins 19 and 20 on *both* extension headers on the selected side (left or right) must be shorted.

- For each of these headers, install one of the supplied keyed 20-pin terminators 34934-80006 securely in that header, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2.

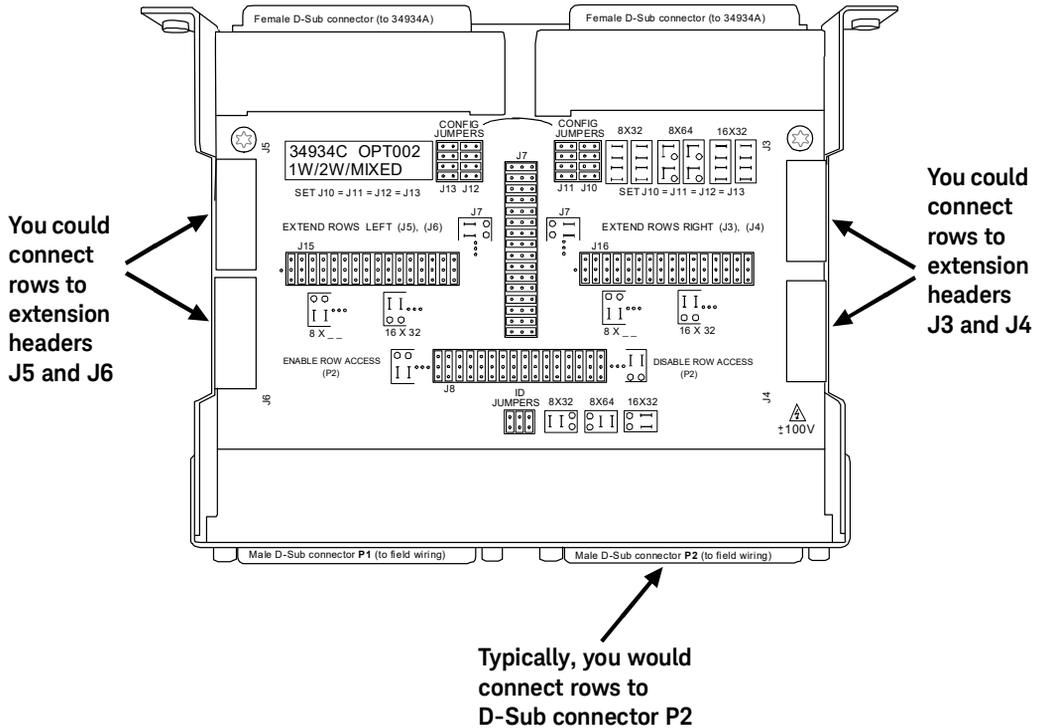
- The extension headers on the opposite (unused) side of the 34934C-002 do not require a safety interlock terminator; these will be hidden by the configuration block’s cover.

34934C-002 Configuration Block: Wiring

The next two subsections outline options for making row and column wiring connections to the configuration block.

Extension Header (Row Signal) Wiring to the 34934C-002

Below is an illustration of the possible locations for making row signal connections to the 34934T-002.



Typically, you could make both row and column connections to each module by fabricating custom cabling terminating in female D-Sub connectors. These attach to the configuration block's male D-Sub connectors P1 and P2. For 8x64, all rows connect to P2.

Alternately, you may choose to connect only columns (typically test points on your DUT) to the D-Subs. There may be physical or electrical advantages in making your *row* connections (typically measurement devices) to the configuration block's blue extension connectors J3, J4, J5 and J6. To provide this flexibility, two extension headers are provided on each side of the configuration block.

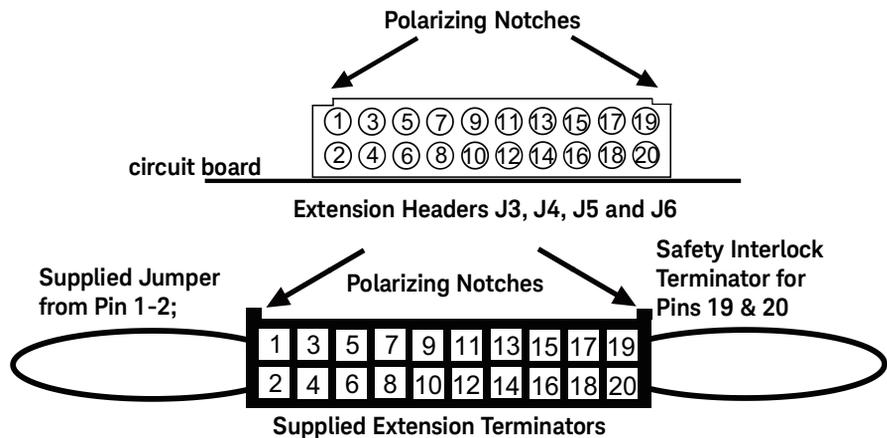
You must have set the jumpers (in ["Placing Row Extension Jumpers on the 34934C-002 Configuration Block"](#) on page 202) to extend rows to two of the four headers. We'll call these the "live" headers.

In “Placing Safety Interlock Continuity Jumpers on the 34934C-002 Configuration Block” on page 204, you will have installed the supplied blue terminators 34934-80006 in the live headers on the configuration block.

You can now make row connections to these headers, by utilizing the central 16 pins (3-18) on the snap-in terminators. The extension header’s supplied terminator is shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934C-002: Extension Header Pin Assignments for 8x64

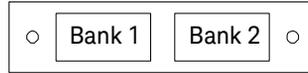
Description	Headers J4, J5		Headers J3, J6				
	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1	3	R1	4	R5	3	R5	4
No connect	5	No connect	6	No connect	5	No connect	6
R2	7	R2	8	R6	7	R6	8
No connect	9	No connect	10	No connect	9	No connect	10
R3	11	R3	12	R7	11	R7	12
No connect	13	No connect	14	No connect	13	No connect	14
R4	15	R4	16	R8	15	R8	16
No connect	17	No connect	18	No connect	17	No connect	18
IL1	19	IL2	20	IL3	19	IL4	20

Note that each row signal (R1 through R8) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

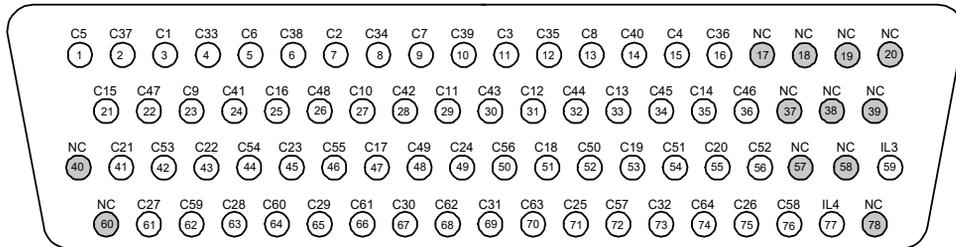
If you are going to create an expanded-row matrix using multiple modules and configuration blocks, plan your wiring placement for each configuration block carefully, to allow for a daisy-chain interconnection between configuration blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Configuration Blocks”](#) on page 261.

2 Base Matrix Configuration

34934C-002 D-Sub Connectors: Pin Assignments for 8x64 (base configuration)

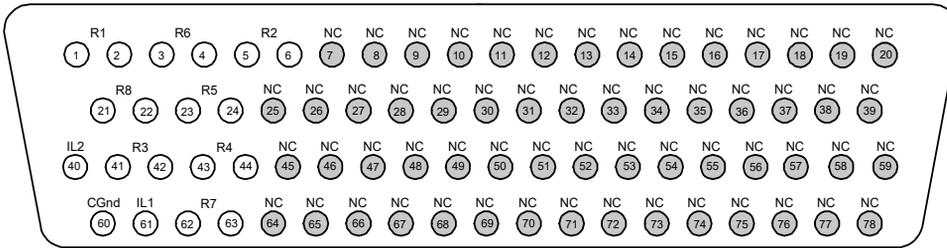


For orientation, the male D-sub connector end of the configuration block is facing you.



78-Pin D-Sub Male Connector
Bank 1 (P1)

Description	Pin										
IL3	59	C1	3	C17	47	C33	4	C49	48	No connect	17
IL4	77	C2	7	C18	51	C34	8	C50	52	No connect	18
		C3	11	C19	53	C35	12	C51	54	No connect	19
		C4	15	C20	55	C36	16	C52	56	No connect	20
		C5	1	C21	41	C37	2	C53	42	No connect	37
		C6	5	C22	43	C38	6	C54	44	No connect	38
		C7	9	C23	45	C39	10	C55	46	No connect	39
		C8	13	C24	49	C40	14	C56	50	No connect	40
		C9	23	C25	71	C41	24	C57	72	No connect	57
		C10	27	C26	75	C42	28	C58	76	No connect	58
		C11	29	C27	61	C43	30	C59	62	No connect	60
		C12	31	C28	63	C44	32	C60	64	No connect	78
		C13	33	C29	65	C45	34	C61	66		
		C14	35	C30	67	C46	36	C62	68		
		C15	21	C31	69	C47	22	C63	70		
		C16	25	C32	73	C48	26	C64	74		



78-Pin D-Sub Male Connector
Bank 2 (P2)

Description	Pin										
R1	1	No connect	7	No connect	25	No connect	45	No connect	64	IL1	61
R1	2	No connect	8	No connect	26	No connect	46	No connect	65	IL2	40
R2	5	No connect	9	No connect	27	No connect	47	No connect	66	Com Gnd	60
R2	6	No connect	10	No connect	28	No connect	48	No connect	67		
R3	41	No connect	11	No connect	29	No connect	49	No connect	68		
R3	42	No connect	12	No connect	30	No connect	50	No connect	69		
R4	43	No connect	13	No connect	31	No connect	51	No connect	70		
R4	44	No connect	14	No connect	32	No connect	52	No connect	71		
R5	23	No connect	15	No connect	33	No connect	53	No connect	72		
R5	24	No connect	16	No connect	34	No connect	54	No connect	73		
R6	3	No connect	17	No connect	35	No connect	55	No connect	74		
R6	4	No connect	18	No connect	36	No connect	56	No connect	75		
R7	62	No connect	19	No connect	37	No connect	57	No connect	76		
R7	63	No connect	20	No connect	38	No connect	58	No connect	77		
R8	21			No connect	39	No connect	59	No connect	78		
R8	22										

34934C-001 D-Sub Connectors: Wiring for 8x64

WARNING

For the 3434C-001, the D-Sub connector pin assignments are identical to the D-Sub connectors on the 34934A module. For the 34934C-002, the D-Sub connector pin assignments are not identical to the D-Sub connectors on the 34934A module. Pay careful attention to the wiring requirements found in the instructions when making D-Sub connections! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

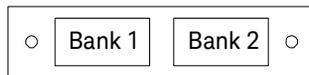
Safety Interlock Continuity While wiring to the 34934C-002's D-Sub connectors, you *must* make the following connections—in addition to the 8x64 row and column connections—to provide for safety interlock continuity:

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

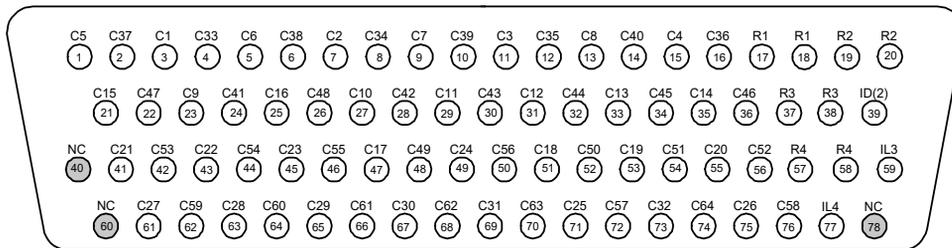
Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

Configuring an 8x64 Matrix by Direct Wiring

34934A module D-Sub Connectors: 8x64 Pin Assignments

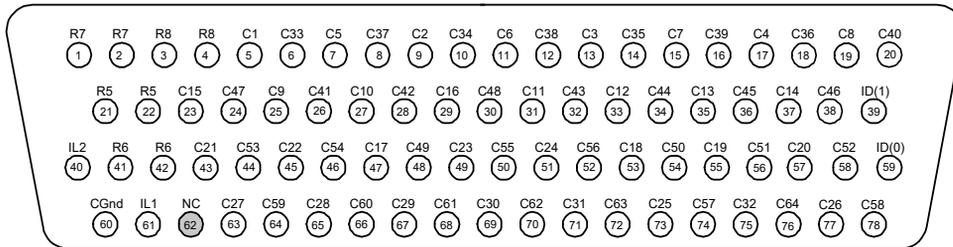


For orientation, the D-sub connector end of the module is facing you.



78-Pin D-Sub Male Connector
Bank 1

Description	Pin										
R1	17	C1	3	C17	47	C33	4	C49	48	No connect	40
R1	18	C2	7	C18	51	C34	8	C50	52	No connect	60
R2	19	C3	11	C19	53	C35	12	C51	54	No connect	78
R2	20	C4	15	C20	55	C36	16	C52	56		
R3	37	C5	1	C21	41	C37	2	C53	42		
R3	38	C6	5	C22	43	C38	6	C54	44		
R4	57	C7	9	C23	45	C39	10	C55	46		
R4	58	C8	13	C24	49	C40	14	C56	50		
		C9	23	C25	71	C41	24	C57	72		
ID2	39	C10	27	C26	75	C42	28	C58	76		
IL3	59	C11	29	C27	61	C43	30	C59	62		
IL4	77	C12	31	C28	63	C44	32	C60	64		
		C13	33	C29	65	C45	34	C61	66		
		C14	35	C30	67	C46	36	C62	68		
		C15	21	C31	69	C47	22	C63	70		
		C16	25	C32	73	C48	26	C64	74		



78-Pin D-Sub Male Connector
Bank 2

2 Base Matrix Configuration

Description	Pin										
R5	21	C1	5	C17	47	C33	6	C49	48	No connect	62
R5	22	C2	9	C18	53	C34	10	C50	54		
R6	41	C3	13	C19	55	C35	14	C51	56		
R6	42	C4	17	C20	57	C36	18	C52	58		
R7	1	C5	7	C21	43	C37	8	C53	44		
R7	2	C6	11	C22	45	C38	12	C54	46		
R8	3	C7	15	C23	49	C39	16	C55	50		
R8	4	C8	19	C24	51	C40	20	C56	52		
		C9	25	C25	73	C41	26	C57	74		
ID0	59	C10	27	C26	77	C42	28	C58	78		
ID1	39	C11	31	C27	63	C43	32	C59	64		
IL1	61	C12	33	C28	65	C44	34	C60	66		
IL2	40	C13	35	C29	67	C45	36	C61	68		
		C14	37	C30	69	C46	38	C62	70		
		C15	23	C31	71	C47	24	C63	72		
		C16	29	C32	75	C48	30	C64	76		

34934A Module D-Sub Connectors: 8x64 Wiring

When wiring directly to the 34934A's D-Sub connectors, you *must* make the following connections—in addition to row and column connections—to configure the 8x64 matrix:

Module Configuration: The 72 connections listed below are required to create the 8x64 matrix.

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2	To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2
C1	3	5	C33	4	6
C2	7	9	C34	8	10
C3	11	13	C35	12	14
C4	15	17	C36	16	18
C5	1	7	C37	2	8
C6	5	11	C38	6	12
C7	9	15	C39	10	16
C8	13	19	C40	14	20
C9	23	25	C41	24	26
C10	27	27	C42	28	28
C11	29	31	C43	30	32
C12	31	33	C44	32	34
C13	33	35	C45	34	36
C14	35	37	C46	36	38
C15	21	23	C47	22	24
C16	25	29	C48	26	30
C17	47	47	C49	48	48
C18	51	53	C50	52	54
C19	53	55	C51	54	56
C20	55	57	C52	56	58
C21	41	43	C53	42	44
C22	43	45	C54	44	46
C23	45	49	C55	46	50
C14	49	51	C56	50	52
C25	71	73	C57	72	74

2 Base Matrix Configuration

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2	To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2
C26	75	77	C58	76	78
C27	61	63	C59	62	64
C28	63	65	C60	64	66
C29	65	67	C61	66	68
C30	67	69	C62	68	70
C31	69	71	C63	70	72
C32	73	75	C64	74	76

To Create This Row #	Short This Pin on Bank 1...	...To This Pin on Bank 1	To Create This Row #	Short This Pin on Bank 2...	...To This Pin on Bank 2
R1	17	18	R5	21	22
R2	19	20	R6	41	42
R3	37	38	R7	1	2
R4	57	58	R8	3	4

Note that these connections require wires run *between* the two D-Sub connectors. You may make these connections near your test instrumentation or DUT, or near the D-Sub ends of your custom cabling.

Module ID: The open pins listed below allow the 34980A mainframe to recognize the 8x64 matrix configuration.

Ground: Pin 39 on Bank 1 (ID bit 2)
 Ground: Pin 39 on Bank 2 (ID bit 1)
 Ground: Pin 59 on Bank 2 (ID bit 0)

Safety Interlock Continuity: The connections listed below provide for safety interlock continuity.

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1
 Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

WARNING

For the 3434C-001, the D-Sub connector pin assignments are identical to the D-Sub connectors on the 34934A module. For the 34934C-002, the D-Sub connector pin assignments are not identical to the D-Sub connectors on the 34934A module. Pay careful attention to the wiring requirements found in the instructions when making D-Sub connections! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

NOTE

If you use the 34934C-002 Configuration Block—in conjunction with the 34934A module—to make wiring connections, the additional **Module Configuration** and **Module ID** connections are *not required*. Those connections are made within the hardware of the configuration block. You would need only make the **Safety Interlock Continuity** connections.

16x32 Matrix Configuration

To create a 16x32 matrix, you may connect your field wiring to the 34934A by three methods:

Terminal Block You can use terminal block 34934T-002. The terminal block connects to the 34934A's two D-Sub connectors; it provides 12 blocks of 12 screw terminals each, and a *16x32 removable overlay* (supplied with the 34934T-002) which provides guidance for field wiring the 16x32 matrices. Follow these sections in order to configure and wire the terminal block:

- “34934T-002 Terminal Block: 16x32 Layout” on page 219
- “34934T-002 Terminal Block: 16x32 Jumper Configuration” on page 222
- “34934T-002 Terminal Block: 16x32 Wiring” on page 225

Configuration Block You can use configuration block 34934C-001. Follow these sections in order to configure and wire the configuration block:

- “34934C-002 Configuration Block: Layout” on page 232
- “34934C-002 Configuration Block: 16x32 Jumper Configuration” on page 233
- “34934C-002 Configuration Block: Wiring” on page 239

WARNING

Do NOT use the 34934C-001 with the 34934T-001 terminal block for field wiring connections! This combination is not mechanically reliable and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Direct Wiring You can wire directly to the module's D-Sub connectors using custom cabling. You will have to short specific pins on and between the D-Sub connectors to create the 16x32 configuration. Review *both* these sections *before* wiring to the module's D-Subs:

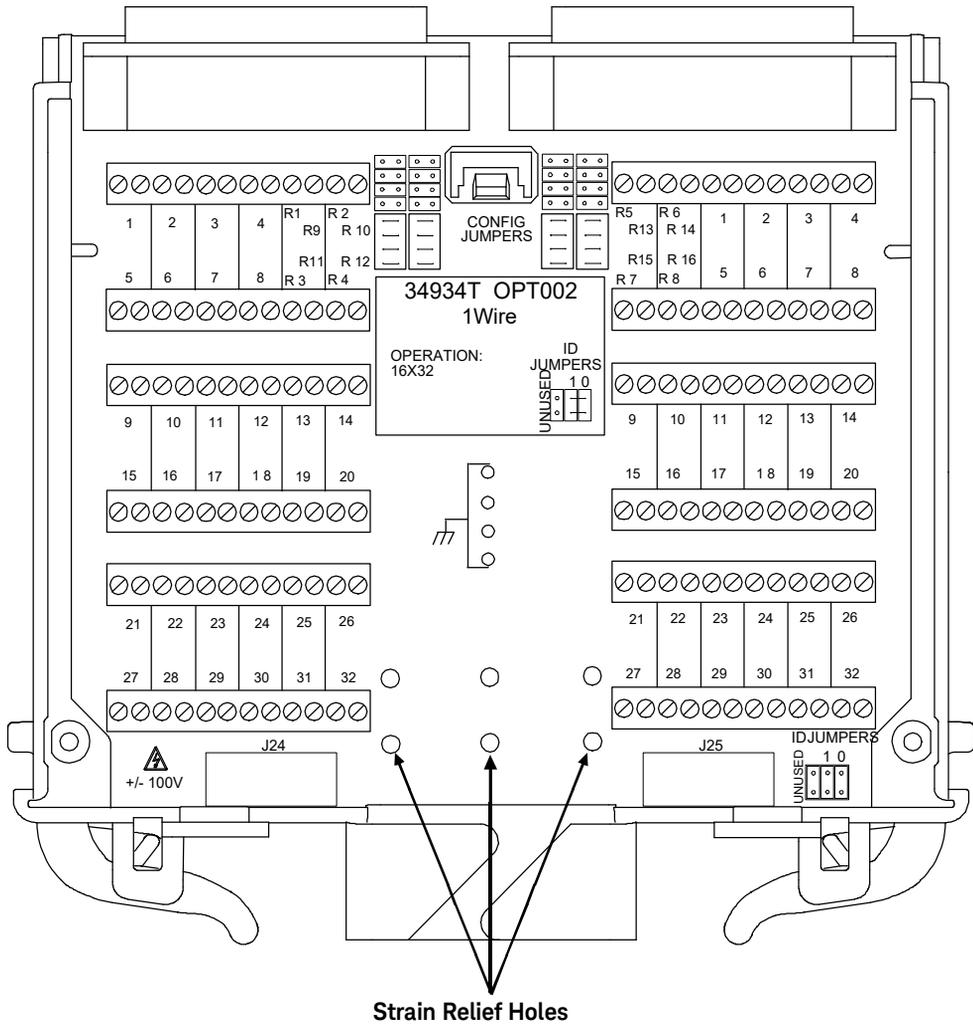
- “34934A D-Sub Connectors: 16x32 Pin Assignments” on page 245
- “34934A Module D-Sub Connectors: 16x32 Wiring” on page 247

Configuring a 16x32 Matrix using a Terminal Block

34934T-002 Terminal Block: 16x32 Layout

This terminal block with screw-type connections is labeled with the model number and the abbreviated module name. In addition, space is available on the label for you to write the slot number.

This terminal block's *16x32 removable overlay* shows the row and column terminals for the 16x32 matrix configuration:



Note the six strain relief holes provided in the terminal block PCB. See the following page for instructions on providing wiring strain relief.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

Terminal block strain relief

To reduce strain on the wiring at the terminals and maintain connection reliability, a strain relief feature has been incorporated into the terminal block design. Six holes have been placed near the lower edge of the circuit board, lined up with holes in the terminal block's bottom cover, as seen on "[34934T-002 Terminal Block: 16x32 Layout](#)" on page 219.

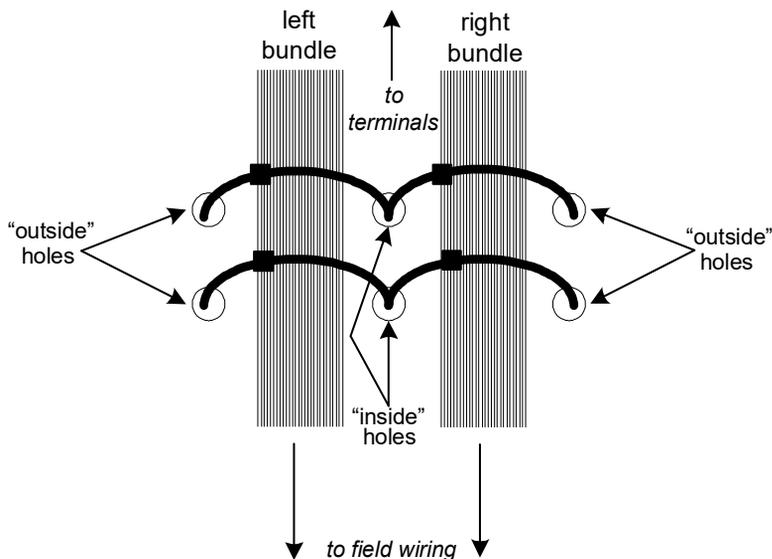
WARNING

Always include wiring strain relief to reduce strain on the connections. Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

To use this feature:

- 1 Once all jumpers are installed and wiring is connected to the terminals, arrange the wires leading away from the terminal block in two bundles. This is easiest if the left bundle leads to the terminals on the left side of the block, and conversely for the right side.
- 2 Insert a cable tie (tie-wrap, zip-tie) down through each of the four outside holes in the PCB, continuing down through the holes in the terminal block's bottom cover.
- 3 Route these cable ties up through the center holes in the bottom cover and PCB.
- 4 Close and tighten the cable ties as required, and remove any excess so that it does not interfere with cover closure.

The completed bundles are shown below.



34934T-002 Terminal Block: 16x32 Jumper Configuration

This terminal block has three types of jumpers which must be connected to configure the 16x32 matrix (matrices):

- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four 4x2* blocks of jumper pins located near the board's female D-Sub connectors, as shown.
- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins on the lower right corner of the board, as shown.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 or equivalent custom wiring on the two 20-pin extension headers at the bottom of the terminal block, as shown.

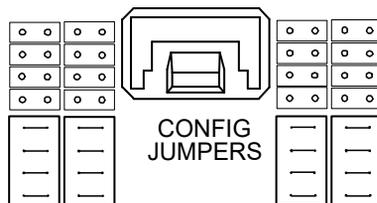
NOTE

In a high temperature–high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation $> 10 \text{ G}\Omega$ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

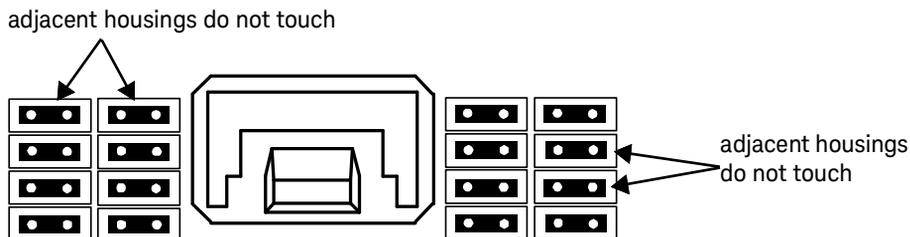
Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

Placing CONFIG Jumpers for 16x32 The CONFIG jumper area—shown below with jumpers removed—includes an illustration (on the *16x32 removable overlay*) that shows jumper placement for setting a 16x32 matrix configuration.



There are four 4x2 blocks of CONFIG JUMPER pins on either side of the board's center—you *must* set all four blocks identically.

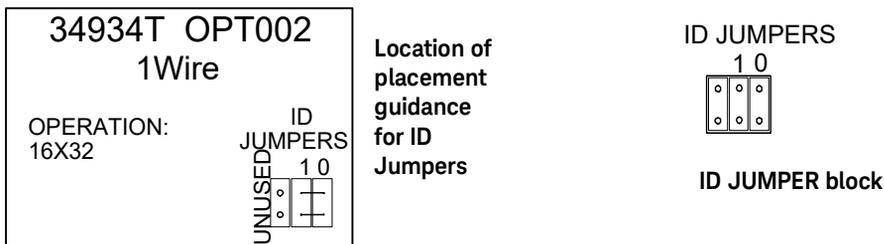
- 1 Place sixteen horizontally positioned jumpers as shown below.



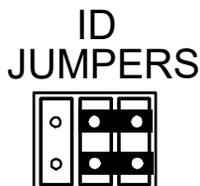
WARNING

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Placing ID Jumpers for 16x32 The ID jumpers allow the 34980A to recognize the 16x32 matrix configuration, upon mainframe bootup. Jumper placement guidance is provided (on the *16x32 removable overlay*) as shown below left; the ID jumper block is shown at right with jumpers removed.



To set the correct ID code for 16x32, place two horizontally positioned jumpers as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing Safety Interlock Continuity Jumpers for 16x32 At the bottom (end opposite the D-Sub connectors) of the terminal block are two 20-pin extension headers—marked J24 and J25 on the circuit board. These headers provide access to the module's rows. Pins 19 and 20 on each header must be shorted to provide continuity for the module's safety interlock function.

Two keyed 20-pin terminators 34934-80006 are supplied for providing safety interlock continuity. Install the terminators securely in the headers, as shown below.



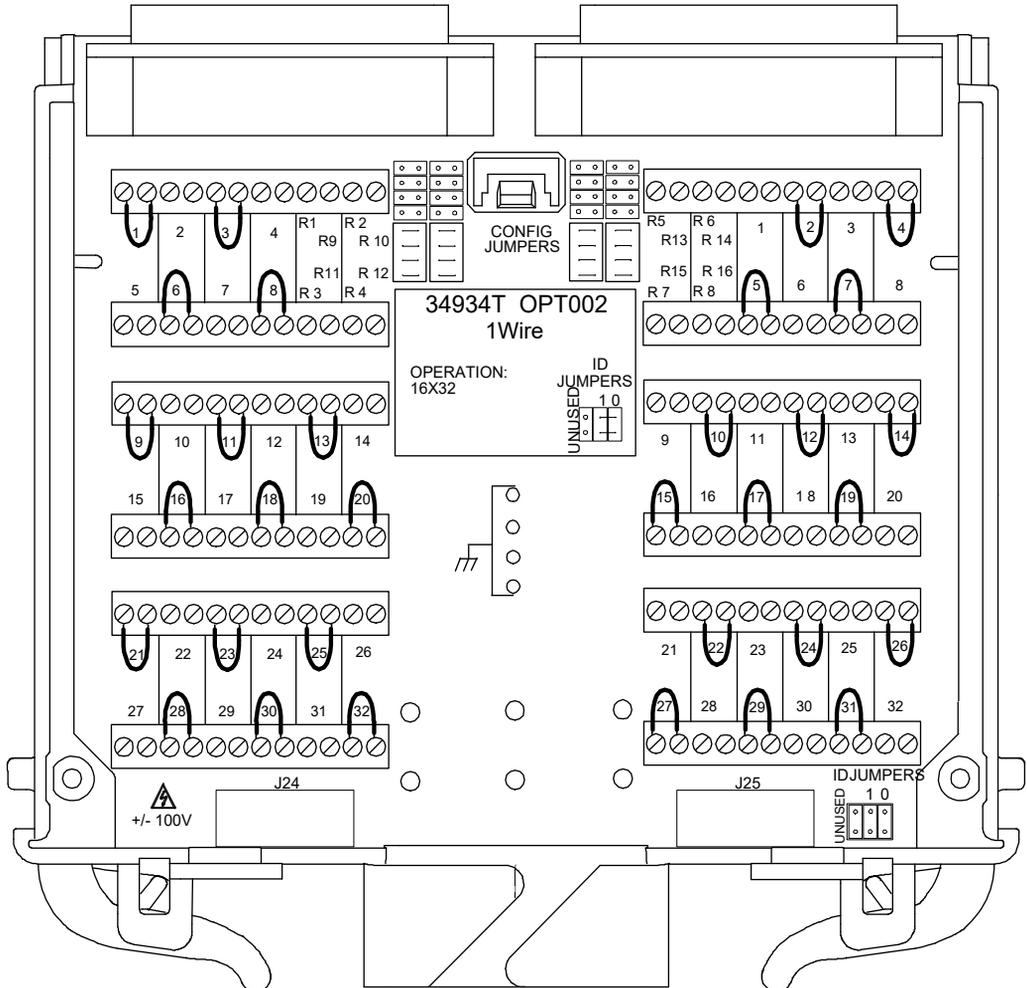
The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2. Detailed pin diagrams for these terminators are provided on [page 229](#).

34934T-002 Terminal Block: 16x32 Wiring

Once the required jumpers have been placed for 16x32, you are ready to complete 16x32 wiring configuration; then make row and column connections to the terminal block.

Terminal block wire jumpers

You *must* provide a jumper on the terminal block between the left and right screw terminals for each of the 32 columns. This can be done—channel by channel—on either side of the terminal block, but not on both sides for any channel. An example of jumper placement is shown below.



Once these jumpers are placed, you can make column connections to the column terminals which remain open.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

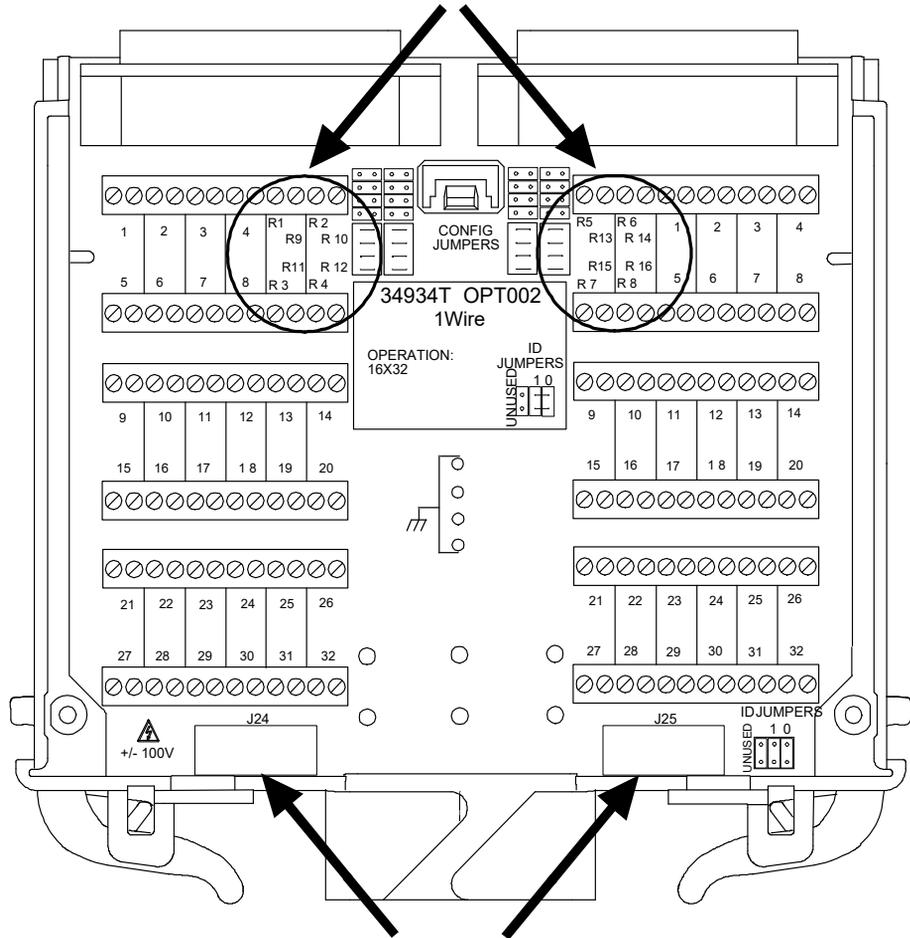
Row and Column Wiring

You may connect to the module's rows and columns by wiring to the screw terminals marked C1 through C32 and R1 through R16 (H, L) on the silk-screen (see ["34934T-002 Terminal Block: 16x32 Layout"](#) on page 219). The combined weight of these wires can create significant strain on the wiring; to minimize this see ["Terminal block strain relief"](#) on page 221.

However, you may prefer to use physically separate cabling for the row connections (e.g. if you connect your DUT to the columns and your measurement device(s) to the rows). The terminal block's extension headers J24 and J25 provide access to the rows; you can make row connections to the central 16 terminals (pins 3-18) on the supplied terminators you installed in ["Placing Safety Interlock Continuity Jumpers for 16x32"](#) on page 225.

Below is an illustration of the possible locations for making row signal connections to the 34934T-002:

You can connect row signals to the screw terminals here...



...or to the central 16 pins of extension headers J24 and J25 here.

WARNING

Terminal block wiring: Failure to follow the instructions below could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

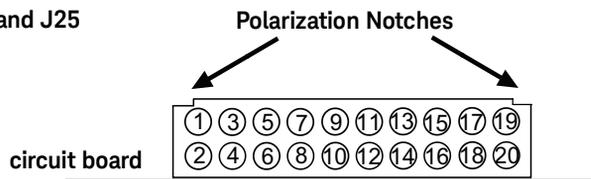
When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required.

Wiring of the terminal block must be performed by qualified persons. A MAXIMUM of 5 mm of conductor insulation is to be removed. All wire strands must be appropriately inserted in the connector housing. The screw connections must be sufficiently secured to prevent accidental loosening.

Never operate the instrument without the terminal block covers securely installed. Use caution to prevent operators from accessing any external circuits, test fixtures, cables or whenever hazardous voltages may be present.

The following subsection outlines how to make row connections to extension headers J24 or J25.

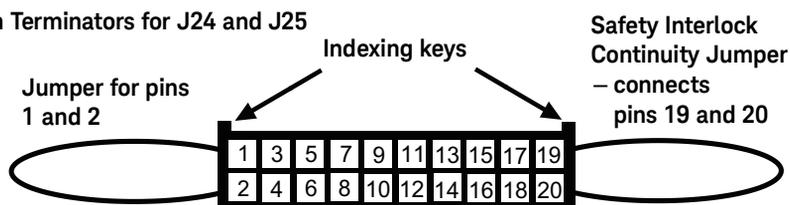
Extension Header (Row Signal) Wiring for 16x32 The position of extension headers J24 and J25 are marked on the 34934T-002's silk-screen. Oriented with the terminal side of the board up, the pin numbers for these headers are shown below.

Extension Headers J24 and J25

The supplied 20-pin terminators 34934-80006 are identical, but the pin assignments for 16x32 are not (rows 1-8 are on J24; rows 9-16 are on J25). The terminal numbers—corresponding to the headers—are shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934T-002: Extension Header Pin Assignments for 16x32

Header J24				Header J25			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No Connect	1	No Connect	2	No Connect	1	No Connect	2
R1	3	R1	4	R5	3	R5	4
R2	7	R2	8	R6	7	R6	8
R3	11	R3	12	R7	11	R7	12
R4	15	R4	16	R8	15	R8	16
R9	5	R9	6	R13	5	R13	6
R10	9	R10	10	R14	9	R14	10
R11	13	R11	14	R15	13	R15	14
R12	17	R12	18	R16	17	R16	18
IL1	19	IL2	20	IL3	19	IL4	20

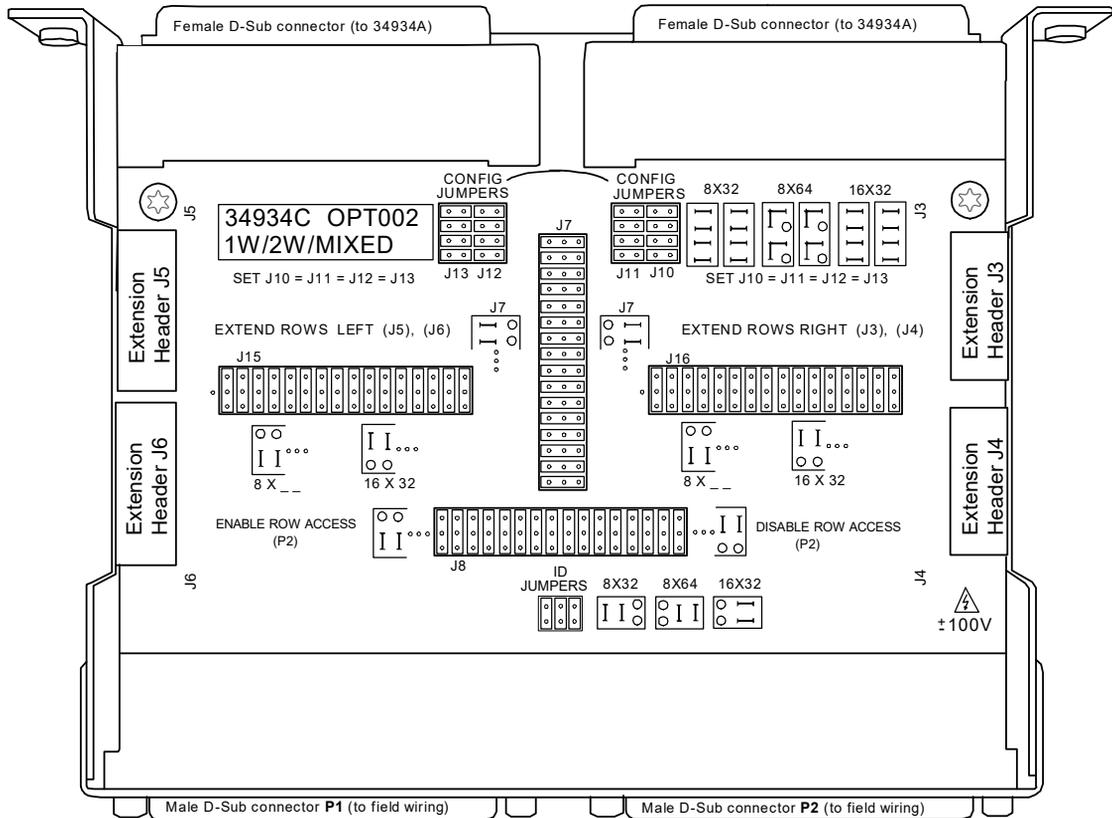
Note that each row signal (R1 through R16) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going to create an expanded-row matrix using multiple modules and terminal blocks, plan your wiring placement for each terminal block carefully, to allow for a daisy-chain interconnection between terminal blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Terminal Blocks”](#) on page 257.

Configuring a 16x32 Matrix using a Configuration Block

34934C-002 Configuration Block: Layout

This configuration block is labeled with the model number and the abbreviated module name.



Note five types of on-board jumpers you'll have to set to configure this board; some will be familiar from the configuration descriptions of the *terminal blocks*, but some are new. Instructions for setting these are provided in ["34934C-002 Configuration Block: 16x32 Jumper Configuration"](#) on page 233.

- ID Jumpers
- CONFIG Jumpers (J10, J11, J12 and J13)

- Row Extension Jumpers (J7)
- Row Access Jumpers (J8)
- Row Setting Jumpers (J15 and J16)

WARNING

Do not install substitute parts or perform any unauthorized modifications to the instrument to ensure the safety features are maintained in operational condition. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

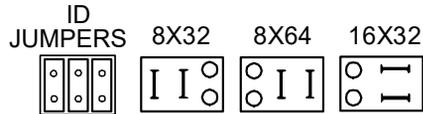
Also note the four extension headers (J3, J4, J5 and J6). Instructions for wiring to these are provided in [“34934C-002 Configuration Block: Wiring”](#) on page 239.

34934C-002 Configuration Block: 16x32 Jumper Configuration

The configuration block has five types of jumpers which must be connected to configure it for use in an expanded 4-row matrix. In this section, you will place:

- **ID Jumpers:** place these 2-pin jumpers on a 2x3 block of jumper pins near the bottom center of the board. See [“Placing ID Jumpers on the 34934C-002 Configuration Block for 16x32”](#) on page 234.
- **CONFIG Jumpers:** place these 2-pin jumpers *identically on all four* 4x2 blocks of jumper pins located near the board’s female D-Sub connectors. See [“Placing CONFIG Jumpers on the 34934C-002 Configuration Block for 16x32”](#) on page 234.
- **Row Extension Jumpers:** place these 16-pin jumpers on a 16x3 block (J7) of jumper pins at the board’s center. See [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 235.
- **Row Access Jumpers:** place these 16-pin jumpers on a 3x16 block of jumper pins located just above the ID Jumpers. See [“Placing Row Access Jumpers on the 34934C-002 Configuration Block”](#) on page 237.
- **Safety Interlock Continuity Jumpers:** place the supplied pre-wired terminators 34934-80006 on the four 20-pin extension headers at the sides of the configuration block. See [“Placing Safety Interlock Continuity Jumpers on the 34934C-002 Configuration Block”](#) on page 238.

Placing ID Jumpers on the 34934C-002 Configuration Block for 16x32 The ID jumpers allow the 34980A to recognize the base matrix configuration used by the 34934C-002, upon mainframe bootup. The ID jumpers are placed in a 2x3 jumper block, located below jumper block J8. Jumper placement guidance is provided on the configuration block's silk-screen, to the right of the jumpers:



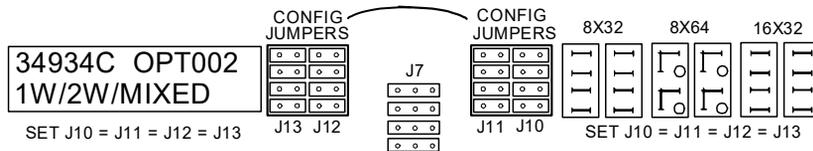
To set the correct ID code for 16x32, place two horizontally positioned jumpers as shown below.



CAUTION

The ID jumpers **MUST** correspond to the matrix configuration set by the CONFIG jumpers in the previous step. The instrument firmware functions based on the ID it detects from the (ID) jumper positions and assumes the CONFIG jumpers are set accordingly.

Placing CONFIG Jumpers on the 34934C-002 Configuration Block for 16x32 The CONFIG jumper area—shown below with jumpers removed— includes an illustration to its right that shows jumper placement for setting the 8x32, 8x64, and 16x32 base matrix configurations.



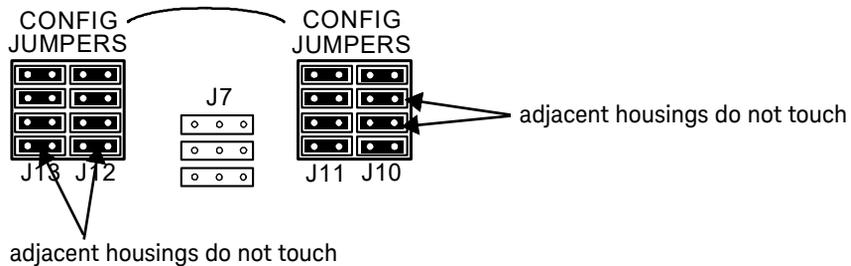
NOTE

In a high temperature-high humidity environment, salt and oil contaminants transferred from the fingertips to the moveable CONFIG jumpers can affect the 34934A's published DC Isolation specification if adjacent jumpers are in physical contact with each other. In applications where DC (channel to channel) isolation $> 10 \text{ G}\Omega$ is required, use clean cotton gloves when handling the CONFIG jumpers. If cotton gloves are not available, clean the plastic jumper surfaces using a cotton cloth and 91% isopropyl alcohol after handling.

WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

To set the CONFIG jumpers for 16x32, place 16 horizontally positioned jumpers as shown below.

**WARNING**

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Placing Row Extension Jumpers on the 34934C-002 Configuration Block

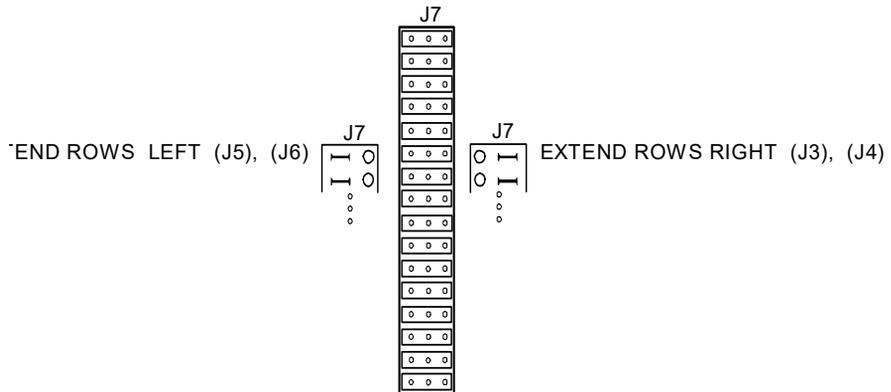
The jumpers in 16x3 jumper block J7 (at board center) determine if row connections can be made from the left or right side of the board (i.e. they “extend” the row signal connection points out to *only one* of the two pairs of blue 20-pin connectors at the sides of the configuration block).

2 Base Matrix Configuration

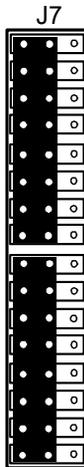
Note that “row connections” at minimum include the connections between modules in the matrix, but may also include your access from field wiring to the matrix rows.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling, depending on slot placement of the individual 34934A modules and their corresponding 34934C-002 blocks.

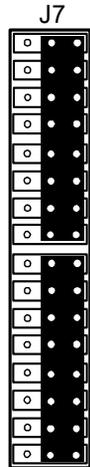
The following illustration shows the jumper block J7, along with the relevant guidance provided on the board’s silk-screen for jumper placement:



If you plan to extend rows *left*, to J5 and J6, place two 2x8 jumpers as shown at right.



If you plan to extend rows *right*, to J3 and J4, place two 2x8 jumpers as shown at right.



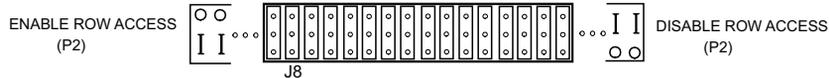
Placing Row Access Jumpers on the 34934C-002 Configuration Block

The jumpers in 3x16 jumper block J8 (at lower board center) determine if row connections can be made from the male D-Sub connector P2 (i.e. they “enable” the row signal connection points at this D-sub).

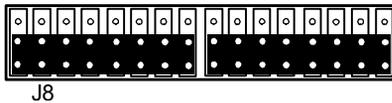
These “row connections” are the traditional access points from field wiring to the matrix rows. In practice, your arrangement of test instrumentation and DUT test points will determine if you wish to make row connections at the D-sub, at the blue 20-pin extension connectors, or both.

Setting these jumpers provides you the flexibility to plan how to route your custom cabling.

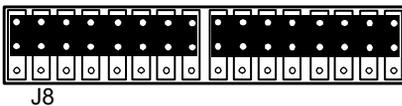
The following illustration shows the jumper block J8, along with the relevant guidance provided on the board’s silk-screen for jumper placement.



If you plan to connect row signals to D-sub connector P2, place two 2x8 jumpers on block J8, as shown below.



If you do not need to connect row signals to D-sub connector P2, place two 2x8 jumpers on block J8, as shown below.

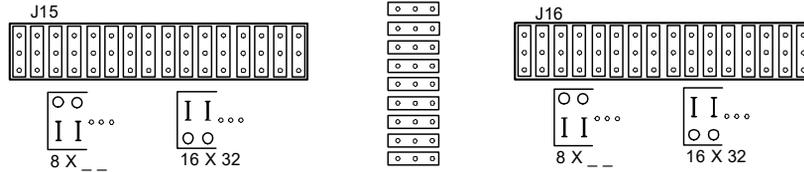


Note that by not routing signals to the D-sub connectors, module bandwidth may be preserved and capacitive loading minimized.

Placing Row Setting Jumpers on the 34934C-002 Configuration Block

The jumpers in 3x16 jumper blocks J 15 and J16 (left and right of lower board center) set the row depth (i.e. 8-rows or 16-rows) for the expanded matrix. In conjunction with the CONFIG jumpers, these jumpers set the matrix configuration.

The following illustration shows jumper blocks J15 and J16, along with the relevant guidance provided on the board's silk-screen for jumper placement.



For a base configuration of 16x32, place two 2x8 jumpers *on each of* blocks J15 and J16, as shown below.



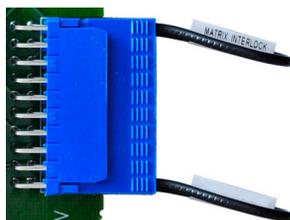
Placing Safety Interlock Continuity Jumpers on the 34934C-002

Configuration Block At each side of the configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. In all cases, pins 19 and 20 are the safety interlock pins.

These headers provide for row extension and row signal access, depending on the placement of the jumpers in J7. You must have set the jumpers to extend rows either to the left **or** right side of the configuration block (see [“Placing Row Extension Jumpers on the 34934C-002 Configuration Block”](#) on page 235).

To provide continuity for the module's safety interlock function, pins 19 and 20 on *both* extension headers on the selected side (left or right) must be shorted.

- For each of these headers, install one of the supplied keyed 20-pin terminators 34934-80006 securely in that header, as shown below.



The supplied terminators incorporate both the shorting jumper for pins 19 and 20 and a jumper precluding connection to pins 1 and 2.

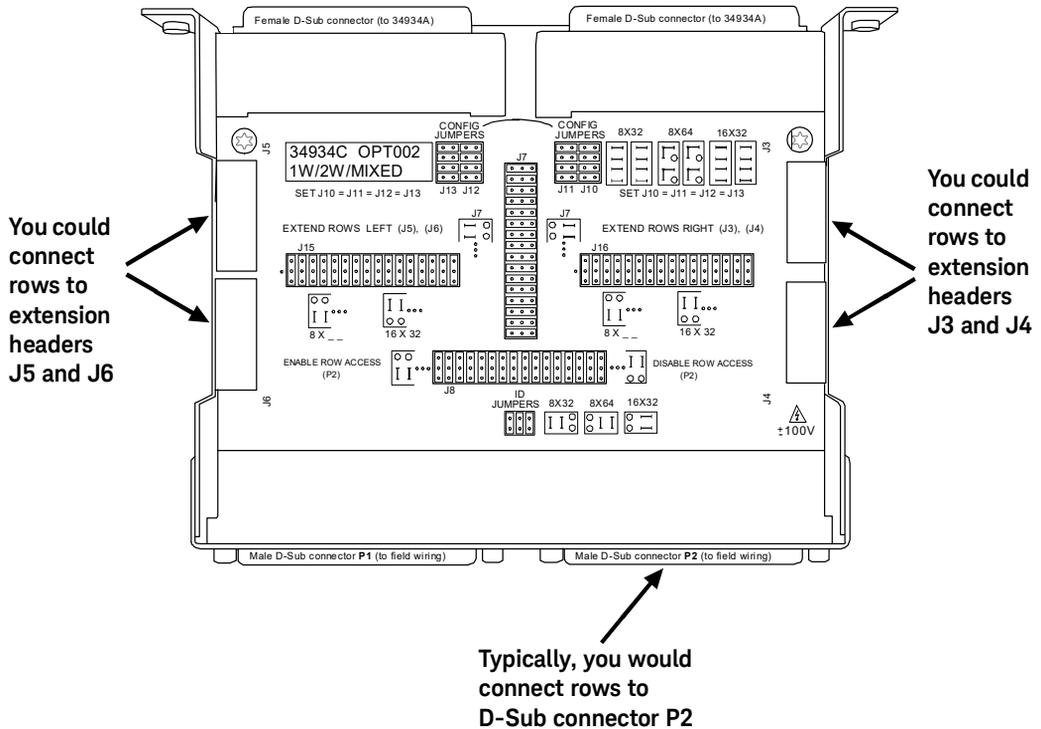
- The extension headers on the opposite (unused) side of the 34934C-002 do not require a safety interlock terminator; these will be hidden by the configuration block's cover.

34934C-002 Configuration Block: Wiring

The next two subsections outline options for making row and column wiring connections to the configuration block.

Extension Header (Row Signal) Wiring to the 34934C-002

Below is an illustration of the possible locations for making row signal connections to the 34934T-002.



Typically, you could make both row and column connections to each module by fabricating custom cabling terminating in female D-Sub connectors. These attach to the configuration block's male D-Sub connectors P1 and P2. For 16x32, all rows connect to P2.

Alternately, you may choose to connect only columns (typically test points on your DUT) to the D-Subs. There may be physical or electrical advantages in making your row connections (typically measurement devices) to the configuration block's blue extension connectors J3, J4, J5 and J6. To provide this flexibility, two extension headers are provided on each side of the configuration block.

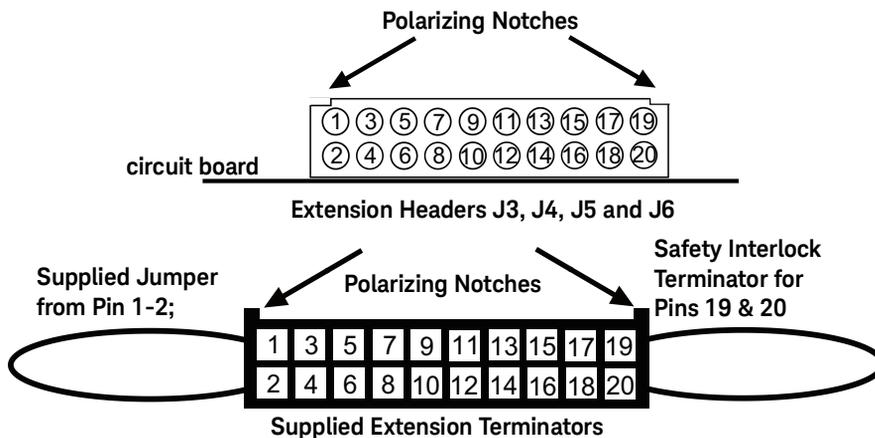
You must have set the jumpers (in "Placing Row Extension Jumpers on the 34934C-002 Configuration Block" on page 235) to extend rows to two of the four headers. We'll call these the "live" headers.

In "Placing Safety Interlock Continuity Jumpers on the 34934C-002 Configuration Block" on page 238, you will have installed the supplied blue terminators 34934-80006 in the live headers on the configuration block.

You can now make row connections to these headers, by utilizing the central 16 pins (3-18) on the snap-in terminators. The extension header's supplied terminator is shown below.

WARNING

Pay careful attention to the indexing keys on the connectors, to correctly identify terminal #1! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.



You must fabricate custom cabling to make row signal connections to the central 16 pins (3-18) of these headers, using the pin assignments below. The supplied blue terminators 34934-80006 accommodate 22 AWG coated wire, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

34934C-002: Extension Header Pin Assignments for 16x32

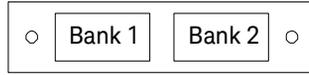
Headers J4, J5				Headers J3, J6			
Description	Pin	Description	Pin	Description	Pin	Description	Pin
No connect	1	No connect	2	No connect	1	No connect	2
R1	3	R1	4	R5	3	R5	4
R9	5	R9	6	R13	5	R13	6
R2	7	R2	8	R6	7	R6	8
R10	9	R10	10	R14	9	R14	10
R3	11	R3	12	R7	11	R7	12
R11	13	R11	14	R15	13	R15	14
R4	15	R4	16	R8	15	R8	16
R12	17	R12	18	R16	17	R16	18
IL1	19	IL2	20	IL3	19	IL4	20

2 Base Matrix Configuration

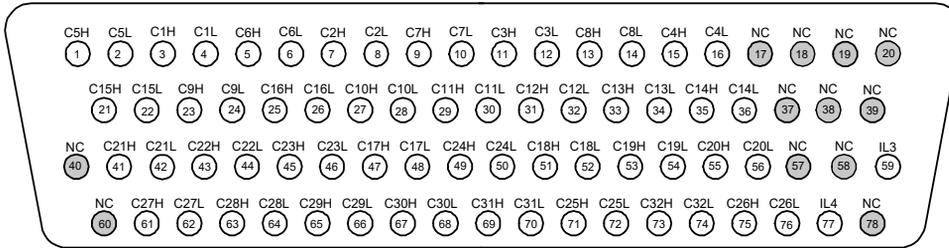
Note that each row signal (R1 through R16) is routed out on two pins on the extension headers. You can make row signal connections to either row (upper, lower) of the connector.

If you are going to create an expanded-row matrix using multiple modules and configuration blocks, plan your wiring placement for each configuration block carefully, to allow for a daisy-chain interconnection between configuration blocks—in addition to any row wiring you may connect to the expansion headers. This interconnection is illustrated in [“Arranging Multiple 34934A Modules with Configuration Blocks”](#) on page 261.

34934C-002 D-Sub Connectors: Pin Assignments for 16x32 (base configuration)



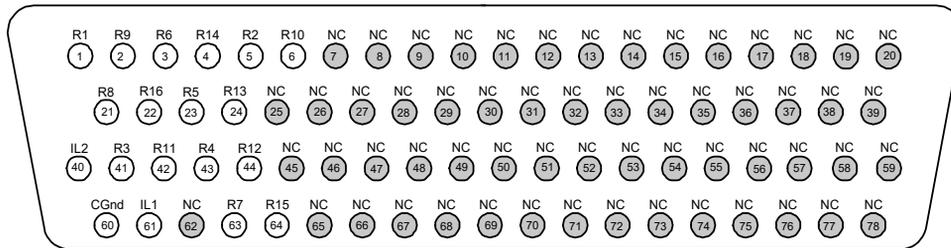
For orientation, the male D-sub connector end of the configuration block is facing you.



78-Pin D-Sub Male Connector
Bank 1 (P1)

Description	Pin										
IL3	59	C1	3	C9	23	C17	47	C25	71	No connect	17
IL4	77	C1	4	C9L	24	C17	48	C25	72	No connect	18
		C2	7	C10	27	C18	51	C26	75	No connect	19
		C2	8	C10	28	C18	52	C26	76	No connect	20
		C3	11	C11	29	C19	53	C27	61	No connect	37
		C3	12	C11	30	C19	54	C27	62	No connect	38
		C4	15	C12	31	C20	55	C28	63	No connect	39
		C4	16	C12	32	C20	56	C28	64	No connect	40
		C5	1	C13	33	C21	41	C29	65	No connect	57
		C5	2	C13	34	C21	42	C29	66	No connect	58
		C6	5	C14	35	C22	43	C30	67	No connect	60
		C6	6	C14	36	C22	44	C30	68	No connect	78
		C7	9	C15	21	C23	45	C31	69		
		C7	10	C15	22	C23	46	C31	70		
		C8	13	C16	25	C24	49	C32	73		
		C8	14	C16	26	C24	50	C32	74		

2 Base Matrix Configuration



78-Pin D-Sub
Male Connector
Bank 2 (P2)

Description	Pin										
R1	1	No connect	7	No connect	25	No connect	45	No connect	62	IL1	61
R2	5	No connect	8	No connect	26	No connect	46	No connect	65	IL2	40
R3	41	No connect	9	No connect	27	No connect	47	No connect	66	Com Gnd	60
R4	43	No connect	10	No connect	28	No connect	48	No connect	67		
R5	23	No connect	11	No connect	29	No connect	49	No connect	68		
R6	3	No connect	12	No connect	30	No connect	50	No connect	69		
R7	63	No connect	13	No connect	31	No connect	51	No connect	70		
R8	21	No connect	14	No connect	32	No connect	52	No connect	71		
R9	2	No connect	15	No connect	33	No connect	53	No connect	72		
R10	6	No connect	16	No connect	34	No connect	54	No connect	73		
R11	42	No connect	17	No connect	35	No connect	55	No connect	74		
R12	44	No connect	18	No connect	36	No connect	56	No connect	75		
R13	24	No connect	19	No connect	37	No connect	57	No connect	76		
R14	4	No connect	20	No connect	38	No connect	58	No connect	77		
R15	64			No connect	39	No connect	59	No connect	78		
R16	22										

34934C-001 D-Sub Connectors: Wiring for 16x32

WARNING

For the 3434C-001, the D-Sub connector pin assignments are identical to the D-Sub connectors on the 34934A module. For the 34934C-002, the D-Sub connector pin assignments are not identical to the D-Sub connectors on the 34934A module. Pay careful attention to the wiring requirements found in the instructions when making D-Sub connections! Failure to follow the pin assignments could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

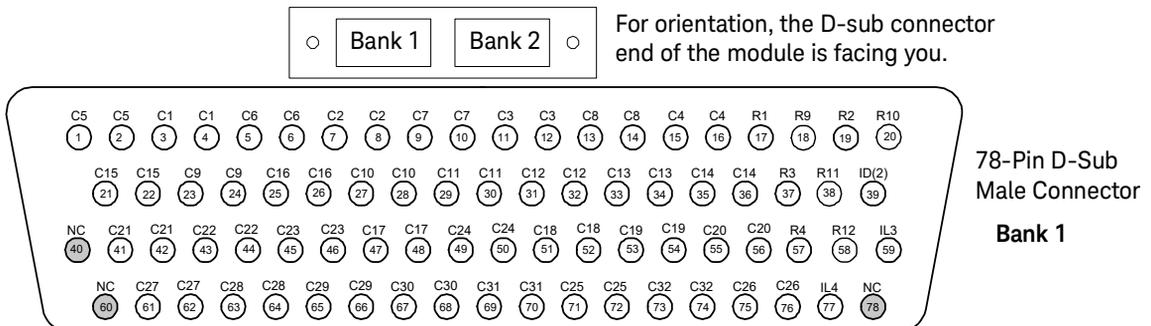
Safety Interlock Continuity While wiring to the 34934C-002's D-Sub connectors, you *must* make the following connections—in addition to the 16x32 row and column connections—to provide for safety interlock continuity:

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1

Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

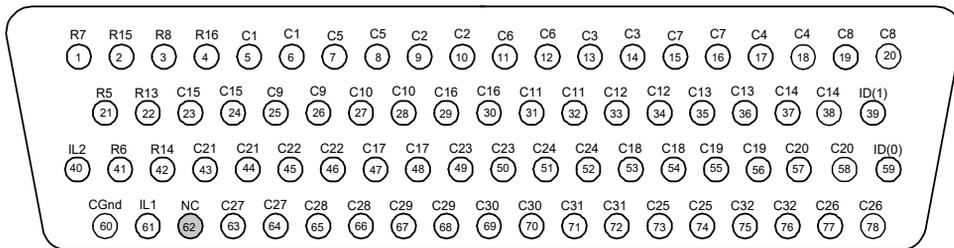
Configuring a 16x32 Matrix by Direct Wiring

34934A D-Sub Connectors: 16x32 Pin Assignments



2 Base Matrix Configuration

Description	Pin										
R1	17	C1	3	C9	23	C17	47	C25	71	No connect	40
R2	19	C1	4	C9	24	C17	48	C25	72	No connect	60
R3	37	C2	7	C10	27	C18	51	C26	75	No connect	78
R4	57	C2	8	C10	28	C18	52	C26	76		
		C3	11	C11	29	C19	53	C27	61	ID2	39
R9	18	C3	12	C11	30	C19	54	C27	62	IL3	59
R10	20	C4	15	C12	31	C20	55	C28	63	IL4	77
R11	38	C4	16	C12	32	C20	56	C28	64		
R12	58	C5	1	C13	33	C21	41	C29	65		
		C5	2	C13	34	C21	42	C29	66		
		C6	5	C14	35	C22	43	C30	67		
		C6	6	C14	36	C22	44	C30	68		
		C7	9	C15	21	C23	45	C31	69		
		C7	10	C15	22	C23	46	C31	70		
		C8	13	C16	25	C24	49	C32	73		
		C8	14	C16	26	C24	50	C32	74		



**78-Pin D-Sub
Male Connector
Bank 2**

Description	Pin										
R5	21	C1	5	C9	25	C17	47	C25	73	No connect	62
R6	41	C1	6	C9	26	C17	48	C25	74		
R7	1	C2	9	C10	27	C18	53	C26	77	ID(0)	59
R8	3	C2	10	C10	28	C18	54	C26	78	ID(1)	39
		C3	13	C11	31	C19	55	C27	63	IL1	61
R13	22	C3	14	C11	32	C19	56	C27	64	IL2	40
R14	42	C4	17	C12	33	C20	57	C28	65		
R15	2	C4	18	C12	34	C20	58	C28	66		
R16	4	C5	7	C13	35	C21	43	C29	67		
		C5	8	C13	36	C21	44	C29	68		
		C6	11	C14	37	C22	45	C30	69		
		C6	12	C14	38	C22	46	C30	70		
		C7	15	C15	23	C23	49	C31	71		
		C7	16	C15	24	C23	50	C31	72		
		C8	19	C16	29	C24	51	C32	75		
		C8	20	C16	30	C24	52	C32	76		

34934A Module D-Sub Connectors: 16x32 Wiring

When wiring directly to the 34934A's D-Sub connectors, you *must* make the following connections—in addition to row and column connections—to configure the 16x32 matrix:

Module Configuration: The 96 connections listed in the two tables below (64 here; 32 in the table on the following page) are required to create the 16x32 matrix.

In the first table, note the **AND** indicating that you must short all connections shown. In the second table, note the **OR** indicating that for each line in the table, you must short one set of pins or the other, but not both.

2 Base Matrix Configuration

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2	AND...	Short This Pin on Bank 1...	...To This Pin on Bank 2
C1	3	5		4	6
C2	7	9		8	10
C3	11	13		12	14
C4	15	17		16	18
C5	1	7		2	8
C6	5	11		6	12
C7	9	15		10	16
C8	13	19		14	20
C9	23	25		24	26
C10	27	27		28	28
C11	29	31		30	32
C12	31	33		32	34
C13	33	35		34	36
C14	35	37		36	38
C15	21	23		22	24
C16	25	29		26	30
C17	47	47		48	48
C18	51	53		52	54
C19	53	55		54	56
C20	55	57		56	58
C21	41	43		42	44
C22	43	45		44	46
C23	45	49		46	50
C14	49	51		50	52
C25	71	73		72	74

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 2	AND...	Short This Pin on Bank 1...	...To This Pin on Bank 2
C26	75	77		76	78
C27	61	63		62	64
C28	63	65		64	66
C29	65	67		66	68
C30	67	69		68	70
C31	69	71		70	72
C32	73	75		74	76

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 1	OR...	Short This Pin on Bank 2...	...To This Pin on Bank 2
C1	3	4		5	6
C2	7	8		9	10
C3	11	12		13	14
C4	15	16		17	18
C5	1	2		7	8
C6	5	6		11	12
C7	9	10		15	16
C8	13	14		19	20
C9	23	24		25	26
C10	27	28		27	28
C11	29	30		31	32
C12	31	32		33	34
C13	33	34		35	36
C14	35	36		37	38
C15	21	22		23	24
C16	25	26		29	30

2 Base Matrix Configuration

To Create This Column #	Short This Pin on Bank 1...	...To This Pin on Bank 1	OR...	Short This Pin on Bank 2...	...To This Pin on Bank 2
C17	47	48		47	48
C18	51	52		53	54
C19	53	54		55	56
C20	55	56		57	58
C21	41	42		43	44
C22	43	44		45	46
C23	45	46		49	50
C24	49	50		51	52
C25	71	72		73	74
C26	75	76		77	78
C27	61	62		63	64
C28	63	64		65	66
C29	65	66		67	68
C30	67	68		69	70
C31	69	70		71	72
C32	73	74		75	76

Module ID: The open pins listed below allow the 34980A mainframe to recognize the 16x32 matrix configuration.

Ground: Pin 39 on Bank 1 (ID bit 2)
 Leave Open: Pin 39 on Bank 2 (ID bit 1)
 Leave Open: Pin 59 on Bank 2 (ID bit 0)

Safety Interlock Continuity: The connections listed below provide for safety interlock continuity.

Short: Pin 59 (IL3) to Pin 77 (IL4) on Bank 1
 Short: Pin 40 (IL2) to Pin 61 (IL1) on Bank 2

NOTE

If you use the 34934C-002 Configuration Block—in conjunction with the 34934A module—to make wiring connections, the additional **Module Configuration** and **Module ID** connections are *not required*. Those connections are made within the hardware of the configuration block. You would need only make the **Safety Interlock Continuity** connections.

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3 Expanded-Row Matrix Configuration

- Linking Multiple 34934A Matrix Modules 254
- Physical Arrangement of Multiple 34934A Modules in a 34980A Mainframe 256
- Creating Expanded-Row Matrices Using Terminal Blocks 265
- Creating Expanded-Row Matrices Using Configuration Blocks 268

This chapter provides complete instructions for you to combine multiple *configured* 34934A modules to create large matrices.

WARNING

It is possible to connect more than one power source at the same time with a matrix. Ensure that dangerous or unwanted conditions are not created by these connections. Failure to recognize or observe normal safety precautions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

Linking Multiple 34934A Matrix Modules

You can combine up to eight modules to design your own large matrices. From a programming standpoint, each matrix module operates as an independent module regardless of the external connections. When linking modules, the channel numbering scheme for each module remains the same as for single modules.

The maximum row width (in columns) you can create with a single 34934A module is 128 for four rows, 64 for eight rows or 32 for 16 rows. Using multiple modules in the same 34980A mainframe—electrically linked using external cabling—you can create the following *expanded-row matrix* configurations:

Base Config	Number of Linked 34934A Modules Used in an Expanded-row Matrix						
	2	3	4	5	6	7	8
4x128	4x256	4x384	4x512	4x640	4x768	4x896	4x1024
8x64	8x128	8x192	8x256	8x320	8x384	8x448	8x512
16x32	16x64	16x96	16x128	16x160	16x192	16x224	16x256

Both the terminal blocks and configuration blocks provide a convenient means to link multiple 34934A modules, so that the resulting expanded-row structure can be treated as a single matrix.

Four-Row Matrices: Order a terminal block 34934T-001 (Option 001) or configuration block 34934C-001 (Option 001) for each 34934A module used in an expanded row matrix whose base configuration is *four rows*.

Eight-Row or Sixteen-Row Matrices: Order a terminal block 34934T-002 (Option 002) or configuration block 34934C-002 (Option 002) for each 34934A module used in an expanded row matrix whose base configuration is *eight or sixteen rows*.

Each terminal block or configuration block must be configured and wired individually, as if a stand-alone base matrix; complete instructions are in [Chapter 2, “Base Matrix Configuration”](#). *All blocks in an expanded row matrix must have their ID and CONFIG jumpers set identically.*

Once individually configured, you will create the large matrix (expand the rows) by linking the expansion headers on each block in a daisy-chain. You may then make row connections to that matrix at any open row on the appropriate header(s).

Physical Arrangement of Multiple 34934A Modules in a 34980A Mainframe

WARNING

It is possible to connect more than one power source at the same time with a matrix. Ensure that dangerous or unwanted conditions are not created by these connections. Failure to recognize or observe normal safety precautions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

You can employ anywhere from two to eight modules in a single 34980A mainframe to create an expanded matrix. This will depend on slot availability and your needs.

If using Terminal Blocks The terminal blocks must be linked (“daisy-chained” in the case of more than two modules) with module interconnections extending from the expansion headers at the rear of the terminal blocks.

If using Configuration Blocks The configuration blocks must be linked (“daisy-chained” in the case of more than two modules) with module interconnections extending from the expansion headers at the sides of the configuration blocks.

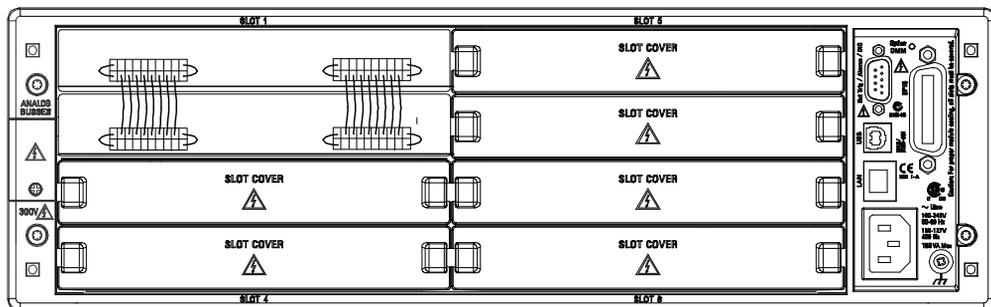
For flexibility, you can make these connections from either the right or left side of each configuration block.

In either case Plan carefully to provide adequate physical room to make all needed connections and allow for insertion/removal of other modules.

Some terminal blocks for other 34980A modules are shorter than those for the 34934A; if placed side-by-side in the mainframe, the 34934A may preclude independent removal of the shorter module. Therefore, if you need to attach or remove a shorter terminal block—adjacent to a 34934A with attached 34934T—the 34934T will need to be removed first.

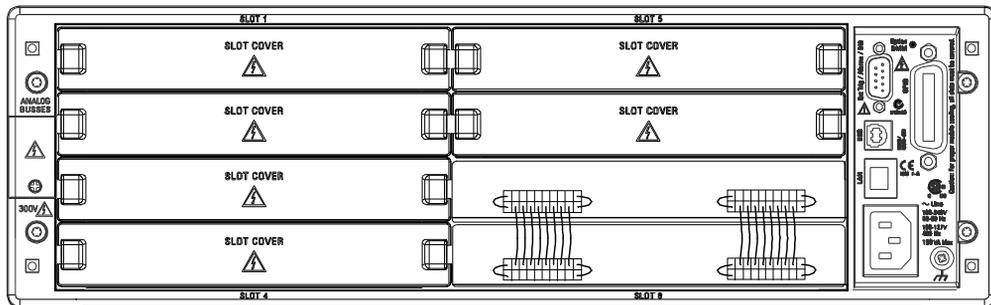
Arranging Multiple 34934A Modules with Terminal Blocks

The illustrations below show just a few of the possible physical installations of multiple 34934A-34934T pairs in a 34980A mainframe.



Two 34934A modules (with attached terminal blocks) connected in an extended-row configuration.

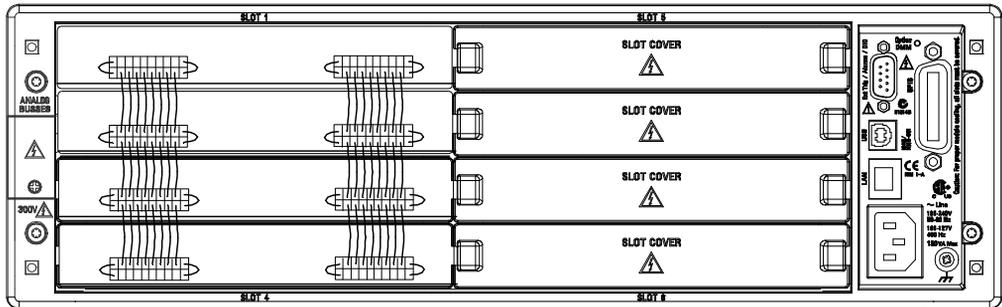
Example: base configuration for each is 4x128; resulting expanded-row matrix is 4x256.



Alternate method of connecting two 34934A modules.

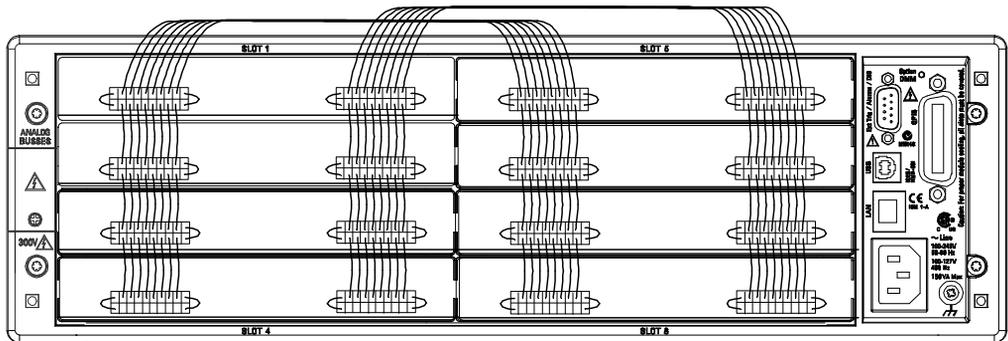
Example: base configuration for each is 8x64; resulting expanded-row matrix is 8x128.

3 Expanded-Row Matrix Configuration



Four 34934A modules (with attached terminal blocks) connected in an extended-row configuration.

Example: base configuration for each is 16x32; resulting expanded-row matrix is 16x128.

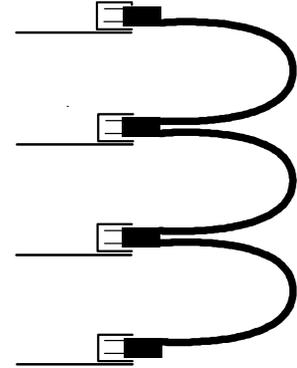


Eight 34934A modules (with attached terminal blocks) connected in an extended-row configuration.

Example: base configuration for each is 8x64; resulting expanded-row matrix is 8x512.

In the previous examples, note the module interconnection wires (illustrated from the rear view) which link the terminal blocks.

Typically, you would connect the upper row of the extension connector on a lower block to the lower row of the block above it, as shown in the detail at right (side view of terminal blocks and headers).



For convenience, Keysight sells (available separately) a 34934-80003 Expansion Cable Kit rated 30Vrms / 60Vdc max, which includes pre-wired terminators (sold in pairs) you would use to replace all but the bottom-most of the supplied terminators in the daisy-chain illustrated at right.

The Expansion Cable (pictured below) has eight 20 AWG wires pre-connected to the lower row (even pins 4-18) of an expansion terminator. The eight wires connect to the upper row (odd pins 3-17) of the next-lower 34934C-001 in the mainframe.



Four modules can be interconnected on each side of the mainframe in this manner, using three expansion cables. Should you need to make the last connection (between the two upper or lower modules) for an eight-module expanded matrix, you would have to fabricate the interconnection using eight 22 to 28 AWG coated wires, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

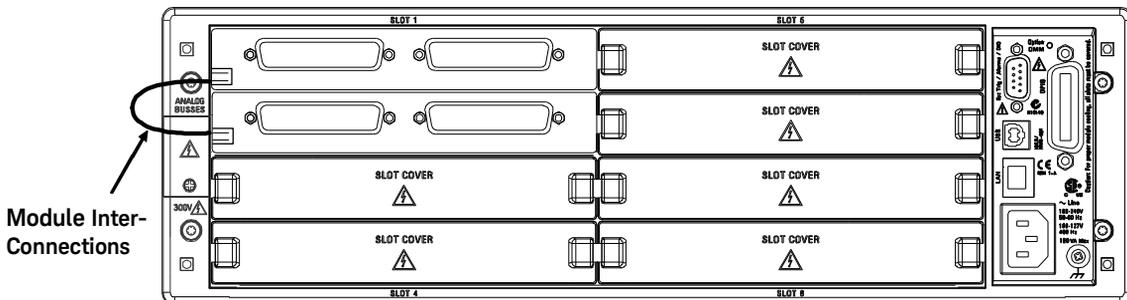
When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

WARNING

Fabrication of the interconnection is to be made by suitably trained personnel. A suitable length needed for interconnection is required and shall be rated for use with voltages up to 100V_{peak} and suitable to carry 0.5A. For applications where the maximum voltage is less than 100V_{peak} and 0.5A use wiring rated for at least the maximum voltage and current.

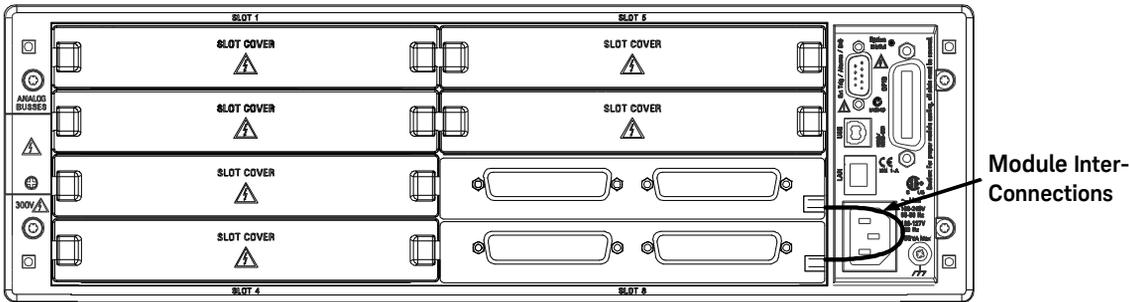
Arranging Multiple 34934A Modules with Configuration Blocks

The illustrations below show just a few of the possible physical installations of multiple 34934A-34934C pairs in a 34980A mainframe.



Two 34934A modules (with attached configuration blocks) connected in an extended-row configuration

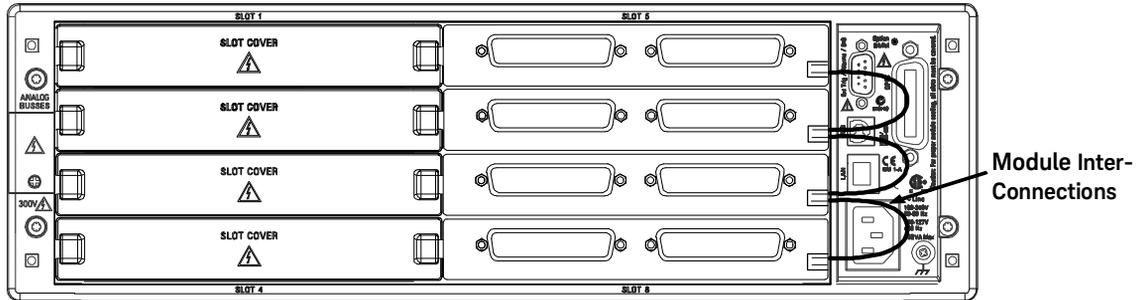
Example: base configuration for each is 4x128; resulting expanded-row matrix is 4x256.



Alternate method of connecting two 34934A modules.

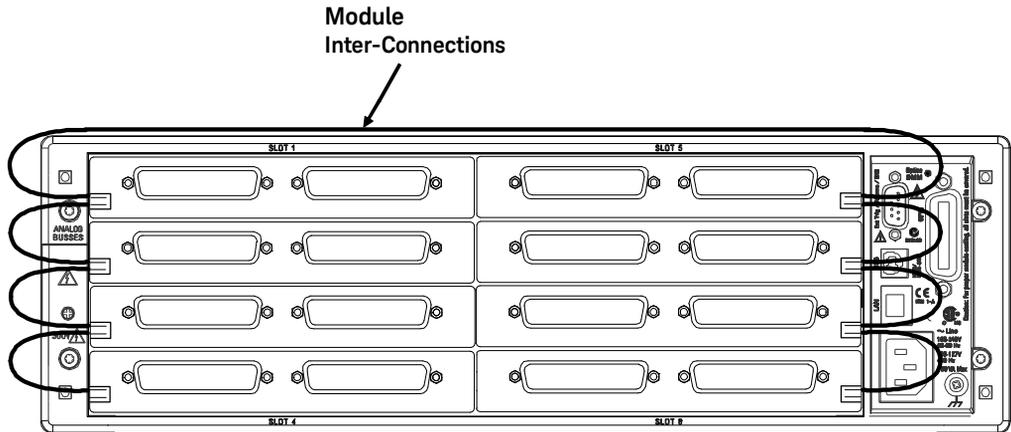
Example: base configuration for each is 8x64; resulting expanded-row matrix is 8x128.

3 Expanded-Row Matrix Configuration



Four 34934A modules (with attached configuration blocks) connected in an extended-row configuration.

Example: base configuration for each is 16x32; resulting expanded-row matrix is 16x128.



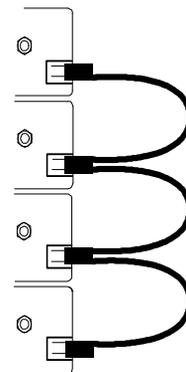
Eight 34934A modules (with attached configuration blocks) connected in an extended-row configuration.

Example: base configuration for each is 8x64; resulting expanded-row matrix is 8x512.

In the previous examples, note the module interconnection wires (illustrated from the side view) which link the configuration blocks. From a side view, although there are two extension connectors on each side edge (left and right) of each configuration block; only one is shown.

Typically, you would connect the upper row of the extension connector on a lower block to the lower row of the block above it, as shown in the detail at right.

For convenience, Keysight sells (available separately) a 34934-80003 Expansion Cable Kit rated 30Vrms / 60Vdc max, which includes pre-wired terminators (sold in pairs) you would use to replace all but the bottom-most of the supplied terminators in the daisy-chain illustrated at right.



The Expansion Cable (pictured below) has eight 20 AWG wires pre-connected to the lower row (even pins 4-18) of an expansion terminator. The eight wires connect to the upper row (odd pins 3-17) of the next-lower 34934C-001 in the mainframe.



Four modules can be interconnected on each side of the mainframe in this manner, using three expansion cables. Should you need to make the last connection (between the two upper or lower modules) for an eight-module expanded matrix, you would have to fabricate the interconnection using eight 22 to 28 AWG wires, fitted to “crimp-to-wire” contacts with latch tabs.

WARNING

When wiring with hazardous voltages, use wiring rated for the maximum voltage of the external source and follow (USA) NFPA 79 Table 12.5.1 Conductor Ampacity and 12.5.5(a) Ambient Temperature Correction Factors (or equivalent National Code requirements) when selecting the AWG required. Wiring of the terminal block must be performed by qualified persons. Failure to follow the instructions could result in equipment damage and may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

WARNING

When interconnecting system components, the overall system maximum allowable input rating of the system would default to the lowest rating of any one system component.

WARNING

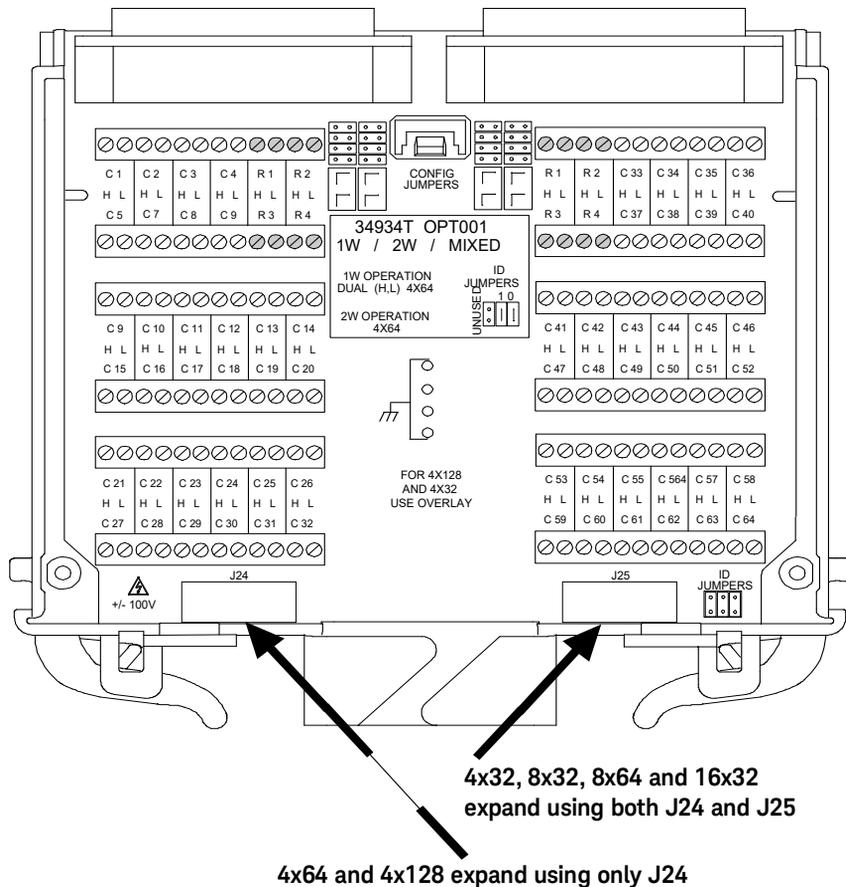
Fabrication of the interconnection is to be made by suitably trained personnel. A suitable length needed for interconnection is required and shall be rated for use with voltages up to 100V_{peak} and suitable to carry 0.5A. For applications where the maximum voltage is less than 100V_{peak} and 0.5A use wiring rated for at least the maximum voltage and current.

Creating Expanded-Row Matrices Using Terminal Blocks

Four-row matrices use the 34934T-001 terminal block. Eight-row or sixteen-row matrices use the 34934T-002 terminal block.

Your large matrix will be built as multiples of a base configuration of 4x32, 4x64, 4x128, 8x32, 8x64 or 16x32. Thus, please thoroughly read the appropriate base configuration section in [Chapter 2, “Base Matrix Configuration”](#), before combining modules and these terminal blocks to create multiples of your base matrix.

The illustration below shows expansion header locations on the 34934T-001 terminal block; the expansion header layout for the 34934T-002 is the same.



The next section shows how to make connections linking the modules through the expansion headers.

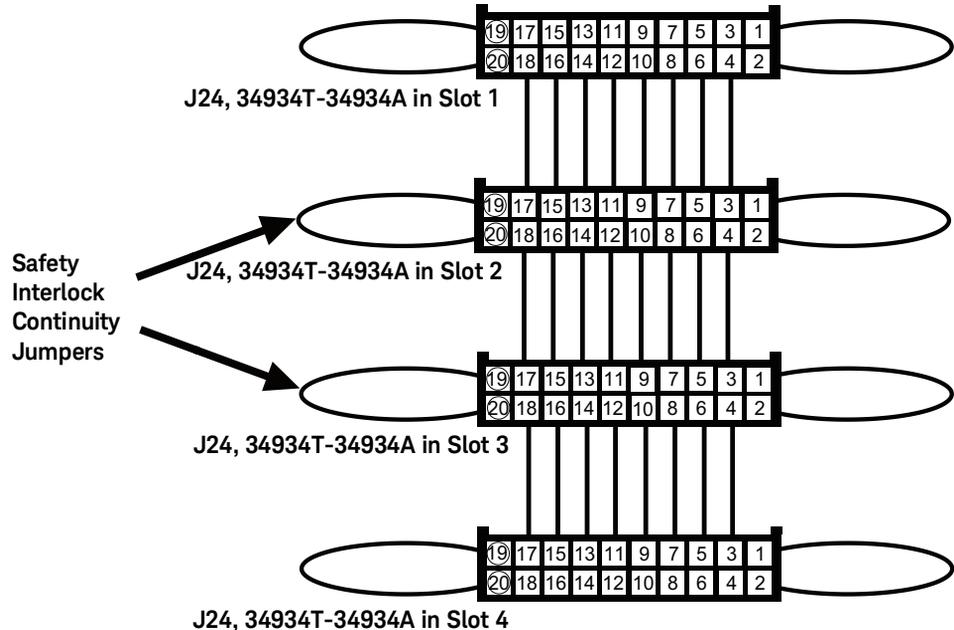
Interconnecting Terminal Blocks

At the rear of each terminal block are a pair of 20-pin extension headers, J24 and J25. These headers provide for row extension and row signal access.

If your base configuration is 4x64 or 4x128, you need create only *one* module interconnection chain, linking all J24 connectors.

For all other base configurations, you must connect create *two* module interconnection chains; one linking all J24 connectors, the other linking all J25 connectors.

For each interconnection chain, to physically link the member modules in an expanded-row matrix, you must connect the center eight pins either pins 3-17 (top row) or pins 4-18 (bottom row) of the corresponding (J214 or J25) extension headers on each terminal block in the matrix. An example of a daisy-chain connection of a four-module matrix is shown below.



For convenience, Keysight sells an optional 34934-80003 Expansion Cable Kit rated 30Vrms / 60Vdc max, which includes two Matrix Expansion Cables (these are like the supplied terminators 34934-80006, but with eight additional wires). There are two per kit for modes in which you will need two cables per T-block to build the daisy-chain. On [page 263](#) is an illustration of the Expansion Cable which comes in the optional kit, and associated wiring instructions.

Keep the bottom-most supplied terminator in a chain intact; then use an Expansion Cable to connect to the T-block above it. Add an Expansion Cable per additional T-block in the daisy chain.

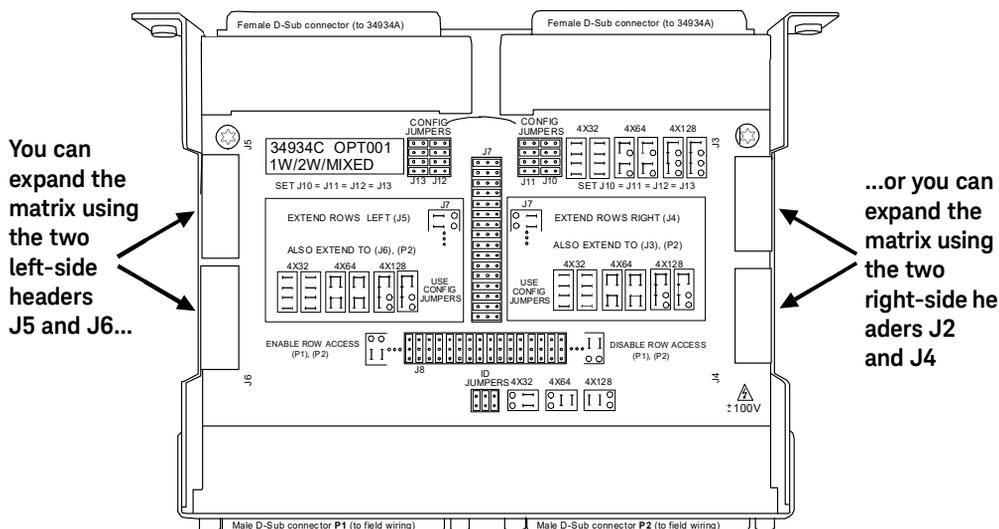
Creating Expanded-Row Matrices Using Configuration Blocks

Four-row matrices use the 34934C-001 configuration block. Eight-row or sixteen-row matrices use the 34934C-002 configuration block.

Your large matrix will be built as multiples of a base configuration of 4x32, 4x64, 4x128, 8x32, 8x64 or 16x32. Thus, please thoroughly read the appropriate base configuration section in [Chapter 2, “Base Matrix Configuration”](#), before combining modules and these configuration blocks to create multiples of your base matrix.

In [Chapter 2](#), you placed jumpers on each configuration block which determined *which expansion headers are available for module interconnection*.

The illustration below shows expansion header locations on the 34934C-001 configuration block; the expansion header layout for the 34934C-002 is the same.



The next section shows how to make connections linking the modules through the expansion headers.

Interconnecting Configuration Blocks

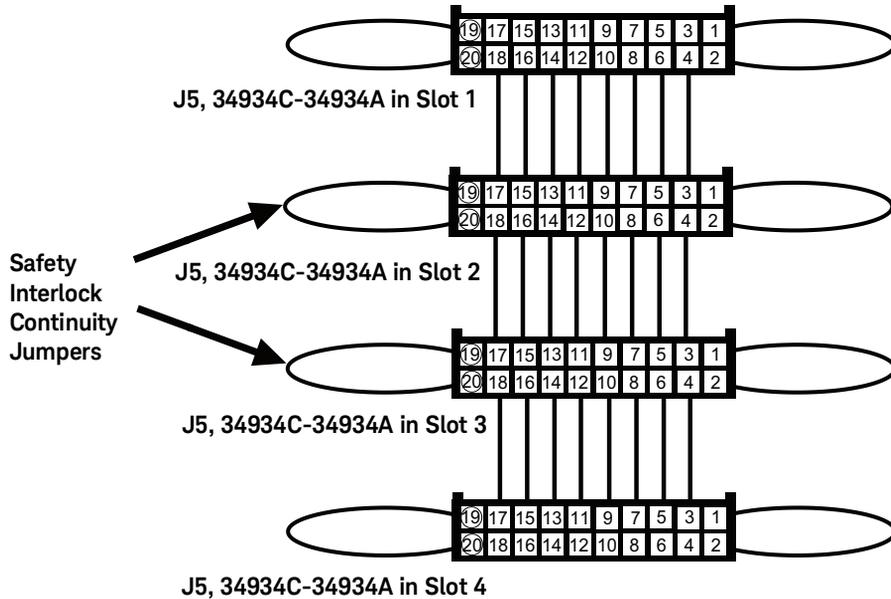
At each side of each configuration block are a pair of 20-pin extension headers. On the right side are labeled J3 and J4; on the left side are J5 and J6. These headers provide for row extension and row signal access, depending on the

placement of the block's J7 jumpers. You must have set the jumpers to extend rows to at least one of the four extension headers—possibly two. We'll call these the "live" headers.

If your base configuration is 4x64 or 4x128, you need create only *one* module interconnection chain, linking all J5 connectors (if expanding left) or linking all J4 connectors (if expanding right). If you set the J7 jumpers to also make the J3 or J6 headers live on *all* blocks in the matrix, you can alternately create this chain by linking all J3 or all J6 connectors.

For all other base configurations, you must connect create *two* module interconnection chains; one linking all J5 connectors *and* one linking all J6 connectors (if expanding left); one linking all J3 connectors *and* one linking all J4 connectors (if expanding right).

For each *interconnection chain*, to physically link the member modules in an expanded-row matrix, you must connect the center eight pins either pins 3-17 (top row) or pins 4-18 (bottom row) of the corresponding (J214 or J25) extension headers on each terminal block in the matrix. An example of *one column* of a daisy-chain connection of a four-module matrix is shown below.



3 Expanded-Row Matrix Configuration

For convenience, Keysight sells an optional 34934-80003 Expansion Cable Kit rated 30Vrms / 60Vdc max, which includes two Matrix Expansion Cables (these are like the supplied terminators 34934-80006, but with eight additional wires). There are two per kit for modes in which you will need two cables per T-block to build the daisy-chain. On [page 263](#) is an illustration of the Expansion Cable which comes in the optional kit, and associated wiring instructions.

Keep the bottom-most supplied terminator in a chain intact; then use an Expansion Cable to connect to the T-block above it. Add an Expansion Cable per additional T-block in the daisy chain.

4 SCPI Programming

Channel Numbering 272
SCPI Commands Used by the 34934A 273

This chapter provides two main sections.

The first provides the channel numbering algorithm for all configurations of the 34934A matrix module.

The second presents a summary of the programming commands you can send to the 34980A to operate and query status of the 34934A module. For the more frequently used commands, a description and examples are provided.

This chapter is not intended to be an exhaustive reference to these commands; you are encouraged to consult the *Keysight 34980A Programmer's Reference* for complete command syntax and applicability rules.

Channel Numbering

The slot and channel addressing scheme used in the programming examples follow the general form *sxxx* where *s* is the slot number for the 34980A mainframe (1 through 8) and *xxx* is the three-digit channel number. Channel numbers for the matrix modules are derived as shown in the following table:

34934A Module Channel Numbering		
Each row, column intersection has a unique channel number. This 4-digit channel number is of the form sxxx , where s is the mainframe slot# for the 34934A module. Calculate xxx from row R and column C , as shown below		
4x32 (4 matrices)	for M1H:	xxx = 100(2R-1) + C
	for M2H:	xxx = 100(2R-1) + C + 32
	for M1L:	xxx = 100(2R-1) + C + 64
	for M2L:	xxx = 100(2R-1) + C + 96
4x64 (2 matrices)	for MH:	xxx = 100(2R-1) + C
	for ML:	xxx = 100(2R-1) + C + 64
4x128	xxx = 100(2R-1) + C	
8x32 (2 matrices)	for MH:	xxx = 100R + C
	for ML:	xxx = 100R + C + 32
8x64	xxx = 100R + C	
16x32	xxx = 50(R+1) + C	
<u>Examples:</u>		
in slot #1, 8x64 matrix, you need to address row 3, column 16 Then xxx = 100(3) + 16 = 316 , so the channel # is 1316		

in slot #2, 4x32 matrix M2H, you need to address row 4, column 21 Then xxx = 100(2x4 - 1) + 21 + 32 = 753 , so the channel # is 2753		

The 34980A Web Browser interface, offers the easiest method to view the channel map for any set configuration of the module.

For complete guidance on LAN connection and navigating the 34980A Web Browser Interface, see the "Getting Started" chapter of the 34980A Mainframe Users Guide.

SCPI Commands Used by the 34934A

Command Summary

The commands listed below are used to control and query the operational state of the 34934A. Not listed are commands which apply to all 34980A modules.

Subsystem	Commands
DIAGnostic	DIAGnostic:RElAy:CYCLes? (@<ch_list>) DIAGnostic:RElAy:CYCLes:CLear (@<ch_list>)
ROUTe	ROUTe:CLOSe (@<ch_list>) ROUTe:CLOSe? (@<ch_list>) ROUTe:CLOSe:EXCLusive (@<ch_list>) ROUTe:CLOSe:PAIR (@<ch_list>) ROUTe:CLOSe:PAIR? (@<ch_list>) ROUTe:MODule:BUSY? {slot ANY} ROUTe:MODule:WAIT <slot ALL> ROUTe:MODule:WAIT? <slot ALL> ROUTe:OPEN (@<ch_list>) ROUTe:OPEN? (@<ch_list>) ROUTe:OPEN:ALL [{<slot> ALL}] ROUTe:OPEN:PAIR (@<ch_list>) ROUTe:OPEN:PAIR? (@<ch_list>) ROUTe:OPERation:OVERlap[:ENABle] <mode> ROUTe:OPERation:OVERlap[:ENABle]?
System - Related Commands	SYSTEM:MODule:ROW:PROTection <slot>, <mode> SYSTEM:MODule:ROW:PROTection? <slot> SYSTEM:MODule:ROW:PROTection DEFault, <mode> SYSTEM:MODule:ROW:PROTection? DEFault SYSTEM:MODule:TERMinAl:TYPE? <slot>

In the following subsections are examples which highlight *some* of the more commonly used commands listed above.

For complete command syntax, related information and examples of these and other SCPI commands used to program the 34980A, refer to the *Keysight 34980A Programmer's Reference* which can be downloaded from www.keysight.com/find/34980A.

Opening and Closing Channels

No physical matrix configuration changes are required for 1-wire versus 2-wire operation. The SCPI commands alone determine if channels on paired matrices are closed individually or in pairs. Therefore, individual 1-wire and 2-wire commands can be combined in a program sequence.

One Wire Operation

The ROUTe:CLOSe and ROUTE:OPEN commands close and open a single one-wire channel or list of one-wire channels.

Example 1: The example below opens a single crosspoint at row 3, column 1, matrix M1L; with the module in Slot 1 and configured for 4x32 mode.

ROUT:OPEN (@1565)

Example 2: The example below closes a path containing two crosspoints. They are at row 2, columns 2 and 34, matrix MH; with the module in Slot 2 and configured for 4x64 mode.

ROUT:CLOS (@1302,1334)

Two Wire Operation

The ROUTe:CLOSe:PAIR and ROUTE:OPEN:PAIR commands close and open a single two-wire channel pair or list of two-wire channel pairs. They apply only to the following matrix modes, and operate channels in the listed paired matrices:

- 4x32: Paired matrices M1H and M1L; Paired matrices M2H and M2L
- 4x64: Paired matrices MH and ML
- 8x32: Paired matrices MH and ML

Example 1: The example below opens a single paired crosspoint at row 3, column 1, matrices M1H and M1L; with the module in Slot 1 and configured for 4x32 mode.

ROUT:OPEN:PAIR (@1565)

Note the similarity in syntax to the 1-wire command; *in any paired command, you specify only the high channel in the pair*. However, this command not only opens channel 1565 on M1H, but also automatically opens paired channel 1629 on M1L.

Example 2: The example below closes a 2-wire path containing four crosspoints (two pairs of channels). The crosspoints are row 2, columns 2 and 34, matrices MH and ML; with the module in Slot 2 and configured for 4x64 mode.

ROUT:CLOS:PAIR (@1302, 1334)

Note the similarity in syntax to the 1-wire command. However, this command not only opens channels 1302 and 1334 on M1H, but also automatically opens paired channels 1366 and 1400 on M1L.

Querying Channels for Open or Closed State

One Wire Operation

The ROUTe:CLOSe? and ROUTe:OPEN? commands query the state of a single one-wire channel or list of one-wire channels. It returns (for each channel in the list) a “1” (true) if the channel is closed or a “0” (false) if the channel is open.

Example: The example below closes the crosspoint at row 4, column 2, matrix MH; with the module in Slot 5 and configured for 8x32 mode. Then two queries are sent, with the expected results.

```
ROUT:CLOS (@5402)
ROUT:CLOS? (@5402) !Returns a 1
ROUT:OPEN? (@5402) !Returns a 0
```

Two Wire Operation

The ROUTe:CLOSe:PAIR? and ROUTe:OPEN:PAIR? commands query the state of a single two-wire channel pair or list of two-wire channel pairs. They apply only to the following matrix modes, and operate channels in the listed paired matrices:

- 4x32: Paired matrices M1H and M1L; Paired matrices M2H and M2L
- 4x64: Paired matrices MH and ML
- 8x32: Paired matrices MH and ML

Example: The example below closes the channel pair at row 3, column 2, matrices MH and ML; with the module in Slot 5 and configured for 8x32 mode. Then two queries are sent, with the expected results.

```
ROUT:CLOS:PAIR (@5402)
ROUT:CLOS:PAIR? (@5402) !Returns a 1
ROUT:OPEN:PAIR? (@5402) !Returns a 0
```

Note the similarity in syntax to the 1-wire command; *in any paired command, you specify only the high channel in the pair*. However, this command returns (for each channel pair in the list) a “1” if both relays are closed; it returns a “0” if either or both relays are open.

Querying the Mainframe for Module Identity

The SYSTEM:MODule:TERMinal:TYPE? command queries the 34980A mainframe for the recognized base configuration “ID” for the 34934A module in a specified slot. The mainframe sees this ID upon bootup, whether the ID was set by direct D-Sub wiring, terminal block jumper placement or configuration block jumper placement.

Example: The following command returns the identity of the 34934A module installed in slot 7, if set for a base configuration of 16x32. The result does not depend on whether this module is a stand-alone matrix or used as a member of an extended-row matrix configuration.

```
SYST:MOD:TERM:TYPE? 7 !Returns R16_C32
```

Setting the Relay Protection Mode

The SYSTem:MODule:ROW:PROTection command sets the row protection mode for the 34934A module in a specified slot.

Example: The following command sets the row protection mode to FIXed, which closes the row protection relays on all rows for the 34934A module in slot 7.

```
SYST:MOD:ROW:PROT 7, FIX
```

Example: The following command sets the *default* row protection mode to ISOLated, which opens the row protection relays and bypass relays on all rows for

all 34934A modules in the mainframe. The command is executed at power-on or following a *RST or SYSTem:CPON command.

SYST:MOD:ROW:PROT DEF, ISO

Reading Relay Cycle Count

The DIAGnostic:RELy:CYCLes? command queries the cumulative number of operational cycles (relay cycle count) for any of the 512 crosspoint relays in the 34934A module, since it was last reset. The query can be used for a single channel or list of channels.

Example: The following command returns the cycle count at row 8, column 28, with the module in Slot 4 and configured for 8x64 mode.

DIAG:REL:CYCL? (@4831)

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5 34980A Current Limiting Graphs

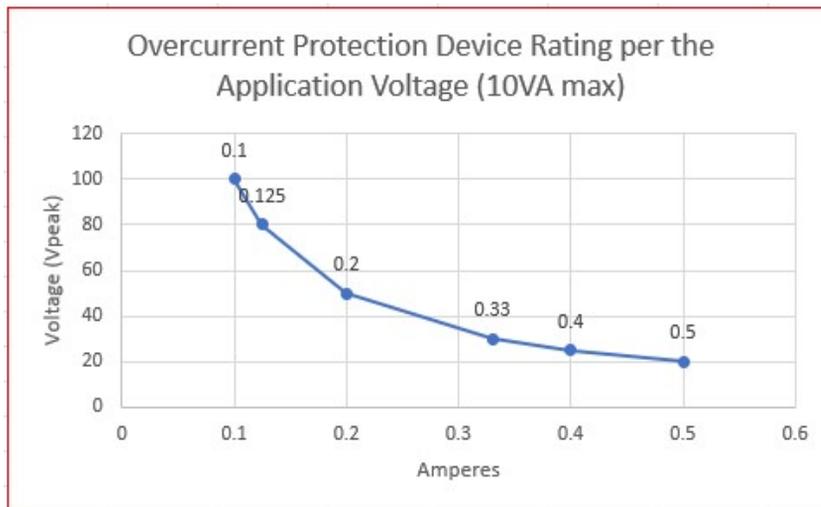
34980A Current Limiting Graphs 280

This section provides the current limiting graphs for the 34980A.

34980A Current Limiting Graphs

Modules	Pollution Degree 1	Pollution Degree 2	Transients
Reed Matrix 34934A	Quad 4x32 matrix, 20 MHz	Quad 4x32 matrix, 20 MHz	450Vpk
Terminal Block 34934T	$\pm 100\text{Vpk}^1$	$\pm 100\text{Vpk}^1$	
Configuration Block 34934C	0.5A (switch) / 0.5 A (carry) 10VA/channel ^{2,3} Volt-Hertz limit 10^8 Initial closed channel resistance ⁴ : $<1\ \Omega / 100\ \Omega$	0.5A (switch) / 0.5A (carry) 10VA/channel ^{2,3} Volt-Hertz limit 10^8 Initial closed channel resistance ⁴ : $<1\ \Omega / 100\ \Omega$	

The overcurrent protection devices will be rated, or the snubber circuits will limit the current, according to:



1. Peak voltage, channel-to-channel or channel-to-earth.
2. Limited to 6 W channel resistance power loss per module.
3. Protection Resistors: $100\ \Omega \pm 1\%$; 0.25W; TC = $\pm 100\text{ppm}/^\circ\text{C}$.
4. Into analog bus. System errors are included in the internal DMM measurement accuracy specifications.

A Handling Considerations

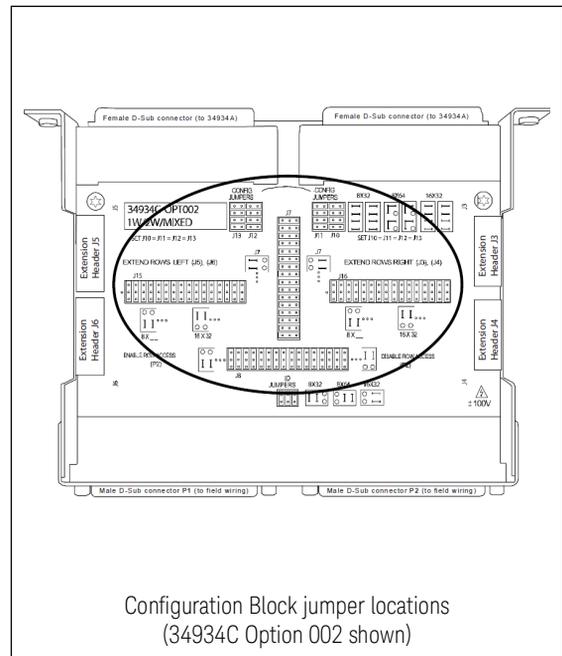
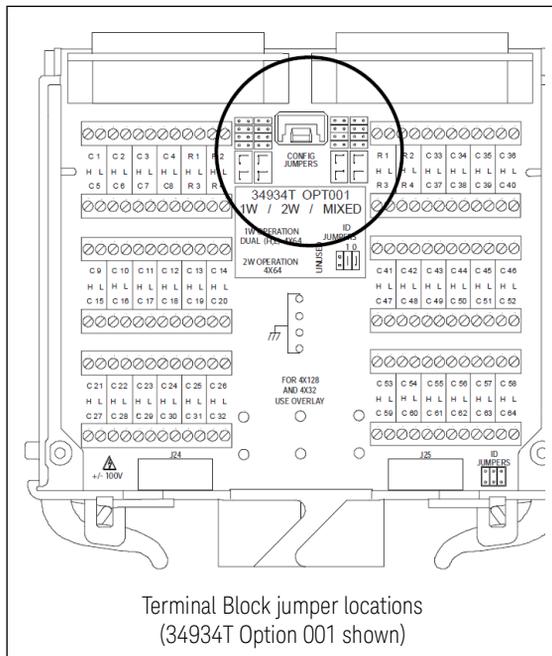
Product Handling Recommendations for Maintaining $> 10G\Omega$ DC Isolation [282](#)

This section contains recommendations for handling jumpers and expansion cables in order to minimize the risk of contamination in applications where DC (channel to channel) isolation $> 10G\Omega$ is required.

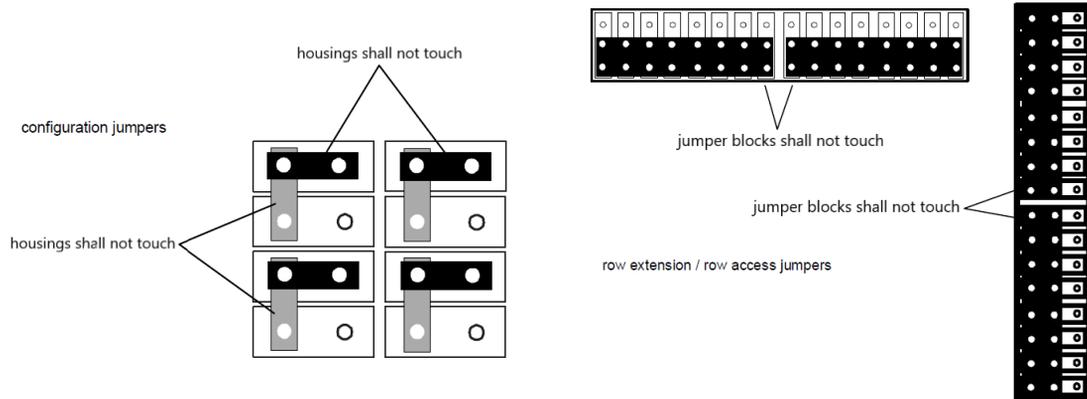
Product Handling Recommendations for Maintaining > 10GΩ DC Isolation

Issue: Recent tests in a high temperature - high humidity environment have revealed that contaminants on the movable jumpers of the 34934A terminal block and configuration block may affect the published DC Isolation specification. Salts/oils from fingertips can create electrical paths (< 10 GΩ) if these contaminants are transferred to the surfaces of adjacent jumpers in physical contact with one another. The paths can cause small amounts current to leak from one signal (channel) to another. Testing in this environment has also shown that leakage currents due to contaminants on adjacent wires of the expansion cable (used to connect multiple 34934A matrix modules) can also affect DC isolation.

Handling - Terminal Block and Configuration Block Jumpers: The locations of the jumpers on options 001 and 002 of the 34934T Terminal Block and on options 001 and 002 of the 34934C Configuration Block requiring special handling are shown in the figures below.



When setting the configuration jumpers for the desired 34934A matrix configuration and the additional row extension and row access jumpers on the Configuration Block, use cotton gloves while handling the individual jumpers. Once the jumpers are set, adjust their position such that adjacent jumpers are not in contact with one another. The figures on the following page show this positioning.

**WARNING**

When installing the Config Jumpers ensure that the adjacent housing do NOT touch! Failure to do so may result in hazardous conditions such as fire or shock and could lead to personal injury or death.

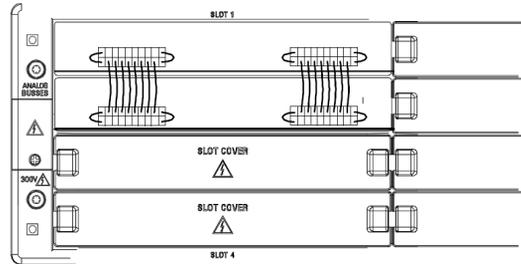
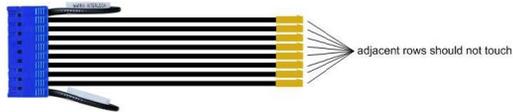
If cotton gloves are not available when setting and positioning the jumpers, clean the surface of the plastic jumper “housings” using a cotton cloth and 91% isopropyl alcohol.

WARNING

Do NOT use 91% isopropyl alcohol on any other part of the 34980A chassis and its modules. Ensure that the surfaces of the jumpers are absolutely dry before applying power.

A Handling Considerations

Handling - Expansion Cables: To avoid degradation of the DC isolation specification in applications involving multiple 34934A modules, the rows of the expansion cables connecting the 34934A modules shall not touch. The figures below identify the expansion cable and cable location.



If necessary, use a cotton cloth and 91% isopropyl alcohol to clean the cable rows after handling.

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