

Keysight x1149 Boundary Scan Analyzer Software Release 2.1.0.110

System Requirement

Release Date	August 10, 2023
Platform	Windows 10 Professional (64-bit) Windows 11 Professional (64-bit) Windows 11 Enterprise (64-bit) In addition, ensure that all Windows updates are installed.
Hardware	Processor : Core Intel Pentium® 4 or better (minimum 1.8 GHz) Memory : Recommended 16 GB RAM (minimum 8 GB RAM) Disk Space : Minimum 10 GB on the installation drive (can be Any drive), plus 1 GB on C: drive.
Display Properties	Minimum Screen Resolution : 1024 x 768 (1600x900 – R&D Verified) Color Quality : Minimum 16-bit
Language Settings	English (United States) Chinese (Simplified)
License	M6800B x1149 Test Development License Bundle M6801B x1149 Run-time License Bundle M6802B x1149 1687-2014 Development License M6803B x1149 1687-2014 Run-time License M6804B x1149 Result Analyzer License M6806B x1149 Development Only Software Upgrade 2.1 License M6813B x1149 Run-time Only Software Upgrade 2.1 License M6815B x1149 Test Development License (Perpetual)

Dear Valued Customer,

Keysight x1149 Software release 2.1.0.110 Release Notes

Keysight Technologies is pleased to introduce the latest software release 2.1.0.110 for x1149 Boundary Scan Analyzer. This release comes with new features and tools added to enhance your experience in the development, debug, and production execution of tests in line with IEEE 1149.1-2013 Standards for Printed Circuit Board Assembly (PCBA) with Boundary Scan devices. The objective is to have the flexible cost while achieving the best Boundary Scan coverage at all levels of your product life cycle.

New IEEE 1149.6-2015 Standard Support

- This new release adds support to IEEE 1149.6-2015 Standard for Testing Advanced Digital Networks.
- Controlling Segmented Boundary Scan Register selection
- Electronic Chip Identification (ECID) feature

New Licensing Scheme for Total Software Management

- This new version of Software Release introduces floating licensing scheme on both subscription and perpetual basis:
 - Ensure support of consistent licensing types and terms company wide across Keysight product lines.
 - Encourage customers to pay for what is only needed and promise consistent ROI.

Brand New Hardware

- Provides a new facelift hardware design with a reinvigorated look in par with Keysight branding to provide the overall new customer experience using our Keysight x1149 Boundary Scan Analyzer platform.

Read a summary of the above and other new features added into this power-packed new release. For up-to-date information, please visit <http://www.keysight.com/find/x1149>. We appreciate your ongoing support and look forward to providing you with more value-added features as we continue to develop this x1149 platform.

Yours faithfully,

Yee May Koh

Product Marketing Manager

Keysight Technologies

yee-may.koh@keysight.com

What's New in Keysight x1149 Boundary Scan Analyzer 2.1.0.110 Software Release

The x1149 Boundary Scan Analyzer 2.1.0.110 Software Release provides the following enhancement and new features to ensure the success of Boundary Scan test at all product life cycle from Pre-PCB Layout, Proto Built, NPI to Production Run.

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Other New Features (must include if can include any other features)

- Keysight x1149 data log path setting at project level.
- DIMM socket pin number when test fail

Project (.absp) Compatibility

1. Project (.absp) file is backward compatible. Project file created or opened in earlier Software Revision can be opened and used in Software Revision 2.1.0.110.
2. Auto Convert or Update .absp/.absl developed in older x1149 software versions
 - The 2.1.0.110 release will automatically convert or update absp and absl from version 1.3.x.y onwards to version 2.1.0.110 and backup the original absp and absl files.
 - Conversion automatically starts when a 1.3.x.y absp or absl is opened in x1149 2.1.0.110 Development GUI.
3. Backup file name for 1.3.x.y to 2.1.0.110:
 - The original absp or absl file will be backed up as <filename>_<YYYYMMDDHHMMSS>_<ProductNum>_<(Version)(SubVersion)>.absp :
 - Example: For a project “digital-plane.absp”
 - Original project file will be saved into “digital-plane_20230801083215_0210_1314.absp”
 - The “digital-plane.absp” will be automatically converted to 2.1.0.110 version format.
4. Note:
 - If conversion passes, project is automatically opened, and user will be prompted to do test regeneration. Click cancel if you wish to keep existing tests.
 - After conversion passes and project is opened successfully, always check the required action status found on bottom right corner.
 - Project files in versions earlier than 1.3.x.y are not supported in the auto conversion and backup.
 - **Previous version of BSDL will open and run without parsing in the new Software Release 2.1.0.110 unless been modified.**

Programming Compatibility

Programming tests created with software versions 1.3.0.0 or 1.3.1.0 (dated before Apr'2015) are not compatible with software version 2.1.0.110. Users are required to delete the programming test and regenerate using 2.1.0.110 software.

- For project that have been developed in the environment as mentioned above, if required action is 'Validate Data', this implies there are data validation errors found in this project.
 - **Note: Click on Project Explorer>Chain>Reconfigure Chain to see error messages, review message and fix library accordingly.**

Enhancements/Defects Fixed

CR#	Title
BBOX-826	Fixes on Wrapper for i3070
BBOX-780	Fixes on BSDL file error report before configure chain
BBOX-829	Fixes on index and cell mismatch as segmented boundary scan cells are merged as one contiguous chain
BBOX-831	Fixes on output panel prompt compilation error when creating a new project
BBOX-794	x1149 Software crash after running 36 hours in production mode
BBOX-748	Create golden absp based on TGL and EBG PCH system test on BSDL parser
BBOX-759	x1149 unknown error during test in production mode
BBOX-773	Unable to save BSDL file: UNDEFINED package name error
BBOX-782	x1149 hangs at Operator ID window in Production Mode
BBOX-813	Floating license unable to run with i3070 license
BBOX-777	Same detection modifier for positive and negative pin not displayed as the same in the BSDL warnings
BBOX-787	Incorrect location of x1149 wrapper dll in Windows 11
BBOX-732	Fixes on parsing not reported upon editing the package until BSDL is modified
BBOX-666	Segment enabled on test editor interpreted by UI and not in the backend
BBOX-760	Additional tide symbol “ ~ ” in repair ticket in front of External Pin when board abbreviation empty
BBOX-763	Regression test for Root3070 – SNCOMP, Test Consultant
BBOX-764	Cell boundary is not found in ParseAioPortInputPin2015
BBOX-781	Fixes on edit value of multiple box at one time
BBOX-791	Semantic check added for incompatible BIDIR pins causing BSDL parsing error
BBOX-525	Unable to save absp file upon editing test sequence
BBOX-540	Node info not updated upon added or removed simultaneously
BBOX-544	Parsing AIO_PIN_BEHAVIOUR with AC parameters for dot6 interconnect test
BBOX-574	Verify result analysis for dot6
BBOX-579	Identify segment from BSDL parsing result and write to database
BBOX-580	Read database and display in user interface
BBOX-585	Write to database when segment enabled and/or disabled in user interface
BBOX-586	MSPD read segment data to produce updated STAPL
BBOX-596	X1149 use coupling time as HP time and Edge_Detect_Time as LP time interchangeably
BBOX-603	License control check for dot 6 2015
BBOX-607	IEEE 1149.6-2015 license support
BBOX-630	Include segment length in segment list table
BBOX-637	Shorted cap dot 6 test generation
BBOX-643	Refresh UI segment length in segment list table

CR#	Title
BBOX-672	CVNVI disable together with other segments PASS integrity test
BBOX-659	Fixes on delay cell in frame debugger
BBOX-131	Register Field Statement rules
BBOX-545	Parsing AIO_PORT_BEHAVIOUR attribute with Port_Link_List
BBOX-595	Fixes on identifying the right node before executing EXTEST
BBOX-597	Fixes for no test definition for pins defined as "no_pulse" as for TPG shall display warning with a remarks
BBOX-611	Custom ECID test generation and execution
BBOX-612	Enable only ECID test as when BSDL have ECID definition
BBOX-625	2013 user defined VHDL package implementation
BBOX-638	Fixes on Segment Name upon executing resistor pull-up/down test
BBOX-647	Fixes on x1149 crash after 5 hours upon updating license after license expiration
BBOX-656	Test editor and frame debugger column update
BBOX-667	Fixes on BSDL compilation error on UI during test generation for projects developed using Software Revision 1.6 and earlier
BBOX-684	Segment select from UI
BBOX-692	Fixes on DIMM/PCIE connector test: Display physical connector pin
BBOX-754	Fixes on wrapper dll working with floating license
BBOX-716	Fixes on option to choose segment name in repair ticket
BBOX-722	Attribute AIO_PIN_BEHAVIOUR rules: BSDL statement rules check
BBOX-619	BSDL statement rules for final semantic check
BBOX-696	Fixes on BSDL showing warning of missing package after user copy Customer VHDL package into customized VHDL folder
BBOX-702	Fixes to export customized VHDL packages files
BBOX-715	ECID end-to-end test execution
BBOX-720	Fixes on ECID result in log files when test sequence set to release in x1149 Debug Mode
BBOX-721	Automatically uninstall the existing version of x1149 during the installation on new x1149
BBOX-724	Fixes to block user from saving project when wrong VHD: package is defined
BBOX-728	Fixes on shorted Cap Dot6 not generated if "On-Chip" and "On_Chip_Programmable" was defined in positive pin in AIO pin
BBOX-743	Fixes on wrapper API unable to launch if the license expired
BBOX-744	Fixes on regression test for Root3070-svf2jam
BBOX-755	Fixes on unable to add power supply in x1149
BBOX-98	Register Field Assignment (USER keyword) defined in 2015 standard
BBOX-665	Fixes on Cygwin installation from direct download
BBOX-669	Fixes on KAL service-related command crash in x1149
BBOX-497	Fixes on test escape: memory exception error