Keysight 2-Port and 4-Port PNA Network Analyzer

N5221B 900 Hz to 13.5 GHz
N5222B 900 Hz to 26.5 GHz

KEYSIGHT

TECHNICAL SPECIFICATIONS

OPTION 210 AND 410

Documentation Warranty

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This is a complete list of the technical specifications for the N5221B and N5222B PNA Series network analyzers with the following options:

2-Port Model

Option 210 – 2-port, single source, with metrology configuration

4-Port Model

Option 410 – 4-port, dual source, with metrology configuration.

See block diagrams on page 39.

Notes

This document provides technical specifications for the 85052B, N4691B and N4433A calibration kits.

Please download our free Uncertainty Calculator from http://www.keysight.com/find/na_calculator to generate the curves for your calibration kit and PNA setup.

For all tables in this data sheet, the specified performance at the exact frequency of a break is the degraded value of the two specifications at that frequency.

Definitions

All specifications and characteristics apply over a 25 °C ±5 °C range (unless otherwise stated) and 90 minutes after the instrument has been turned on.

Specification (spec.): Warranted performance. Specifications include guardbands to account for the expected statistical performance distribution, measurement uncertainties, and changes in performance due to environmental conditions.

Characteristic (char.): A performance parameter that the product is expected to meet before it leaves the factory, but that is not verified in the field and is not covered by the product warranty. A characteristic includes the same guardbands as a specification.

Typical (typ.): Expected performance of an average unit which does not include guardbands. It is not covered by the product warranty.

Nominal (nom.): A general, descriptive term that does not imply a level of performance. It is not covered by the product warranty.

Supplemental performance data (SPD): Represents the value of a parameter that is most likely to occur; the expected mean or average.

Calibration: The process of measuring known standards to characterize a network analyzer's systematic (repeatable) errors.

Corrected (residual): Indicates performance after error correction (calibration). It is determined by the quality of calibration standards and how well "known" they are, plus system repeatability, stability, and noise.

Uncorrected (raw): Indicates instrument performance without error correction. The uncorrected performance affects the stability of a calibration.

Standard: When referring to the analyzer, this includes no options unless noted otherwise.

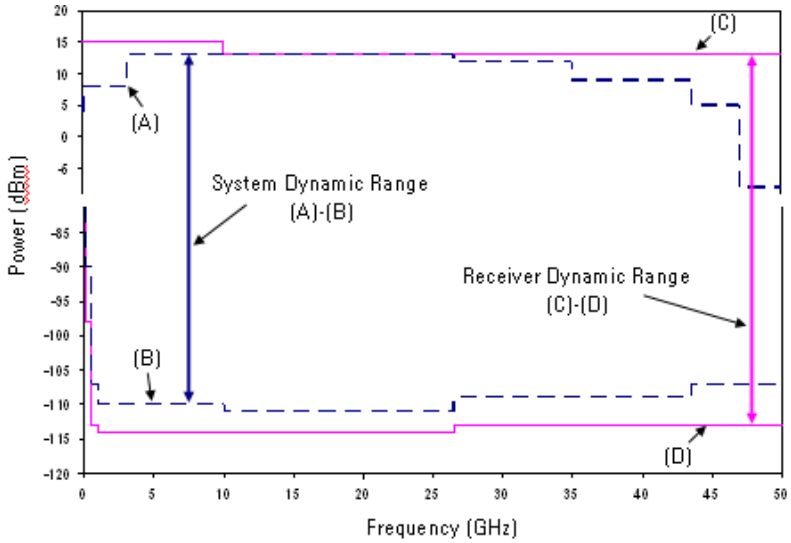
Corrected System Performance

The specifications in this section apply for measurements made with the N5221B and N5222B PNA network analyzers with the following conditions:

- 10 Hz IF bandwidth
- No averaging applied to data
- Isolation calibration with an averaging factor of 8

System Dynamic Range and Receiver Dynamic Range

- System Dynamic Range is defined as the specified source maximum output power (spec) minus the noise floor (spec).
- Extended Dynamic Range at Direct Access Input is defined as the specified source maximum output power (spec) minus the direct receiver access input noise floor (spec).
- Receiver Dynamic Range is defined as the test port compression at 0.1 dB (typical) minus the noise floor (typical).



NOTE The effective dynamic range must take measurement uncertainties and interfering signals into account. This set-up should only be used when the receiver input will never exceed its maximum receiver input. When the analyzer is in segment sweep mode, it can have predefined frequency segments which will output a higher power level when the extended dynamic range is required (i.e. devices with high insertion loss), and reduced power when the maximum receiver input level will occur (i.e. devices with low insertion loss). The extended range is only available in one-path transmission measurements.

It may typically be degraded at particular frequencies below 500 MHz due to spurious receiver residuals.

Table 1a. System Dynamic Range and Receiver Dynamic Range, N5221B, Option 210, 410

Description	Specification			Typical			
	System Dynamic Range (dB) (A)-(B)	Max Leveled Output Power (dBm) (A)	Test Port Noise Floor (dBm) (B)	System Dynamic Range (dB)	Receiver Dynamic Range (dB) (C)-(D)	0.1 dB Compression at Test Port (dBm) (C)	Test Port Noise Floor (dBm) (D)
10 MHz to 50 MHz	88	6	-82	102	103	15	-88
50 MHz to 100 MHz	102	7	-95	117	118	15	-103
100 MHz to 500 MHz	112	7	-105	124	122	12	-110
500 MHz to 2 GHz	121	7	-114	132	130	12	-118
2 GHz to 3.2 GHz	121	7	-114	133	131	12	-119
3.2 GHz to 13.5 GHz	121	7	-114	135	131	12	-119

Table 1b. System Dynamic Range and Receiver Dynamic Range, N5222B, Option 210, 410

Description	Specification			Typical			
	System Dynamic Range (dB) (A)-(B)	Max Leveled Output Power (dBm) (A)	Test Port Noise Floor (dBm) (B)	System Dynamic Range (dB)	Receiver Dynamic Range (dB) (C)-(D)	0.1 dB Compression at Test Port (dBm) (C)	Test Port Noise Floor (dBm) (D)
10 MHz to 50 MHz	88	6	-82	102	103	15	-88
50 MHz to 100 MHz	102	7	-95	117	118	15	-103
100 MHz to 500 MHz	112	7	-105	124	122	12	-110
500 MHz to 2 GHz	121	7	-114	132	130	12	-118
2 GHz to 3.2 GHz	121	7	-114	133	131	12	-119
3.2 GHz to 13.5 GHz	121	7	-114	135	131	12	-119
13.5 GHz to 20 GHz	121	7	-114	134	132	12	-120
20 GHz to 24 GHz	118	7	-111	130	128	10	-118
24 GHz to 26.5 GHz	108	1	-107	124	126	10	-116

Corrected System Performance, Option 210, 410

For any Sii reflection measurement:

- Sjj = 0.

For any Sij transmission measurement:

- Sji = Sij when Sij ≤ 1
- Sji = 1/Sij when Sij > 1
- Skk = 0 for all k

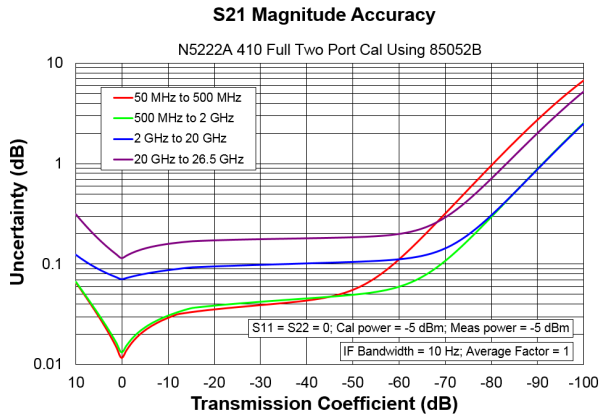
Applies to the N5221B/2A Option 210 or 410 analyzers, 85131F flexible test port cable set, and a full 2-port calibration. Also applies to the following condition:

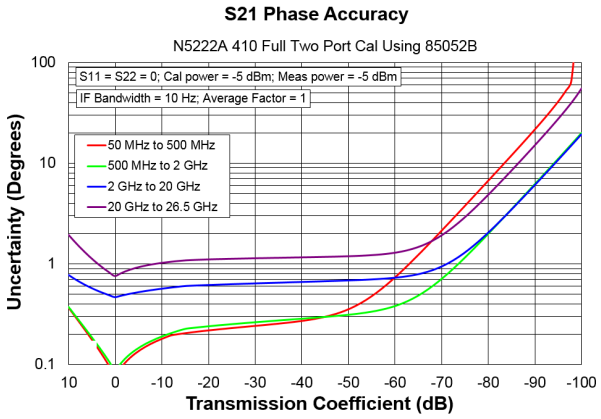
Environmental temperature 23° ±3 °C, with < 1 °C deviation from calibration temperature

Table 2a. N5221B and N5222B with 85052B Calibration Kit - Specification

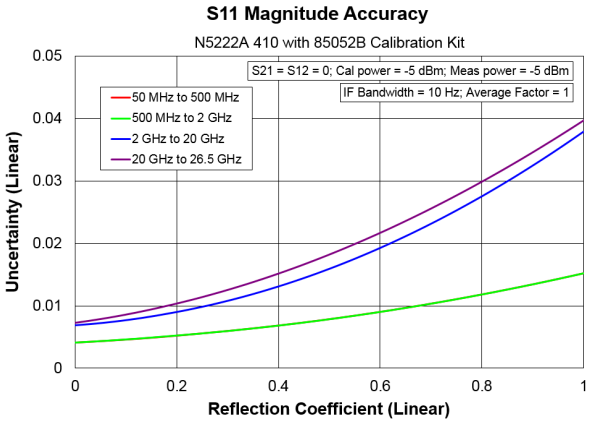
Description	Specification (dB)					
	10 MHz to 50 MHz	50 MHz to 500 MHz	500 MHz to 2 GHz	2 GHz to 13.5 GHz	13.5 GHz to 20 GHz	20 GHz to 26.5 GHz
Directivity	48	48	48	44	44	44
Source Match	40	40	40	31	31	31
Load Match	48	48	48	44	44	44
Reflection Tracking						
Mag	0.003	0.003	0.003	0.006	0.006	0.006
Phase	0.020	0.020	0.020	0.040	0.040	0.040
Transmission Tracking						
Mag	0.006	0.006	0.007	0.046	0.058	0.095
Phase	0.043	0.038	0.049	0.306	0.385	0.626

Transmission Uncertainty





Reflection Uncertainty



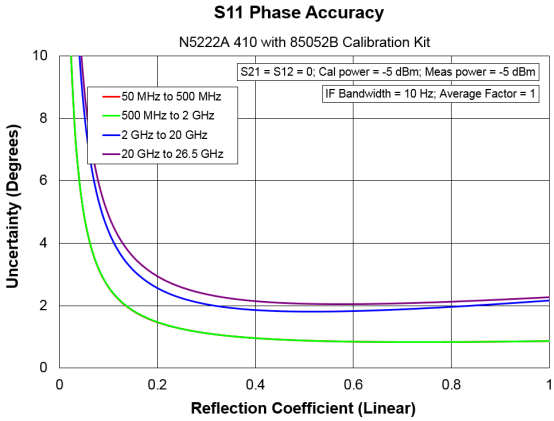
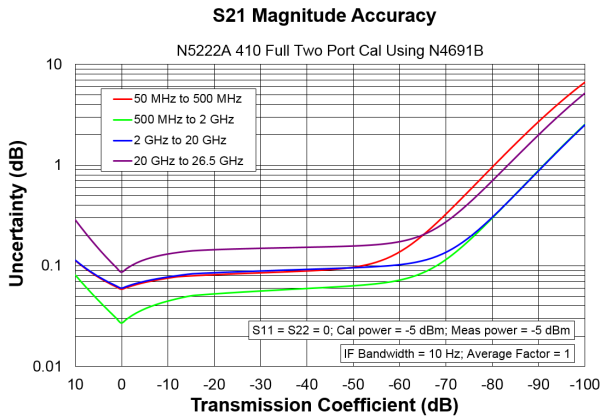
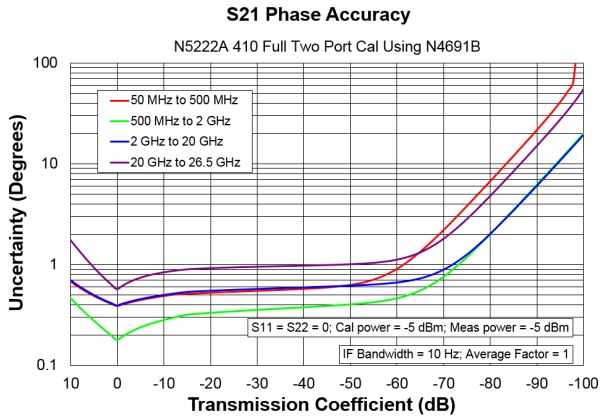


Table 2b. N5221B and N5222B with N4691B 2-Port Electronic Calibration Module

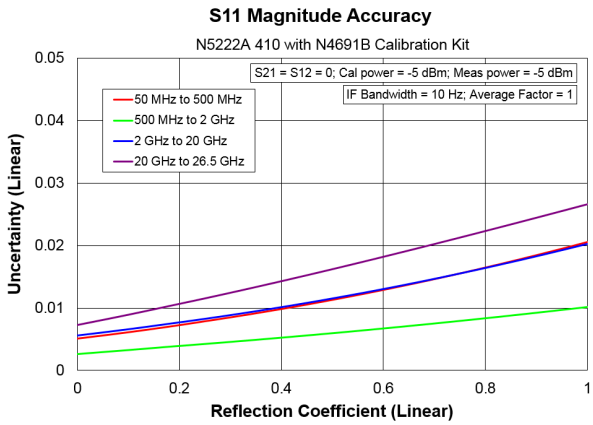
Description	Specification (dB)					
	10 MHz to 50 MHz	50 MHz to 500 MHz	500 MHz to 2 GHz	2 GHz to 13.5 GHz	13.5 GHz to 20 GHz	20 GHz to 26.5 GHz
Directivity	41	46	56	48	48	44
Source Match	36	41	47	44	44	40
Load Match	36	41	47	43	43	39
Reflection Tracking						
Mag	0.060	0.050	0.020	0.040	0.040	0.050
Phase	0.396	0.330	0.132	0.264	0.264	0.330
Transmission Tracking						
Mag	0.065	0.052	0.021	0.045	0.047	0.067
Phase	0.428	0.344	0.138	0.300	0.309	0.444

Transmission Uncertainty





Reflection Uncertainty



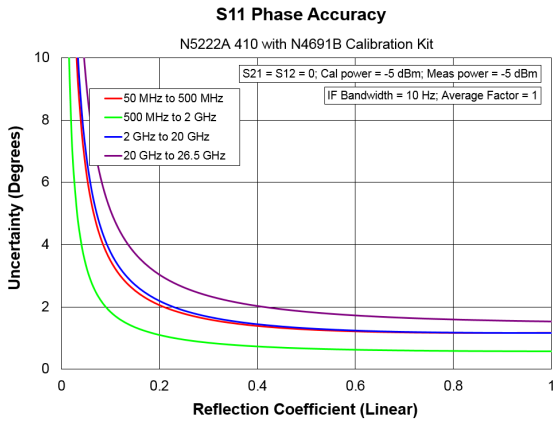
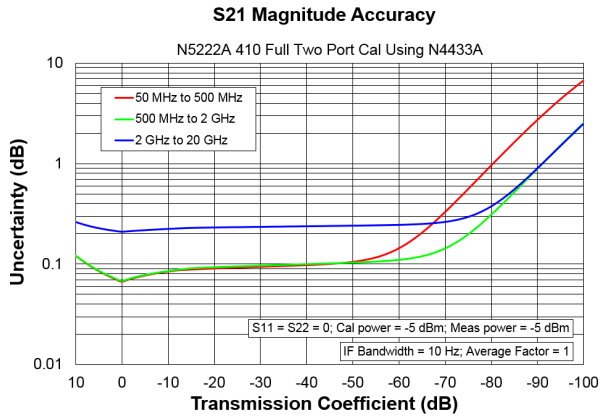


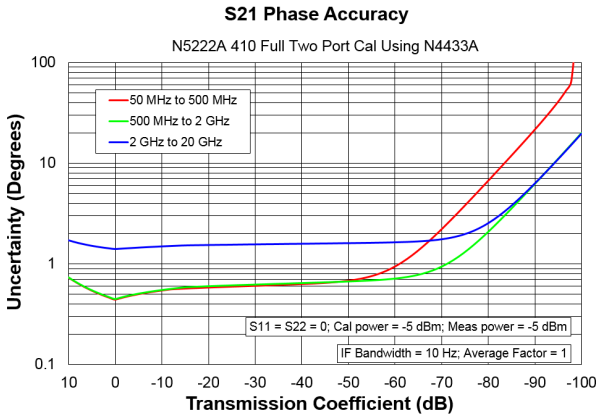
Table 2c. N5221B and N5222B with N4433A 4-Port Electronic Calibration Module

NOTE Uncertainty curves for the N4433A are created using a 2-port calibration. Multiport uncertainties are not supported at this time.

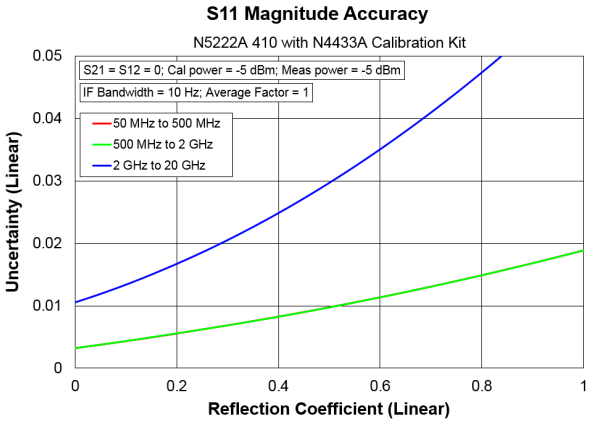
Description	Specification (dB)				
	10 MHz to 50 MHz	50 MHz to 500 MHz	500 MHz to 2 GHz	2 GHz to 13.5 GHz	13.5 GHz to 20 GHz
Directivity	45	52	52	45	45
Source Match	36	42	42	37	31
Load Match	36	42	42	36	30
Reflection Tracking					
Mag	0.100	0.060	0.060	0.100	0.180
Phase	0.660	0.396	0.396	0.660	1.188
Transmission Tracking					
Mag	0.103	0.061	0.061	0.108	0.190
Phase	0.680	0.403	0.405	0.710	1.251

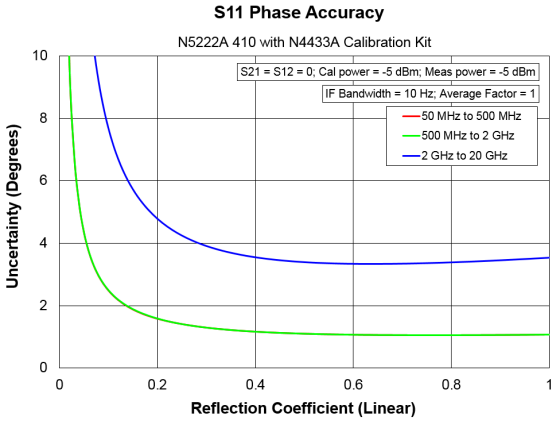
Transmission Uncertainty





Reflection Uncertainty





Uncorrected System Performance

Specifications apply to following conditions:

- Cable loss not included in Transmission Tracking.
- Crosstalk measurement conditions: normalized to a thru, measured with shorts on all ports, 10 Hz IF bandwidth, averaging factor of 8, alternate mode, source power set to the specified maximum power.

Table 3a. Error Terms (dB), All Ports, Option 210, 410 - Specifications

Description	Directivity	Source Match	Load Match	Transmission Tracking	Reflection Tracking	Crosstalk
10 MHz to 50 MHz	16	31	24	--	--	--
50 MHz to 500 MHz	24	28	26	--	--	--
500 MHz to 3.2 GHz	24	25	24	--	--	--
3.2 GHz to 8 GHz	23	23	21	--	--	--
8 GHz to 10 GHz	23	18	17	--	--	--
10 GHz to 13.5 GHz	16	17	16	--	--	--
13.5 GHz to 16 GHz	16	17	15	--	--	--
16 GHz to 20 GHz	16	15	14	--	--	--
20 GHz to 24 GHz	16	12	12	--	--	--
24 GHz to 26.5 GHz	16	10	10	--	--	--

Table 3b. Error Terms (dB), All Ports, Option 210, 410 - Typical

Description	Directivity	Source Match	Load Match	Transmission Tracking	Reflection Tracking	Crosstalk
10 MHz to 50 MHz	23	37	28	+/- 1.5	+/- 1.5	-84
50 MHz to 100 MHz	29	33	32	+/- 1.5	+/- 1.5	-90
100 MHz to 500 MHz	29	33	32	+/- 1.5	+/- 1.5	-110
500 MHz to 3.2 GHz	31	30	29	+/- 1.5	+/- 1.5	-120
3.2 GHz to 8 GHz	25	27	24	+/- 1.5	+/- 1.5	-122
8 GHz to 10 GHz	25	23	22	+/- 1.5	+/- 1.5	-122
10 GHz to 13.5 GHz	21	22	21	+/- 1.5	+/- 1.5	-122
13.5 GHz to 16 GHz	20	21	20	+/- 1.5	+/- 1.5	-122
16 GHz to 20 GHz	20	19	18	+/- 1.5	+/- 1.5	-122
20 GHz to 24 GHz	19	17	17	+/- 1.5	+/- 1.5	-117
24 GHz to 26.5 GHz	20	15	15	+/- 1.5	+/- 1.5	-114

Test Port Output

See Block diagrams on page 39.

Table 4. Frequency Information, Option 210, 410

Description	Specification	Typical
N5221B Frequency Range	10 MHz to 13.5 GHz	--
N5222B Frequency Range	10 MHz to 26.5 GHz	--
Frequency Resolution	1 Hz	--
Frequency Accuracy	+/- 0.7 ppm	--
Initial Frequency Accuracy[1]	±0.2 ppm	±0.1 ppm
Frequency Stability	--	+/-0.05 ppm, -10° to 70° C[2] +/-0.5 ppm (first year)[3]

[1] Verified after Factory Frequency Reference adjustment, or after adjustment at a Keysight Service Center.

[2] Assumes no variation in time.

[3] Assumes no variation in temperature.

Table 5. Maximum Leveled Power (dBm), Option 210, 410

Description	Specification		Typical	
	Port 1, Port 3	Port 2, Port 4	Port 1, Port 3	Port 2, Port 4
10 MHz to 50 MHz	6	6	14	11
50 MHz to 500 MHz	7	7	14	12
500 MHz to 1 GHz	7	7	14	14
1 GHz to 3.2 GHz	7	7	13	14
3.2 GHz to 10 GHz	7	7	16	15
10 GHz to 13.5 GHz	7	7	16	13
13.5 GHz to 16 GHz	7	7	14	12
16 GHz to 20 GHz	7	6	14	10
20 GHz to 24 GHz	7	4	12	8
24 GHz to 26.5 GHz	1	-1	8	5

Table 6. Power Level Accuracy (dB), Option 210, 410

Description	Specification	Typical
10 MHz to 50 MHz	+/- 1.5	+/- 0.5
50 MHz to 500 MHz	+/- 1.0	+/- 0.2
500 MHz to 3.2 GHz	+/- 1.0	+/- 0.1
3.2 GHz to 10 GHz	+/- 1.0	+/- 0.2
10 GHz to 13.5 GHz	+/- 1.2	+/- 0.2
13.5 GHz to 18 GHz	+/- 2.0	+/- 0.3
18 GHz to 26.5 GHz	+/- 2.5	+/- 0.4

Table 7a. Power Level Linearity (dB)

Option 210, 410 - Specification

Description	Port 1 or 3[1] -25dBm ≤ P<-20dBm	Port 1 or 3[1] -20dBm ≤ P<-15dBm	Port 1 or 3[1] P ≥-15dBm
10 MHz to 50 MHz	+/-2.0	+/-1.5	+/-1.5
50 MHz to 26.5 GHz	+/-1.5	+/-1.5	+/-1.5

[1] Either port can be used as the source port.

Table 7b. Power Level Linearity (dB)

Option 210, 410 - Specification

Description	Port 2 or 4[1] -25dBm ≤ P<-20dBm	Port 2 or 4[1] -20dBm ≤ P<-15dBm	Port 2 or 4[1] P ≥-15dBm
10 MHz to 50 MHz	+/-2.5	+/-1.5	+/-1.5
50 MHz to 500 MHz	+/-2.0	+/-1.5	+/-1.5
500 MHz to 26.5 GHz	+/-1.5	+/-1.5	+/-1.5

[1] Either port can be used as the source port.

Table 8. Power Sweep Range (dB), Option 210, 410

Description	Specification		Typical	
	Port 1, Port 3	Port 2, Port 4	Port 1, Port 3	Port 2, Port 4
10 MHz to 50 MHz	31	31	41	38
50 MHz to 500 MHz	32	32	41	39
500 MHz to 1 GHz	32	32	41	41
1 GHz to 3.2 GHz	32	32	40	41
3.2 GHz to 10 GHz	32	32	43	42
10 GHz to 13.5 GHz	32	32	43	40
13.5 GHz to 16 GHz	32	32	41	39
16 GHz to 20 GHz	32	31	41	37
20 GHz to 24 GHz	32	29	39	35
24 GHz to 26.5 GHz	26	24	35	32

Table 9. Nominal (Preset) Power (dBm)

Description	Option 210, 410
Preset Power	-5

Table 10. Power Resolution and Maximum/Minimum Settable Power

Option 210, 410

Description	Specification (dB)	Typical (dBm)
Power Resolution	0.01	--
Maximum Settable Power	--	30
Minimum Settable Power	--	-30

Table 11. 2nd and 3rd Harmonics at Max Specified Power (dBc)

Option 210, 410

Listed frequency is fundamental frequency; test at max specified power.

Description	2nd Harmonic	3rd Harmonic
10 MHz to 50 MHz	-16	-15
50 MHz to 2 GHz	-23	-17
2 GHz to 8.85 GHz	-17	-19
8.85 GHz to 13.25 GHz	-17	--

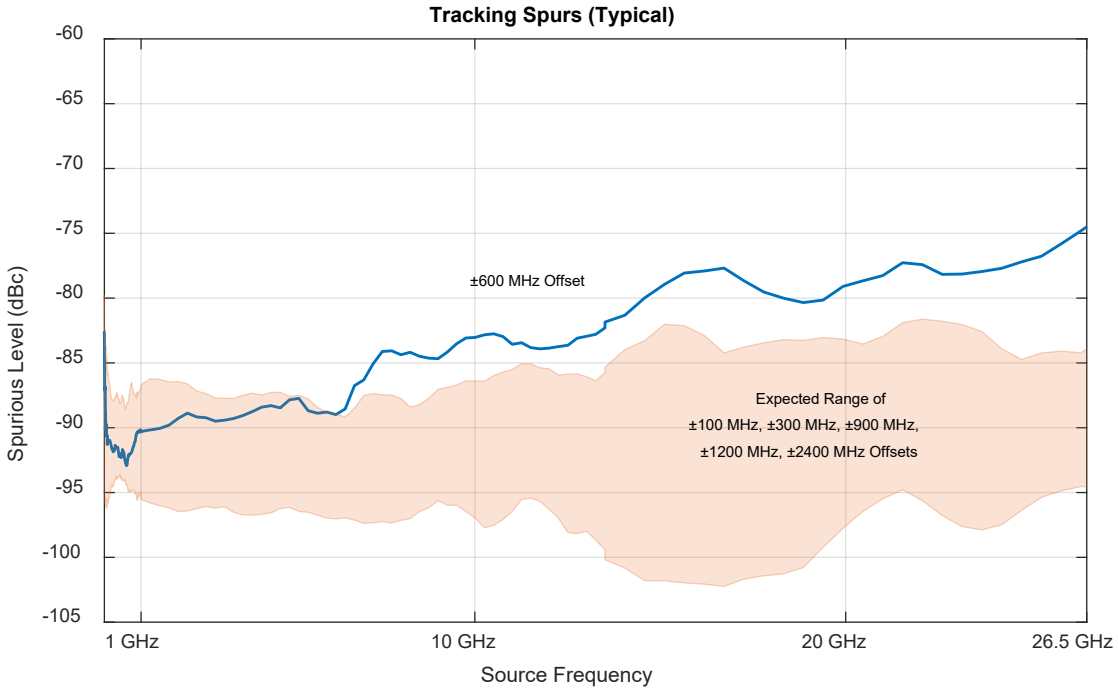
Table 12. Non-Harmonic Spurs at Nominal Power (dBc),

Listed frequency is Source CW frequency, tested at 0 dBm.

Description	Non-Harmonic	±600 MHz Tracking Spur
10 MHz to 1 GHz	-80	-80
1 GHz to 10 GHz	-85	-81
10 GHz to 20 GHz	-82	-75
20 GHz to 26.5 GHz	-80	-70

[1] Non-harmonic spurs are negligible with Option 425 installed and LFE enabled.

Tracking Spurs (Linear Frequency Scale)



Tracking Spurs (Logarithmic Frequency Scale)

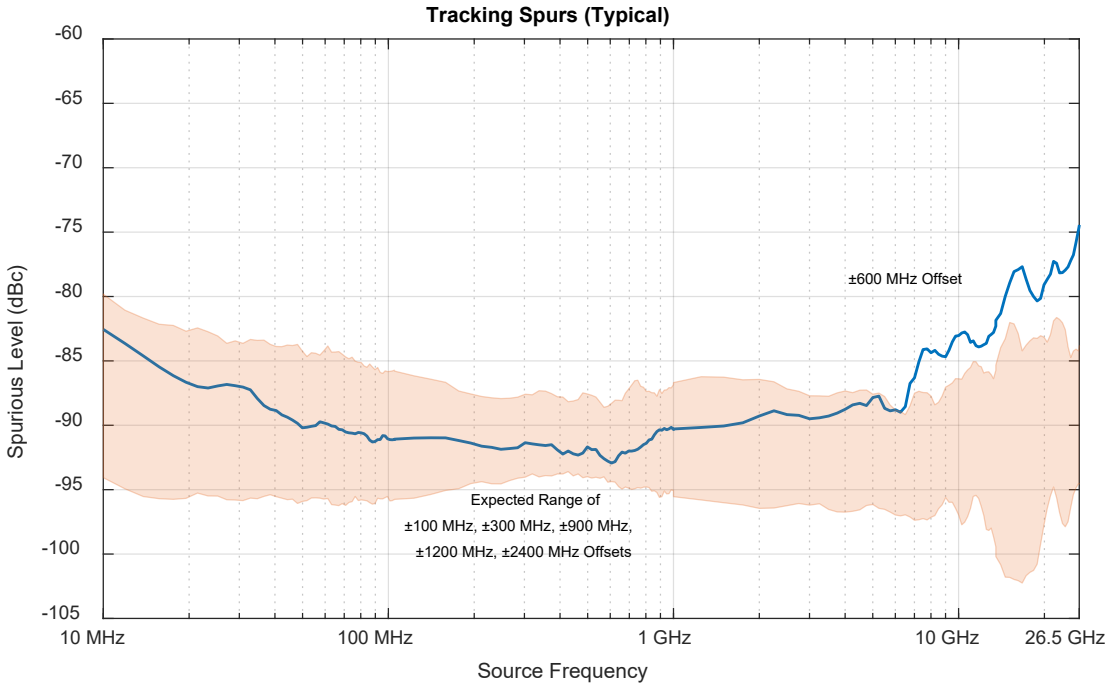


Table 13a. Phase Noise (dBc/Hz), Option 210, 410, with UNY, Port 1, 3 – Typical

CW Frequency	100 Hz Offset	1 kHz Offset	10 kHz Offset	100 kHz Offset	1 MHz Offset	10 MHz Offset
1 GHz	-112	-132	-137	-143	-145	-144
5 GHz	-103	-123	-132	-135	-147	-150
10 GHz	-96	-116	-126	-130	-142	-146
20 GHz	-91	-111	-118	-123	-135	-139
26.5 GHz	-87	-106	-115	-121	-131	-135

Table 13b. Phase Noise (dBc/Hz), Option 210, 410, with UNY, Port 2, 4 – Typical

CW Frequency	100 Hz Offset	1 kHz Offset	10 kHz Offset	100 kHz Offset	1 MHz Offset	10 MHz Offset
1 GHz	-111	-121	-132	-145	-149	-149
5 GHz	-103	-122	-128	-134	-145	-149
10 GHz	-96	-112	-120	-127	-140	-147
20 GHz	-90	-108	-116	-123	-134	-139
26.5 GHz	-86	-106	-114	-121	-131	-135

Phase Noise with Option UNY

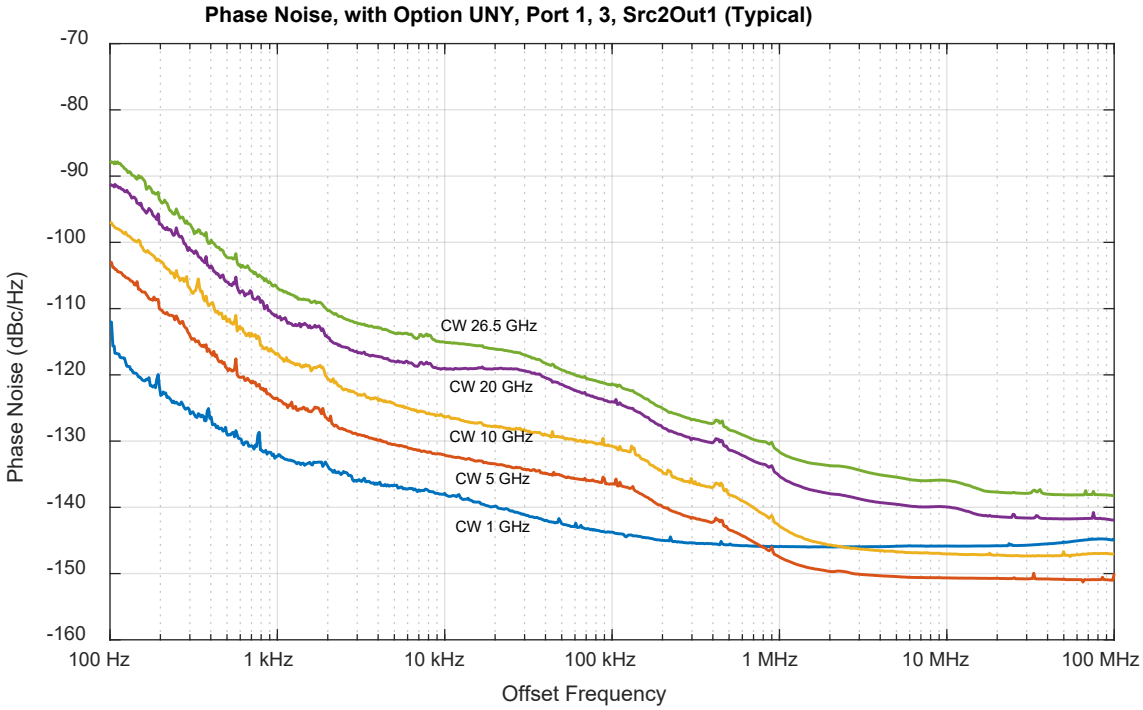
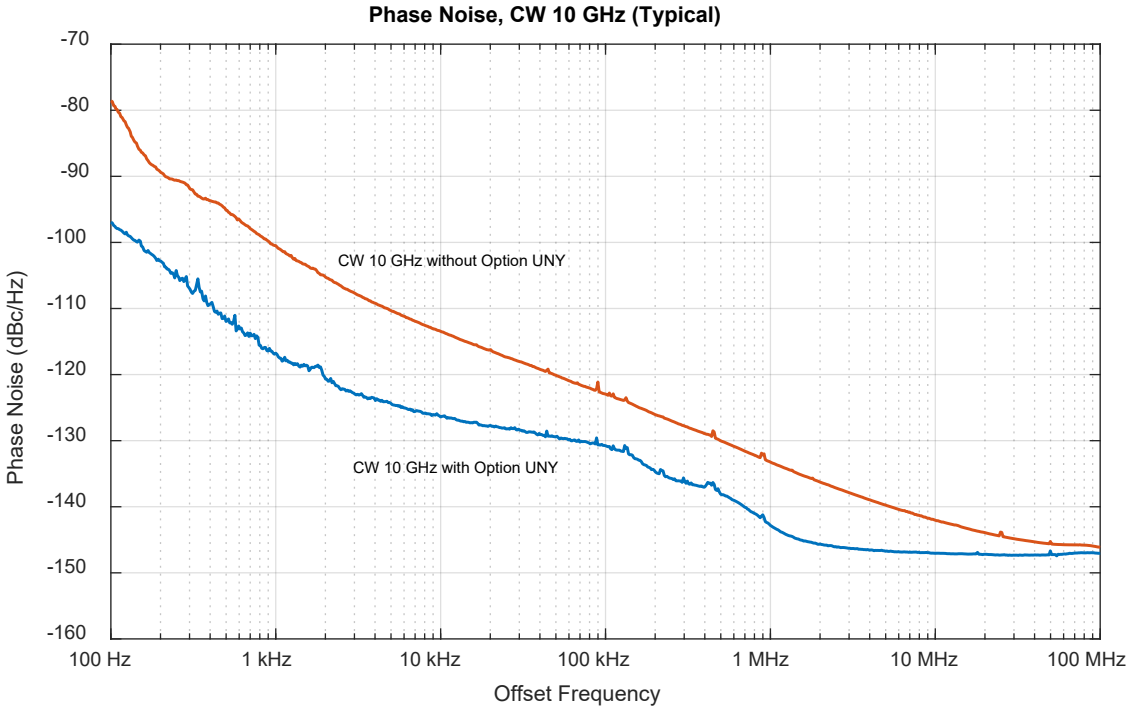


Table 13c. Phase Noise (dBc/Hz), Option 210, 410, without UNY, All Ports - Typical

CW Frequency	100 Hz Offset	1 kHz Offset	10 kHz Offset	100 kHz Offset	1 MHz Offset	10 MHz Offset
1 GHz	-94	-116	-130	-141	-145	-146
5 GHz	-83	-106	-119	-128	-139	-147
10 GHz	-78	-100	-113	-122	-133	-142
20 GHz	-72	-94	-107	-116	-127	-135
26.5 GHz	-67	-90	-104	-114	-124	-132

Phase Noise, CW 10 GHz, with Option UNY and without Option UNY



Test Port Input

Table 14. Test Port Noise Floor (dBm) @ 10 Hz IFBW, Option 210, 410

Total average (rms) noise power calculated as the mean value of a linear magnitude trace expressed in dBm. May typically be degraded at particular frequencies below 500 MHz due to spurious receiver residuals.

Description	Specification	Typical
10 MHz to 50 MHz	-82	-88
50 MHz to 100 MHz	-95	-103
100 MHz to 500 MHz	-105	-110
500 MHz to 2 GHz	-114	-118
2 GHz to 13.5 GHz	-114	-119
13.5 GHz to 20 GHz	-114	-120
20 GHz to 24 GHz	-111	-118
24 GHz to 26.5 GHz	-107	-116

Table 15. 0.1 dB Receiver Compression at Test Port (dBm)

Option 210, 410

Description	Typical
10 MHz to 100 MHz	15
100 MHz to 20 GHz	12
20 GHz to 26.5 GHz	10

Table 16. Receiver Compression at Test Port Power - Specification

Description	Test Port Power (dBm)	Receiver Compression	
	Option 210, 410	Magnitude (dB)	Phase (degrees)
10 MHz to 500 MHz1	--	--	--
500 MHz to 3.2 GHz	8	0.12	0.65
3.2 GHz to 10 GHz	8	0.12	0.85
10 GHz to 13.5 GHz	8	0.15	1.40
13.5 GHz to 16 GHz	8	0.18	1.45
16 GHz to 20 GHz	8	0.12	0.70
20 GHz to 24 GHz	8	0.24	1.70
24 GHz to 26.5 GHz	8	0.42	2.50

[1]Test port receiver compression at specified input levels below 500 MHz due to coupler roll off in this frequency range.

Table 17. Trace Noise Magnitude (dB rms)

Ratioed measurement, nominal power at test port.

Description	Specification	Typical		
	1 kHz IFBW	1 kHz IFBW	100 kHz IFBW	600 kHz IFBW
10 MHz to 100 MHz	0.007	0.0036	0.053	0.103
100 MHz to 13.5 GHz	0.002	0.0005	0.004	0.010
13.5 GHz to 16 GHz	0.002	0.0003	0.003	0.007
16 GHz to 22.5 GHz	0.002	0.0005	0.003	0.007
22.5 GHz to 24 GHz	0.003	0.0008	0.004	0.011
24 GHz to 26.5 GHz	0.005	0.0012	0.007	0.017

Table 18. Trace Noise Phase (deg rms)

Ratioed measurement, nominal power at test port.

Description	Specification	Typical		
	1 kHz IFBW	1 kHz IFBW	100 kHz IFBW	600 kHz IFBW
10 MHz to 100 MHz	0.051	0.0237	0.341	0.663
100 MHz to 13.5 GHz	0.015	0.0045	0.027	0.067
13.5 GHz to 16 GHz	0.042	0.0045	0.019	0.042
16 GHz to 22.5 GHz	0.042	0.0075	0.024	0.050
22.5 GHz to 24 GHz	0.054	0.0080	0.031	0.073
24 GHz to 26.5 GHz	0.054	0.0128	0.049	0.118

Table 19. Reference Level Magnitude, Option 210, 410 - Specification

Description	Magnitude (dB)	Phase (degrees)
Range	+/- 500	+/- 500
Resolution	0.001	0.01

Table 20. Stability vs. Temperature, Option 210, 410 - Typical

Description	Magnitude (dB/°C)	Phase (°/°C)
10 MHz to 50 MHz	0.010	0.180
50 MHz to 500 MHz	0.010	0.060
500 MHz to 3.2 GHz	0.010	0.080
3.2 GHz to 10 GHz	0.020	0.130
10 GHz to 13.5 GHz	0.020	0.160
13.5 GHz to 16 GHz	0.020	0.300
16 GHz to 20 GHz	0.020	0.400
20 GHz to 24 GHz	0.030	0.500
24 GHz to 26.5 GHz	0.030	0.560

Table 21. Stability vs. Time, Option 210, 410 – Specification

The specifications below are observations of the maximum drift performance over +/- 0.5 °C and a period of 24 hours subsequent to a 48 hour warm up period, with ideal load for reflections and ideal thru for transmission measurements.

Description	Magnitude (dB/24 hours)		
	S11, S22, S33, S44	S21, S12, S43, S34	S31, S13, S41, S14, S42, S24, S32, S23
10 MHz to 12 MHz[1]	-52	0.013	0.018
12 MHz to 500 MHz[1]	-60	0.013	0.018
500 MHz to 3.2 GHz	-65	0.010	0.014
3.2 GHz to 10 GHz	-65	0.016	0.023
10 GHz to 13.5 GHz	-65	0.020	0.028
13.5 GHz to 16 GHz	-65	0.025	0.035
16 GHz to 20 GHz	-65	0.026	0.037
20 GHz to 24 GHz	-65	0.029	0.041
24 GHz to 26.5 GHz	-65	0.032	0.045

[1] Performance may be degraded at frequencies below 500 MHz due to spurious receiver residuals.

Table 22. Damage Input Level

Description	Option 200, 201, 219, 400, 401, 419	Option 217, 417
RF, DC	30 dBm, 40 V	30 dBm, 7 V

Phase Noise Measurement Performance (with S930317B phase noise measurement application)[1]

- Offset frequency range: 0.1 Hz to 10 MHz
- Sweep speed (typical): 34 seconds (1 Hz to 10 MHz offset in Normal mode)

Table 23a. Absolute Phase Noise Sensitivity (dBc/Hz), in Best mode - Supplemental Performance Data [2]

Input Power level: +5 dBm (-5 dBm at 50 GHz)

Phase Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-55	-88	-112	-127	-132	-134	-134	-134	-134
10 GHz	-36	-69	-97	-117	-124	-130	-132	-136	-137
20 GHz	-31	-64	-90	-111	-120	-124	-125	-132	-134
40 GHz	-23	-58	-84	-104	-113	-117	-123	-125	-129
50 GHz	-18	-54	-81	-100	-109	-113	-116	-121	-123

Table 23b. Absolute AM Noise Sensitivity (dBc/Hz), in Best mode - Supplemental Performance Data

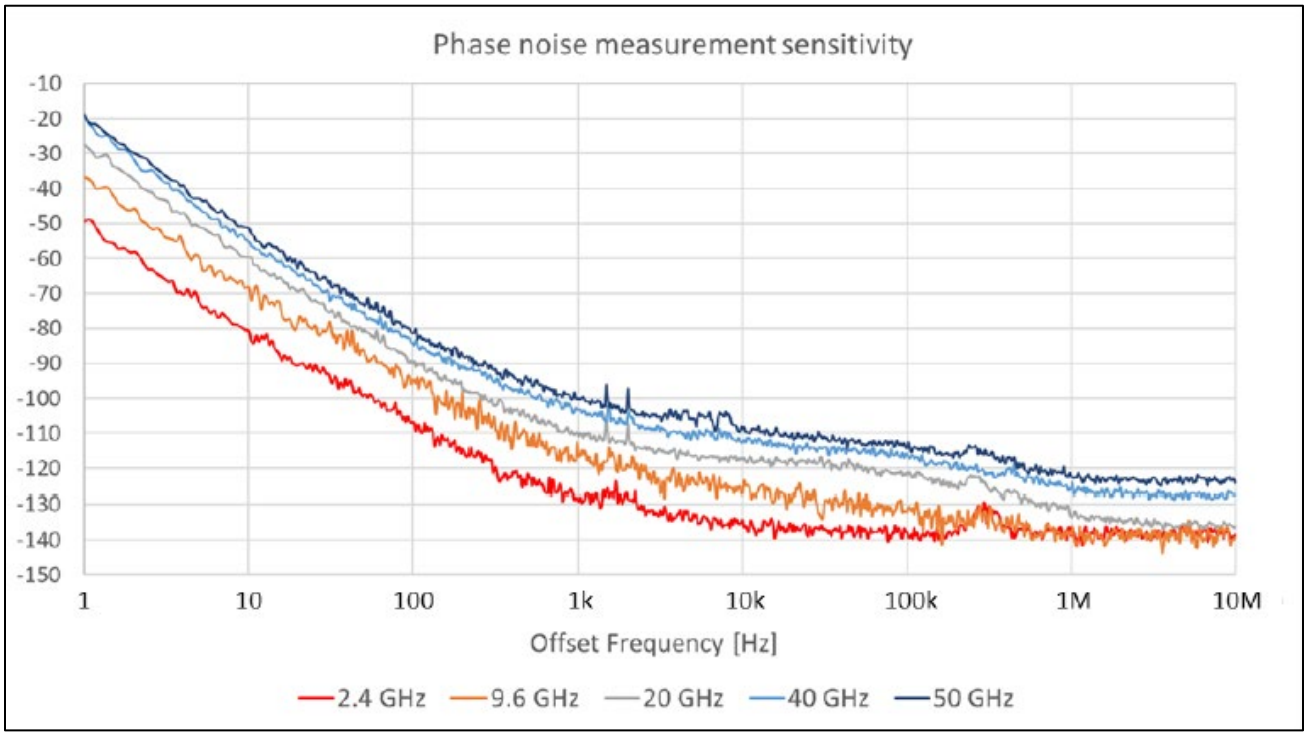
Input Power level: +5 dBm (-5 dBm at 50 GHz)

AM Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-96	-104	-110	-119	-128	-132	-132	-135	-137
10 GHz	-97	-104	-110	-118	-128	-134	-135	-138	-139
20 GHz	-96	-104	-112	-116	-125	-132	-129	-136	-136
40 GHz	-93	-102	-109	-114	-123	-127	-130	-130	-132
50 GHz	-91	-96	-105	-114	-119	-122	-117	-125	-122

[1] The input frequency in the sensitivity tables in this section is limited to the highest frequency of the PNA model.

[2] For embedded-LO frequency converters, the frequency used for the table should be that of the embedded-LO.

Absolute Phase Noise Sensitivity (dBc/Hz) - Supplemental Performance Data



Single-Channel Residual Noise Measurement [3]

Table 23c. Single-Channel Residual Phase Noise Sensitivity (dBc/Hz), with Option UNY in Best mode - Supplemental Performance Data

Input Power level: +10 dBm (+6 dBm at 40 GHz, -2 dBm at 50 GHz)

Residual Phase Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-102	-110	-115	-123	-131	-136	-136	-137	-137
10 GHz	-92	-99	-105	-113	-122	-131	-134	-139	-140
20 GHz	-83	-93	-100	-112	-118	-125	-124	-132	-136
40 GHz	-78	-85	-93	-106	-110	-122	-126	-128	-129
50 GHz	-75	-81	-91	-102	-110	-120	-119	-125	-125

Table 23d. Single-Channel Residual AM Noise Sensitivity (dBc/Hz), with Option UNY in Best mode - Supplemental Performance Data

Input Power level: +10 dBm (+6 dBm at 40 GHz, -2 dBm at 50 GHz)

Residual AM Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-97	-104	-112	-120	-117	-128	-130	-134	-138
10 GHz	-97	-106	-111	-120	-121	-129	-134	-137	-142
20 GHz	-99	-104	-112	-120	-123	-120	-123	-130	-136
40 GHz	-96	-102	-110	-117	-118	-128	-132	-131	-133
50 GHz	-91	-99	-109	-114	-120	-125	-118	-125	-127

[3] The data is when a THRU device is connected, in other words, when the input and output frequencies are the same. The single-channel residual noise measurement is used for the phase noise measurements for frequency converting devices, and the sensitivity is determined as the absolute phase noise sensitivity at the embedded-LO frequency (Table 23a). In the single-channel residual noise measurement mode, the phase or AM noise of the DUT input signal is not measured. The measurement can be done when the phase or AM noise of the signal generated by the DUT is larger than that of the DUT input signal supplied by the PNA internal signal source.

Single-Channel Residual Phase Noise Sensitivity (dBc/Hz) - Supplemental Performance Data

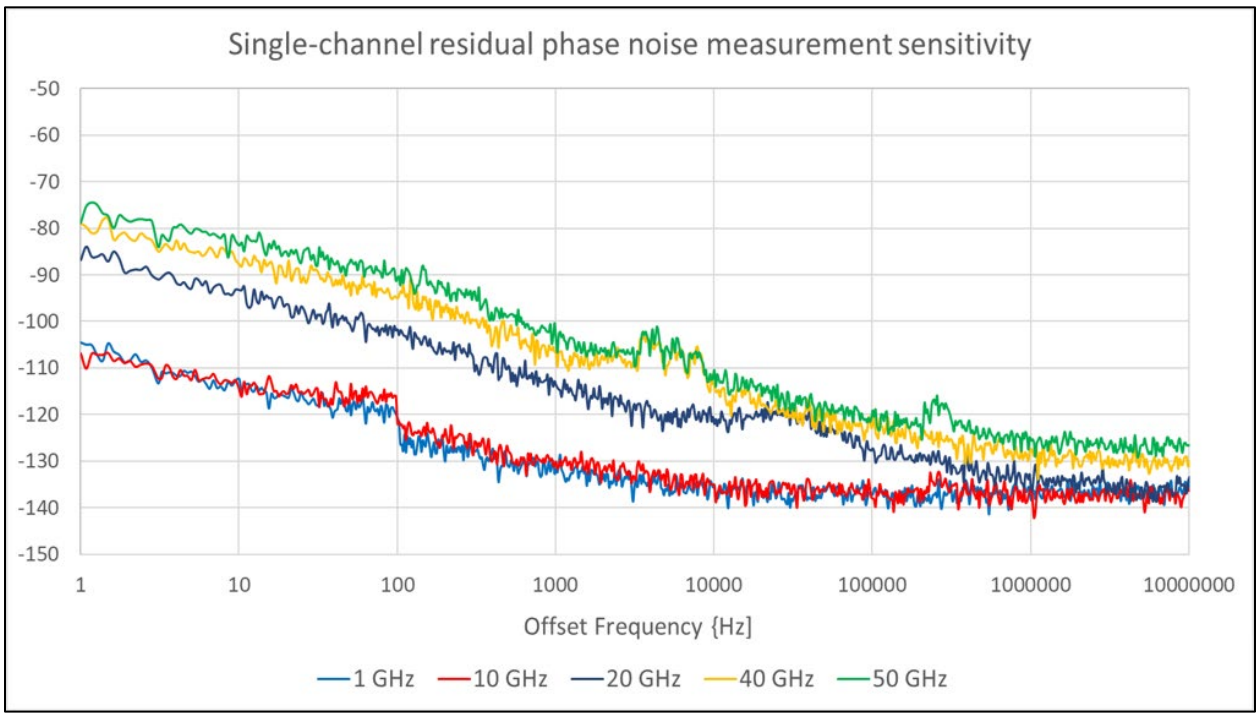


Table 23e. Single-Channel Residual Phase Noise Sensitivity (dBc/Hz), without Option UNY in Best mode - Supplemental Performance Data [4]

Input Power level: +10 dBm (+6 dBm at 40 GHz, 0 dBm at 50 GHz)

Residual Phase Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-94	-95	-100	-120	-133	-136	-138	-137	-136
10 GHz	-77	-78	-83	-102	-114	-124	-129	-135	-138
20 GHz	-70	-74	-75	-95	-109	-118	-127	-128	-133
40 GHz	-64	-67	-70	-92	-102	-112	-116	-121	-128
50 GHz	-61	-63	-66	-87	-101	-109	-113	-119	-125

Table 23f. Single-Channel Residual AM Noise Sensitivity (dBc/Hz), without Option UNY in Best mode - Supplemental Performance Data

Input Power level: +10 dBm (+6 dBm at 40 GHz, 0 dBm at 50 GHz)

Residual AM Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-105	-110	-123	-125	-134	-138	-139	-143	-138
10 GHz	-104	-110	-116	-125	-133	-134	-133	-141	-141
20 GHz	-104	-109	-116	-127	-127	-126	-128	-133	-137
40 GHz	-103	-110	-115	-125	-120	-130	-126	-130	-132
50 GHz	-98	-106	-111	-121	-124	-125	-122	-129	-129

4 The data is when a THRU device is connected, in other words, when the input and output frequencies are the same. The single-channel residual noise measurement is used for the phase noise measurements for frequency converting devices, and the sensitivity is determined as the absolute phase noise sensitivity at the embedded-LO frequency (Table 64). In the single-channel residual noise measurement mode, the phase or AM noise of the DUT input signal is not measured. The measurement can be done when the phase or AM noise of the signal generated by the DUT is larger than that of the DUT input signal supplied by the PNA internal signal source.

2-Channel Residual Noise Measurement for Non-Frequency Converting Devices[5]

Table 23g. 2-Channel Residual Phase Noise Sensitivity (dBc/Hz) in Best mode - Supplemental Performance Data

Input Power level: +10 dBm (+6 dBm at 40 GHz, -3 dBm at 50 GHz)

Residual Phase Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-107	-116	-127	-134	-132	-134	-131	-133	-132
10 GHz	-101	-110	-120	-126	-134	-135	-134	-138	-136
20 GHz	-99	-105	-113	-120	-128	-131	-129	-134	-135
40 GHz	-93	-101	-109	-115	-121	-124	-125	-126	-127
50 GHz	-90	-99	-106	-113	-117	-118	-119	-120	-120

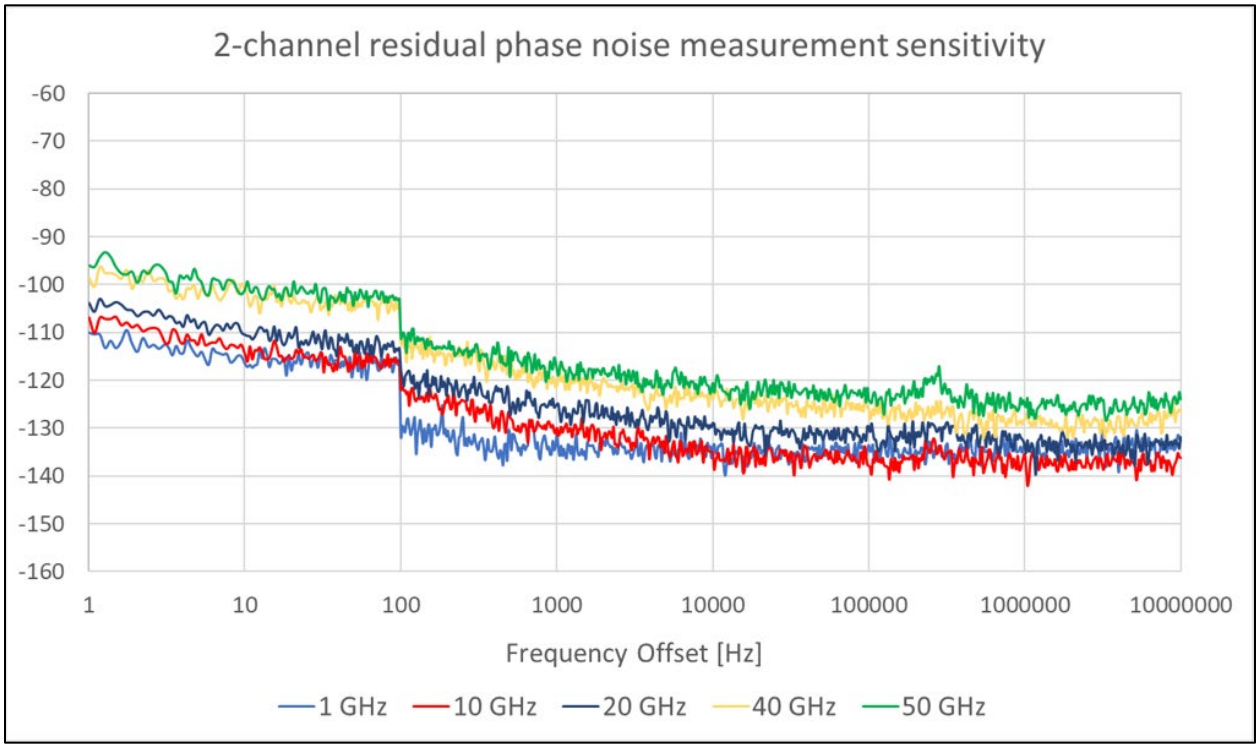
Table 23h. 2-Channel Residual AM Noise Sensitivity (dBc/Hz) in Best mode - Supplemental Performance Data

Input Power level: +10 dBm (+6 dBm at 40 GHz, -3 dBm at 50 GHz)

Residual AM Noise	Offset Frequency								
Input Frequency	1 Hz	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	300 kHz	1 MHz	10 MHz
1 GHz	-99	-106	-109	-120	-131	-133	-138	-137	-134
10 GHz	-95	-105	-113	-121	-130	-134	-136	-139	-139
20 GHz	-96	-104	-113	-120	-129	-128	-129	-136	-136
40 GHz	-98	-107	-112	-118	-120	-127	-129	-130	-131
50 GHz	-93	-102	-110	-115	-120	-121	-119	-122	-122

[5] Both the phase or AM noise of the DUT input signal and that of the DUT output signal are measured.

2-Channel Residual Phase Noise Sensitivity (dBc/Hz) - Supplemental Performance Data



Dynamic Accuracy

Dynamic accuracy is verified with the following measurements:

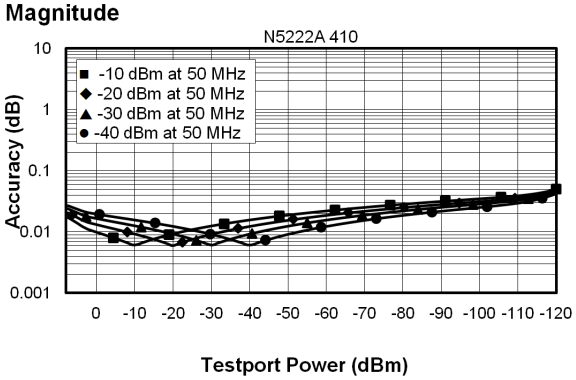
Compression over frequency

IF linearity at a single frequency of 99.6 MHz or 1.998765 GHz using a reference level of -20 dBm for an input power range of 0 to -60 dBm. For values below -60 dBm, refer to VNA Receiver Dynamic Accuracy Specifications and Uncertainties.

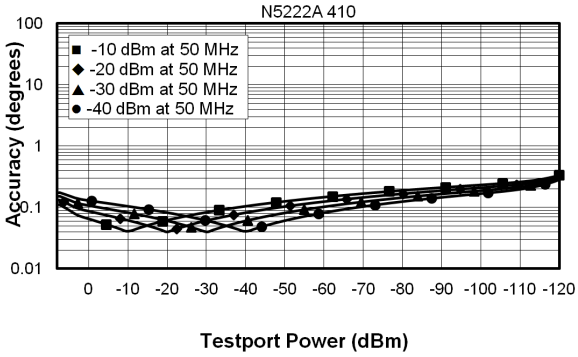
Table 24. N5221B and N5222B Dynamic Accuracy

N5221B/22B Dynamic Accuracy, 50 MHz - Specification

Magnitude

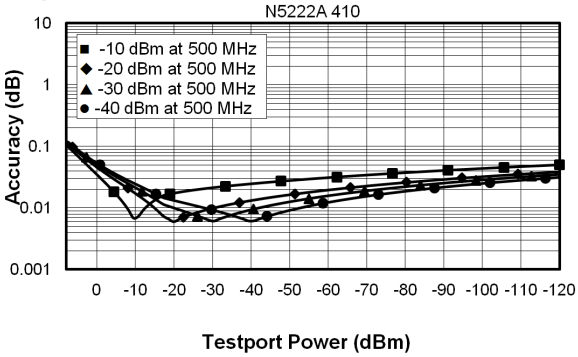


Phase

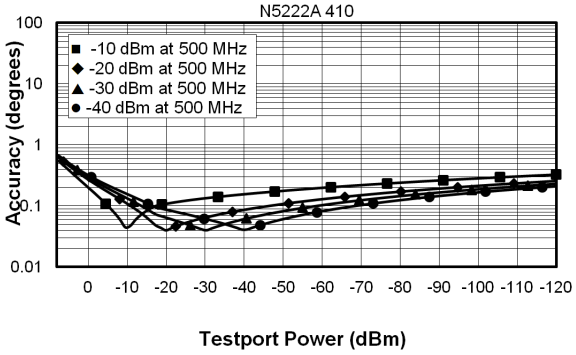


N5221B/22B Dynamic Accuracy, 500 MHz - Specification

Magnitude

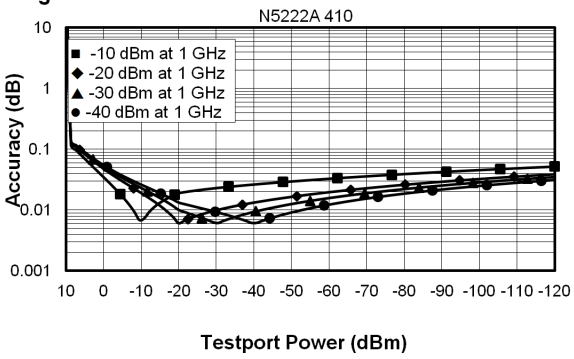


Phase

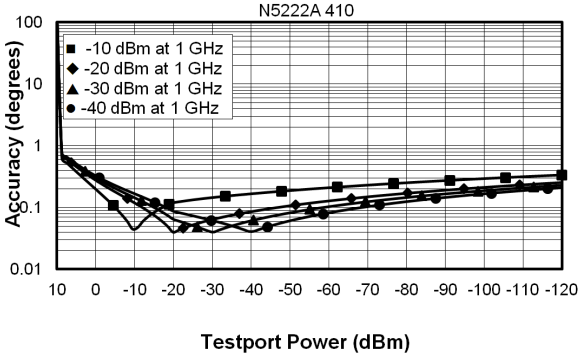


N5221B/22B Dynamic Accuracy, 1 GHz - Specification

Magnitude

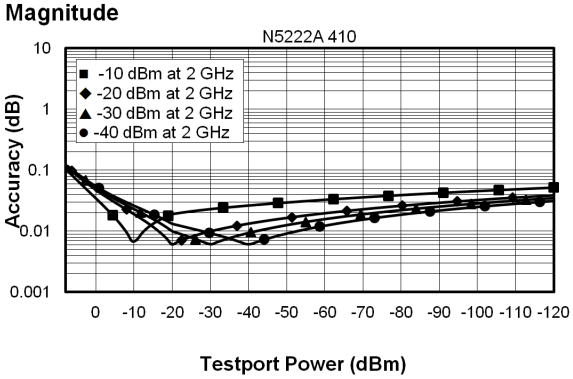


Phase

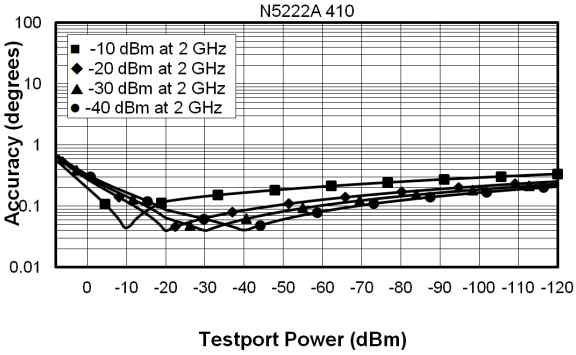


N5221B/22B Dynamic Accuracy, 2 GHz - Specification

Magnitude

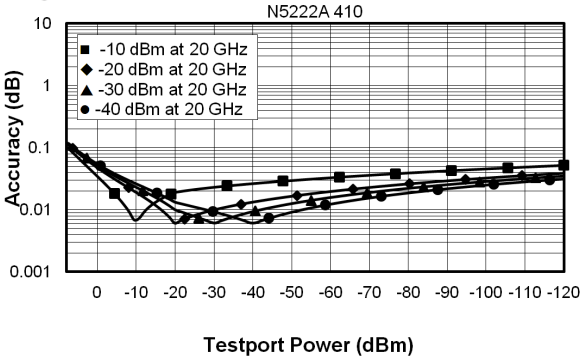


Phase

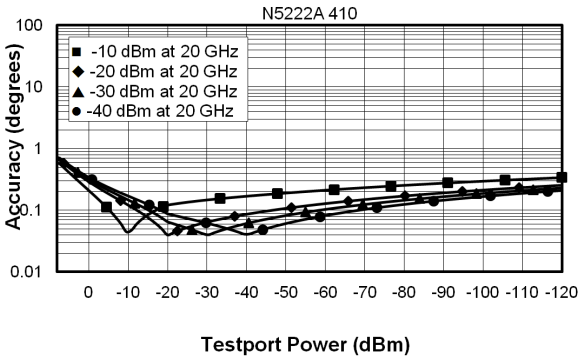


N5221B/22B Dynamic Accuracy, 20 GHz - Specification

Magnitude

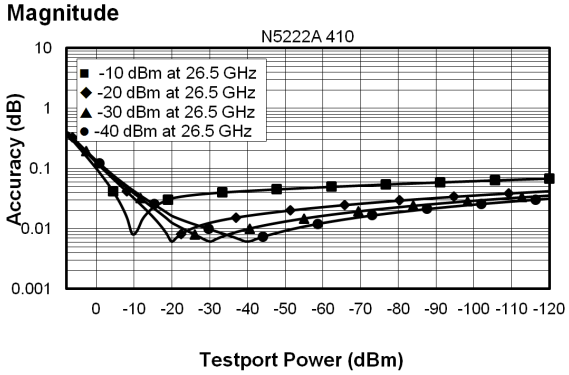


Phase



N5221B/22B Dynamic Accuracy, 26.5 GHz – Specification

Magnitude



Phase

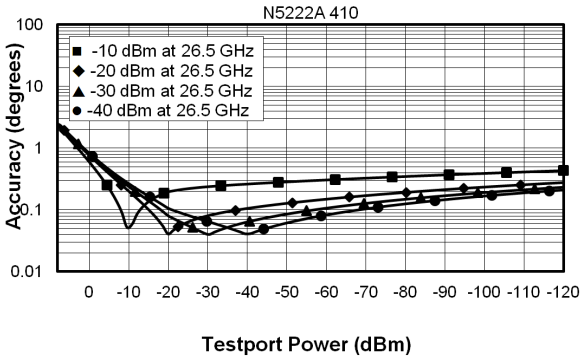


Table 25. Group Delay - Typical

Group delay is computed by measuring the phase change within a specified frequency step (determined by the frequency span and the number of points per sweep). In general, the following formula can be used to determine the accuracy, in seconds, of specific group delay measurement:

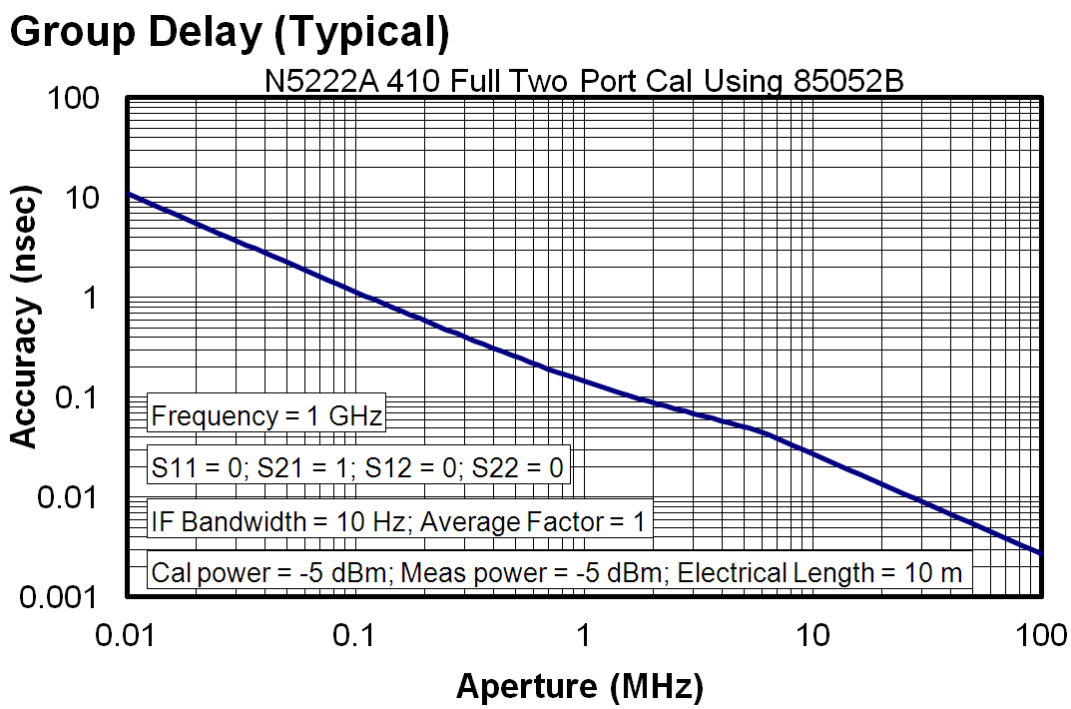
±Phase Accuracy (deg)/[360 × Aperture (Hz)]

Depending on the aperture and device length, the phase accuracy used is either incremental phase accuracy or worst-case phase accuracy

Description	Typical Performance
Aperture (selectable)	(frequency span)/(number of points -1)
Maximum Aperture	20% of frequency span
Range	0.5 x (1/minimum aperture)
Maximum Delay	Limited to measuring no more than 180° of phase change within the minimum aperture.)

The following graphs show characteristic group delay accuracy with full 2-port calibration and a 10 Hz IF bandwidth. Insertion loss is assumed to be < 2 dB and electrical length to be ten meters.

For any S_{ij} Group Delay measurement, $S_{ii} = 0$, $S_{ij} = 1$, $S_{ji} = 0$, $S_{kl} = 0$ for all kl ≠Iij



General Information

- Miscellaneous Information
- Front Panel
- Rear Panel
- Environment and Dimensions

Table 26. Miscellaneous Information

Description	Supplemental Information	
System IF Bandwidth Range	1 Hz to 15 MHz, nominal	
CPU	For the latest information on CPUs and associated hard drives, visit: PNA Hard Drives and CPUs (keysight.com)	
LXI	CPU version 7.0, 8.0	CPU version 9.0
	Class C	LXI 1.5 Extended Functions: HiSLIP; VSI-11 Discovery and Identification

Table 27. Front Panel Information, Option 210, 410

Description	Typical Performance
RF Connectors	
Type	3.5 mm (male), 50 ohm, (nominal)
Center Pin Recession	0.002 in. (characteristic)
USB 2.0 Ports - Primary (4 ports)	
Standard	Compatible with USB 2.0
Connector	USB Type-A female
Display	
Size	31 cm (12.1 in) diagonal color active matrix LCD; 1280 (horizontal) X 800 (vertical) resolution
Refresh Rate	Vertical 60 Hz; Horizontal 49.31 kHz

Pixels	Any of the following would cause a display to be considered faulty: • A complete row or column consists of “stuck” or “dark” pixels. • More than six “stuck on” pixels (but not more than three green) or more than 0.002% of the total pixels are within the LCD specifications. • More than twelve “dark” pixels (but no more than seven of the same color) or more than 0.004% of the total pixels are within the LCD specifications. • Two or more consecutive "stuck on" pixels or three or more consecutive "dark" pixel (but no more than one set of two consecutive dark pixels). • “Stuck on” pixels or more than two “dark” pixels less than 6.5 mm apart (excluding consecutive pixels).

Table 27. (Continued) Front Panel Information, Option 210, 410

Description	Typical Performance
Display Range	
Magnitude	+/-2500 dB (at 500 dB/div), max
Phase	+/-2500° (at 500 degrees/div), max
Polar	10 pUnits, min 10,000 Units, max
Display Resolution	
Magnitude	0.001 dB/div, min
Phase	0.01°/div, min
Marker Resolution	
Magnitude	0.001 dB, min
Phase	0.01°, min
Polar	10 pUnit, min

Table 28. Rear Panel Information, Option 210, 410

Description	Typical Performance
10 MHz Reference In	
Connector	BNC, female
Input Frequency	10 MHz ±1 ppm, 100 MHz ±1 ppm 20 MHz ±1 ppm, 80 MHz ±1 ppm
Input Level	10 MHz: -15 dBm to +20 dBm 100 MHz: -10 dBm to +20 dBm
Input Impedance	50 Ω, nom.

10 MHz Reference Out	
Connector	BNC, female
Output Frequency	10 MHz ±0.7 ppm, 100 MHz ±0.7 ppm
Signal Type	Sine Wave
Output Level	+10 dBm ± 4 dB into 50 Ω
Output Impedance	50 Ω, nominal
Harmonics	<-40 dBc, typical

Table 28. (Continued) Rear Panel Information, Option 210, 410

Description	Typical Performance
External IF Inputs	
Function	Allows use of external IF signals from remote mixers, bypassing the PNA's first converters
Connectors	SMA (female); A, B, C, D, R (4-port); A, B, R1, R2 (2-port)
Input Frequency	
Normal IF path	RF < 53 MHz: IF = 826.446 KHz RF >= 53 MHz: IF = 7.438 MHz
Narrowband IF path	IF = 10.70 MHz
Input Impedance	50 Ω
RF Damage Level	+23 dBm
DC Damage Level	5.5 VDC
0.1 dB Compression Point	
Normal IF path	-9.0 dBm at 7.438 MHz
Narrowband IF path	-17 dBm at 10.70 MHz
Pulse Inputs (IF Gates)	
Function	Internal receiver gates used for point-in-pulse and pulse-profile measurements
Connectors	15-pin mini D-sub
Input Impedance	1 K Ohm
Minimum Pulse Width, Source Modulators	20 ns
Minimum Pulse Width, Receiver Gates	20 ns

DC Damage Level	5.5 VDC
Drive Voltage	0 V (off), +3.3 V (on), nominal
RF Pulse Modulator Input (Source Modulator)	
On/Off Ratio	
10 MHz to 3.2 GHz	-64
3.2 GHz to 26.5 GHz	-80
Pulse Period	
Minimum	20 ns
Maximum	70 s

Table 28. (Continued) Rear Panel Information, Option 210, 410

Description	Typical Performance	
Pulse Outputs		
Voltage (TTL)	High: 3.3V to 3.5V Low: <1V	
Impedance	50 Ohm	
External Test Set Driver		
Function	Used for driving remote mixers	
Connections	3.5 mm (female)	
RF Output Frequency Range	3.2 GHz to 19 GHz	
LO Output Frequency Range	0.01 GHz to 26.5 GHz	
Rear Panel LO Power		
	Upper Limit, Typical (dBm)	**Lower Limit, Typical (dBm)**
10 MHz to 1.7 GHz	--	--
1.7 GHz to 16 GHz	0	-10
16 GHz to 21 GHz	4	-6
21 GHz to 26.5 GHz	6	-4

Rear Panel RF Power		
	Upper Limit, Typical (dBm)	**Lower Limit, Typical (dBm)**
3.2 GHz to 19 GHz	-3	-8

Table 28. (Continued) Rear Panel Information, Option 210, 410

Description	Typical Performance	
Trigger Inputs/Outputs	BNC(f), TTL/CMOS compatible	
Test Set IO	25-pin D-Sub connector, available for external test set control	
Power IO	9-pin D-Sub, female; analog and digital IO	
Handler IO	36-pin parallel I/O port; all input/output signals are default set to negative logic; can be reset to positive logic via GPIB command	
Pulse I/O	15-pin D connector provides access to Pulse Modulators and Generators	
GPIB	Two ports - dedicated controller and dedicated talker/listener. 24-pin D-sub (Type D-24), female; compatible with IEEE-488	
CPU Version	**CPU version 7.0, 8.0**	**CPU version 9.0**
PCIe	Cabled PCIe x4 connector is a 4-lane slot	N/A
USB Ports	Two SuperSpeed USB ports (900 mA each), one USB port below LAN connector, and one USB device port. There are also four USB ports (500 mA each) on the front panel. The total current limit for all rear panel USB ports is 2.3 amps. The total current limit for all front panel USB ports is 2 amps.	Four SuperSpeed USB ports (900 mA each) and one USB device port. There are also four USB ports (500 mA each) on the front panel. The total current limit for all rear panel USB ports is 3.6 amps. The total current limit for all front panel USB ports is 2 amps.
USB-C (Host)	N/A	Two USB-C connectors with support for USB-3.1 (max Power Delivery of 5V@1A), Thunderbolt3 (max Power Delivery of 5V@1A)[1], and Display Port (port TB1 only)
LAN	1G port; 10/100/1000 BaseT Ethernet, 8-pin configuration; auto selects between the data rates	1G and 10G ports; 10GBASE-T, Ethernet, 8-pin configuration; auto selects between the data rates. Works with Cat6/Cat7 cable.
VGA Video Output	15-pin mini D-Sub; Drives VGA compatible monitors	N/A
Mini DisplayPort	Miniature DisplayPort connector for connection to external displays	N/A
DisplayPort	N/A	Standard DisplayPort connector for connection to external displays

Line Power	
Frequency, Voltage	50/60/400 Hz for 100 to 120 VAC 50/60 Hz for 220 to 240 VAC
	Power supply is auto switching
Max	575 watts

[1] High power devices require external power supply

Table 29. Analyzer Dimensions and Weight

All models are shipped with handles.

Cabinet Dimensions	Metric (mm)	Imperial (inches)
Height		
Without bottom feet: [1] EIA RU = 6	266.1	10.5
With bottom feet	279.1	11.0
Width		
Without handles or rack-mount flanges	425.6	16.8
With handles, without rack-mount flanges	458.7	18.1
With handles and rack-mount flanges	482.9	19.0
Depth		
Without front and rear panel hardware	533.0	21.0
With front and rear panel hardware, handles	578.0	22.7

[1]Electronics Industry Association rack units. 1 RU = 1.75 in.

Weight (nominal)	Net	Shipping
2-port models (Option 200, 201, 217 or 219)	27 kg (60 lb)	43 kg (95 lb)
4-port models (Option 400, 401, 417 or 419)	36.7 kg (81 lb)	51 kg (112 lb)

Regulatory and Environmental Information

For Regulatory and Environmental information, refer to the PNA Series Installation and Quick Start Guide, located online at: http://literature.cdn.keysight.com/litweb/pdf/E8356-90001.pdf

Measurement Throughput Summary

- Typical Cycle Time for Measurement Completion
- Cycle Time vs. IF Bandwidth
- Cycle Time vs. Number of Points
- Data Transfer Time

Cycle time Includes sweep time, retrace time and band-crossing time. Analyzer display turned off with DISPLAY:ENABLE OFF. Add 21 ms for display on. Data for one trace (S_{11}) measurement.

Table 30a. Typical Cycle Time (ms) for Measurement Completion, All Models and Options

Sweep Range	IF Bandwidth		Number of Points				
			201	401	1601	16001	32001
9 GHz to 10 GHz	600 kHz	Uncorrected	3.7	4.2	8.1	54	109.7
		2-Port cal	7.6	8.7	16.6	109	216.8
	10 kHz	Uncorrected	29.8	53.2	201.8	1990	3998
		2-Port cal	61.2	107.4	404	3978	7981
	1 kHz	Uncorrected	201.8	399	1585	15812	31643
		2-Port cal	403	798	3169	31664	63412
10 GHz to 20 GHz	600 kHz	Uncorrected	14.6	13.6	14.7	59.3	110.2
		2-Port cal	26.6	27	29.2	120.1	256.5
	10 kHz	Uncorrected	43.8	82.3	223	1995	3983
		2-Port cal	104	163.2	412	4002	7962
	1 kHz	Uncorrected	212	402	1588	15816	31625
		2-Port cal	409	804	3198	31636	63376

Table 30b. N5221B Typical Cycle Time (ms) for Full-Span Measurement Completion

10 MHz to 13.5 GHz		**Number of Points**				
IF Bandwidth		**201**	**401**	**1601**	**16001**	**32001**
600 kHz	Uncorrected	23.6	32.7	44.4	89.3	141.9
	2-Port cal	47.6	65.2	82	177.4	301.3
10 kHz	Uncorrected	51.4	94.3	306	2155	4030
	2-Port cal	104.8	188.4	611	4321	8098
1 kHz	Uncorrected	212.3	416	1641	16200	32388
	2-Port cal	424	829	3296	32397	64922

Table 30c. N5222B Typical Cycle Time (ms) for Full-Span Measurement Completion

10 MHz to 26.5 GHz		**Number of Points**				
IF Bandwidth		**201**	**401**	**1601**	**16001**	**32001**
600 kHz	Uncorrected	27.5	39	57	98.2	145.2
	2-Port cal	55.2	80.6	115	184.4	289.7
10 kHz	Uncorrected	53.6	95.1	337	2295	4103
	2-Port cal	107.1	189.4	697	4560	8203
1 kHz	Uncorrected	214.5	415	1645	16018	32021
	2-Port cal	431	854	3232	32035	64221

Table 31. Cycle Time vs. IF Bandwidth - Typical

Applies to the Preset condition (201 points, correction off) except for the following changes:

- CF = 10 GHz
- Span = 100 MHz
- Display off (add 21 ms for display on)

Cycle time includes sweep and retrace time.

Description	**N5221B/22B**	
IF Bandwidth (Hz)	**Cycle Time (ms)**	**Trace Noise Magnitude (dB rms)**
600,000	2.4	0.0045
100,000	3.5	0.0018
30,000	6.9	0.0011
10,000	26.8	0.0006
3,000	71.7	0.0004
1,000	206.5	0.0003
300	616	0.0002
100	1798	0.0002
30	5954	0.0002
10	17803	0.0002
3	59247	0.0002

Table 32. Cycle Time vs. Number of Points – Typical

Applies to the Preset condition (correction off) except for the following changes:

- CF = 10 GHz
- Span = 100 MHz
- Display off (add 21 ms for display on)

Cycle time includes sweep and retrace time.

Description	**IF Bandwidth (Hz)**			
Number of Points	**1,000**	**10,000**	**30,000**	**600,000**
3	4.7	2.2	2	1.9
11	12.7	3.9	2.7	2.1
51	53.2	8.1	3.1	1.9
101	101.7	14.4	4.8	2.8
201	205	26.9	6.9	2.4
401	398	51.7	12	3.3
801	813	104.7	22	4.4
1,601	1584	200.8	42.5	7.4
6,401	6326	824	162.8	22.3
16,001	15811	1989	401	59.3
32,001	31642	3975	803	106.9

Table 33. Data Transfer Time (ms) - Typical

NOTE The following was measured on a unit with Synthesizer 6.

Measured with the analyzer display off.

Values will increase slightly if the analyzer display is on.

Description	Number of Points				
	201	401	1601	16,001	32,001
SCPI over GPIB (Program executed on external PC[2])					
32-bit floating point	4.6	9.3	38	352	720
64-bit floating point	9.4	18.8	73.4	730	1455
ASCII	36.7	72.5	288	2882	5762
SCPI over SICL/LAN or TCP/IP Socket[1] (Program executed in the analyzer)					
32-bit floating point	<1	<1	<1	1.2	2.4
64-bit floating point	<1	<1	<1	2.3	4.6
ASCII	2.1	4	15	148	295
COM[1] (Program executed in the analyzer)					
32-bit floating point	<1	<1	<1	<1	<1
Variant type	<1	<1	1.4	12.4	25.5
DCOM over LAN[1] (Program executed on external PC)					
32-bit floating point	<1	<1	<1	2.3	4.4
Variant type	<1	1.6	5.3	52	105.5

[1] Values are for real and imaginary pairs, with the analyzer display off, using Gigabit Ethernet.

NOTE Specifications for Recall & Sweep Speed are not provided for the N522xB analyzers.

Test Set Block Diagrams

NOTE For best readability, use a color printer for printing the following graphics.

Legend

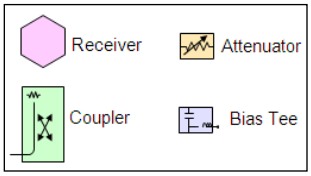


Figure 1. N5221B and N5222B Option 210 (2-port metrology configuration)

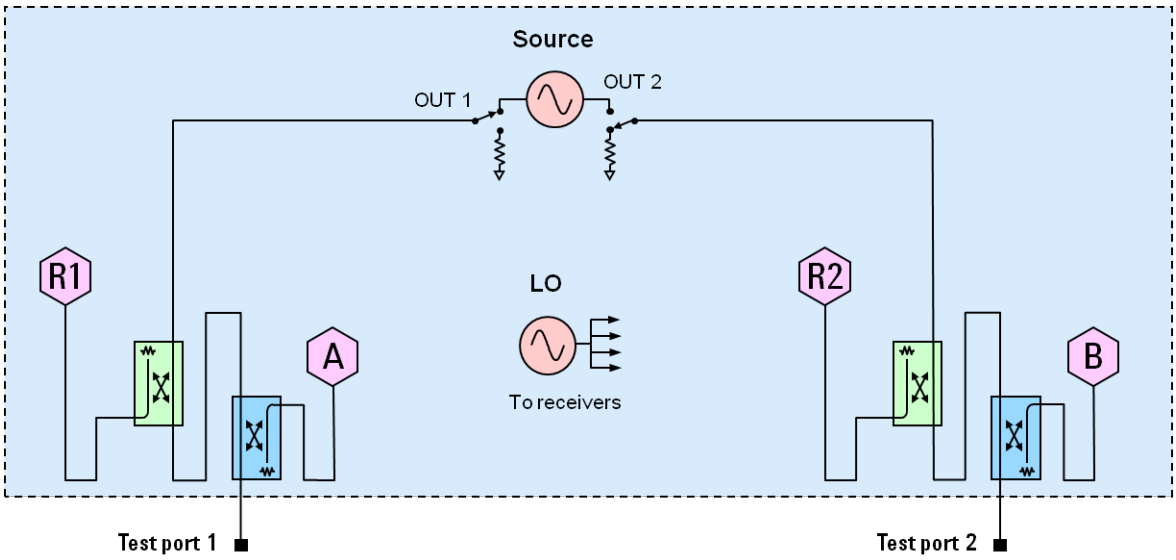


Figure 2. N5221B and N5222B Option 410 (4-port metrology configuration)

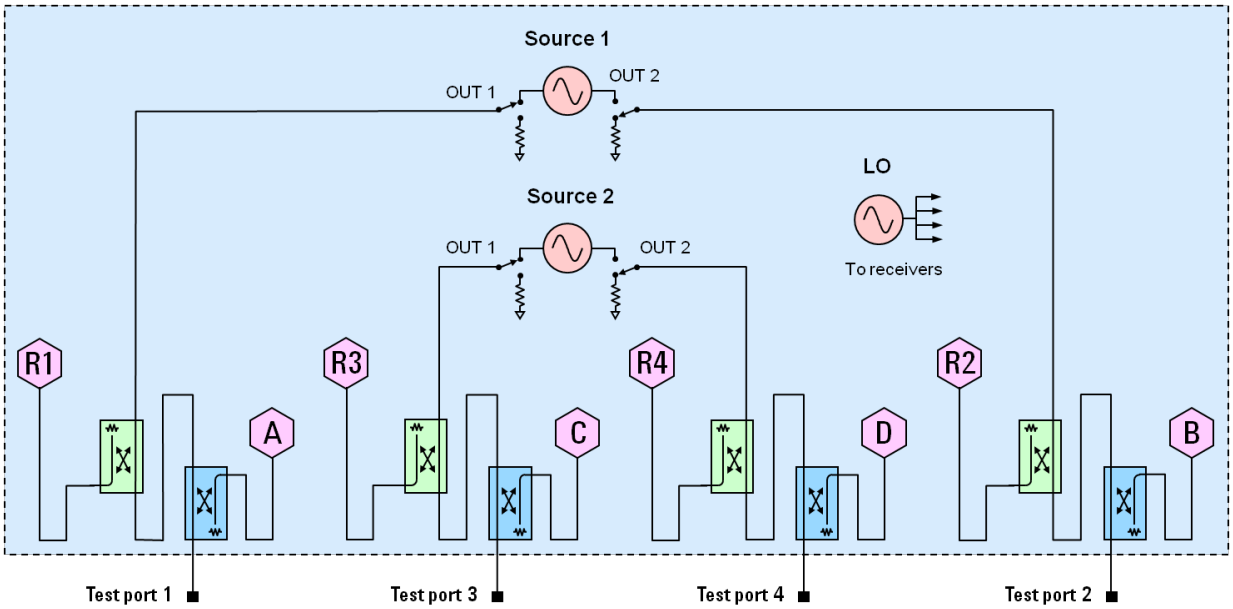
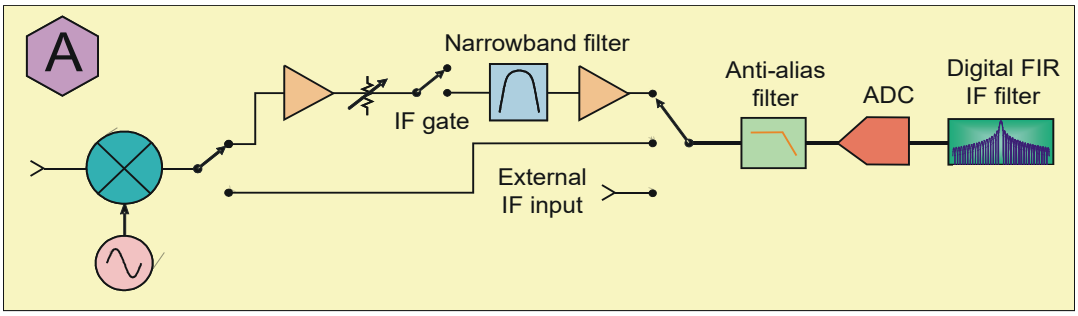


Figure 3. Receiver Block Diagram



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