DATA SHEET

N1012A OIF CEI 3.1

Compliance and Debug Application for 86100D DCA-X and N109X DCA-M Oscilloscopes

Reduce Your OIF CEI 3.1 Test Times from Hours to Minutes

KEYSIGHT TECHNOLOGIES

Introduction

Rapidly increasing worldwide demand for video and data transfer is placing new requirements for network expansion. Designers are creating innovative network elements that allow up to 100 Gb/s, which will be delivered using four lanes of 25 to 28 Gb/s. Extra challenges abound when transferring these signals on printed circuit boards, even for short distances. The Implementation Agreement for Optical Internetworking Forum Common Electrical Interface (OIF CEI) specifies the tests and limits for these devices.

These parameters can take a full day when characterized manually, and the recalculation of factors and CTLE values adds to the time the designer spends on testing. Keysight Technologies, Inc. has created the N1012A OIF CEI 3.1 Compliance and Debug Application for you to simplify measurement of these transmitter parameters and to obtain full results to test limits in a few minutes. This will keep you focused on getting your products to market knowing that your results are built on the heritage and consistency of Keysight measurement technology.

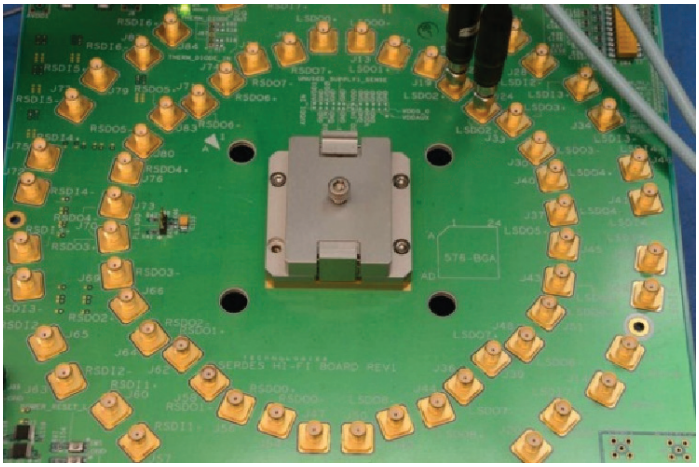
Easy-to-use compliance application that enables you to:

- Quickly set up equipment and make transmitter measurements
- Test your device to compliance or chosen limits
- Debug your device using custom configurations
- Remove effects of cables and fixtures
- Automatically determine optimal value of CTLE Peaking
- Generate reports to share with others

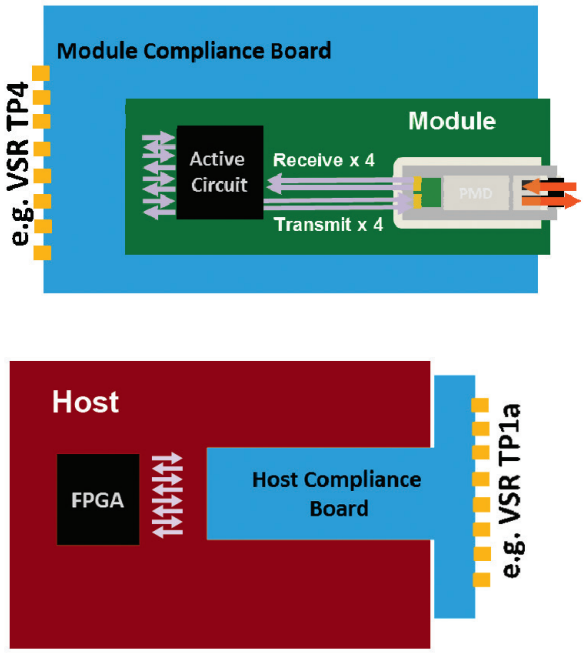
Transform Complexity into Simplicity

Satisfying the broad requirements of the OIF CEI Implementation Agreements can be very complex. The data rates for each interface have a range, rather than a fixed rate common to many standards. The test list between each OIF CEI interface varies as do the test limits; some limits depend upon previous measurements to perform the final calculations. Designers need to satisfy requirements on highly-advanced integrated circuits, host boards and modules when operating over long, medium, short and very short reaches. An extract from the 28G-SR specification table from the OIF CEI Implementation Agreement below.

Characteristic	Symbol	MIN	TYP	MAX	UNIT
Baud rate	T-Baud	19.90		28.1	Gsym/s
Output differential voltage	T_Vdiff	800		1200	mVppd
Differential resistance	T_Rd	80	100	120	Ω
Differential termination resistance mismatch (see table 1 to 3)	T_Rdm			10	%
Output rise and fall time (20% to 80%)	T_tr, T_tf	8			ps
Common mode noise	T_Ncm			12	mVrms
Differential output return loss	T_SDD22			–6	dB
Common mode output return loss	T_SCC22			–4	dB
Output common mode voltage	T_Vcm	–100		1700	mV



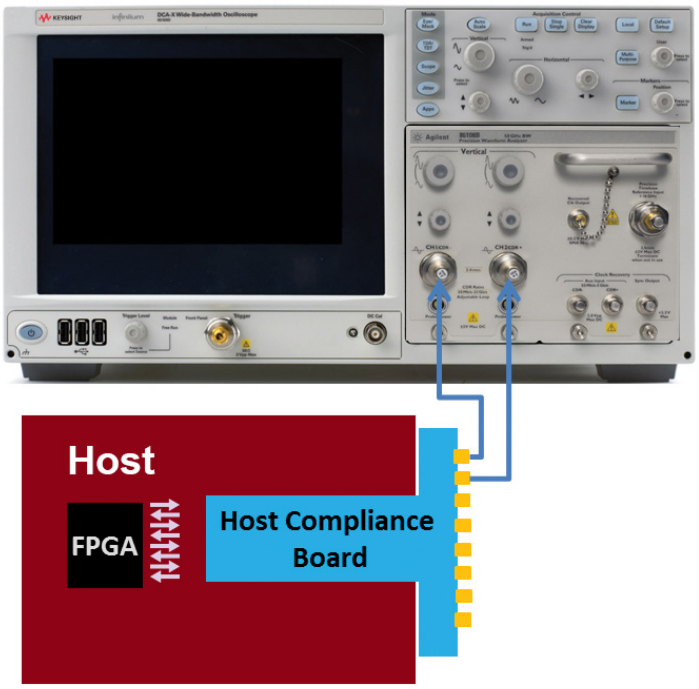
Development and characterization of advanced integrated circuits is time-consuming and expensive. Designers utilize test boards similar to the one shown above to fully characterize their parts for use in their own circuits or in their customer's circuits.



Hosts and modules have unique interface connectors and require compliance boards to enable connection to test equipment as shown here. Designers endeavor to minimize the trace lengths on the compliance boards and cable lengths.

Transform Complexity into Simplicity (Continued)

The N1012A software will control the 86100D DCA-X, N109X DCA-M, or ENA/PNA/TDR and readily measure your device.



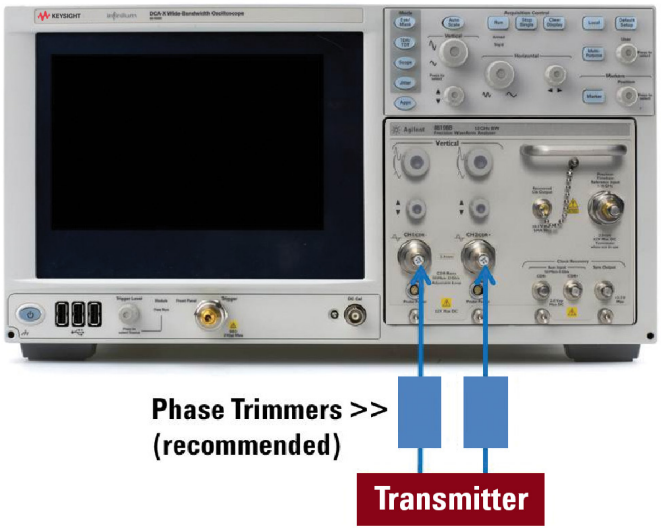
By pairing your test fixture or compliance board with the 86100D, 86108B and N1012A software, you will have the simplest and most powerful solution available to optimize your designs and offer the best products to customers. Phase trimmers and a pair of cables complement your set up for the most consistent and accurate measurements. You can easily remove the effects of cables or fixtures through intuitive Configure choices.

The N1012A software also supports a variety of other DCA configurations (see ordering guide), including the N1094A DCA-M oscilloscope and N1076B Electrical Clock Recovery.

Debug and Verify Your Designs Quickly and Easily

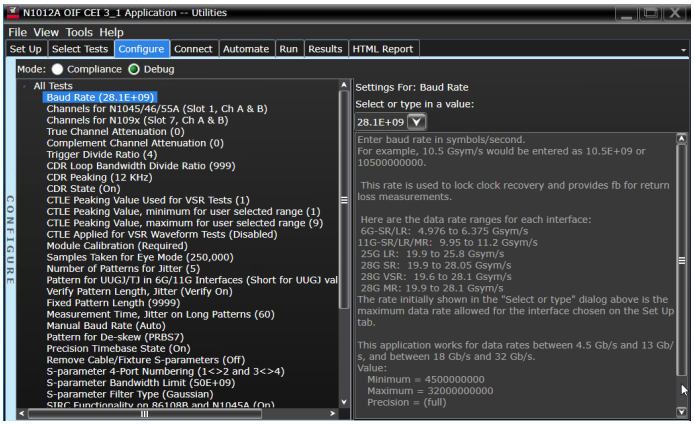
Choose your hardware

Configure your oscilloscope for a single module (on the right) or multi-module (listed in ordering guide). Connect your device through the recommended phase trimmers and you now have access to measurements with intrinsic jitter as low as 45 fs. Also have the PNA available for return loss and differential to common mode measurements.



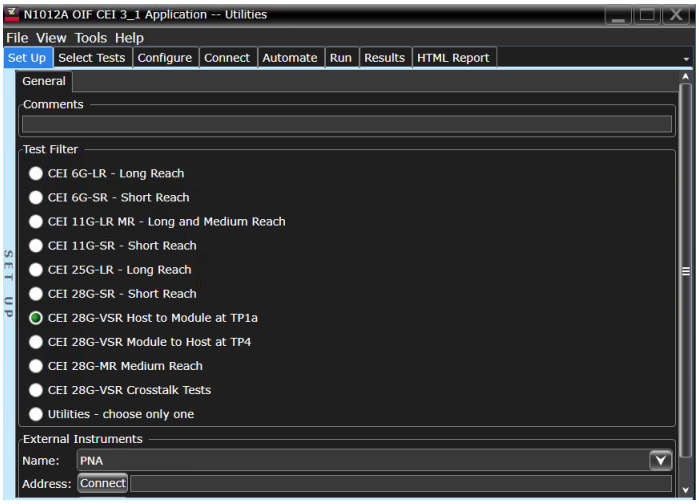
Configure your measurements

Customize parameters that are specific to your set-up such as baud rate and attenuation. Use default values or enter your own for settings such as number of samples or patterns taken and peaking for CTLE. Choose Normal mode to test within compliance limits and choose Debug mode to test to your custom limits.



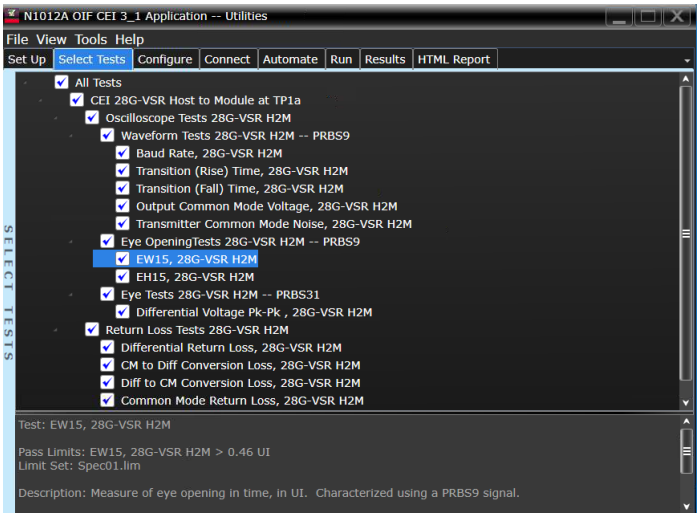
Select any OIF CEI Interface

The N1012A OIF CEI Compliance Application covers all nine OIF CEI 3.1 interfaces, which include rates from 4.9 Gb/s to 28.1 Gb/s. Click on the desired interface and the appropriate tests are offered in Select Tests.



Choose from over 140 tests

All tests required for each of the eight interfaces are available. You may click on all tests, a group of tests or individual tests. The full test name appears in the test list, and also is shown in the test results and reports. A description of the test and reference to the CEI Implementation Agreement are shown for each test.

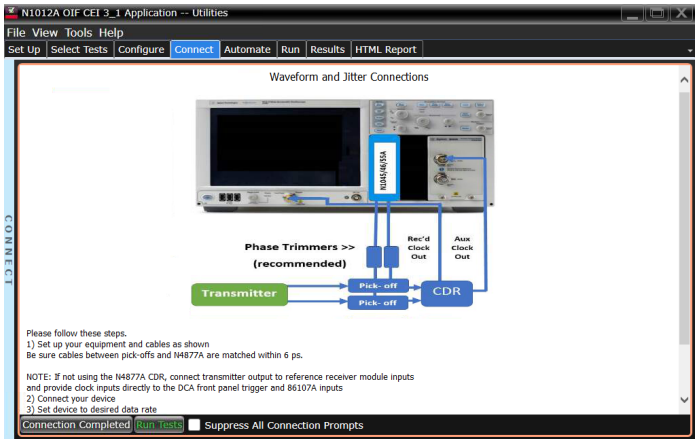


Debug and Verify Your Designs Quickly and Easily (Continued)

Measure challenging parameters fast

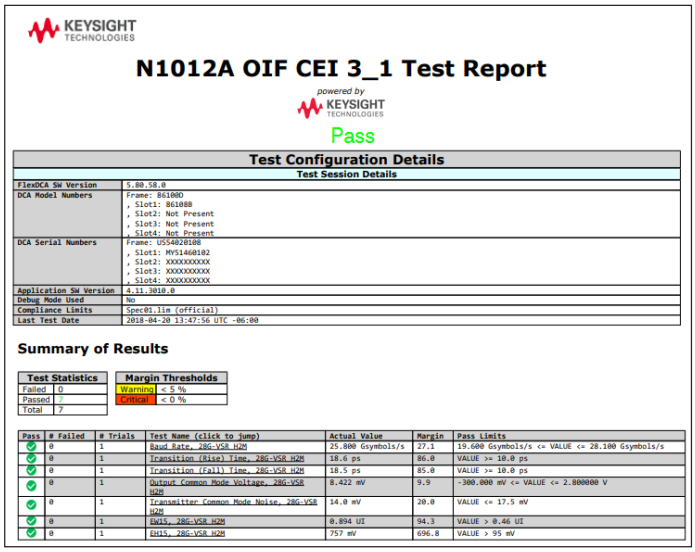
Simply follow the steps and click Run Tests. The N1012A and the DCA will automatically measure your device.

Use a wide range of modules such as the 86108, N1045A and N1055A, as well as the DCA-M oscilloscope. Characterize jitter for PRBS31 signals with integrated Option 401. Remove the effects of cables and fixtures by using convenient Configure choices.



Obtain concise compliance report

Users and customers are interested in the performance of your device. Share a report that shows the test conditions, summary of pass/fail, summary of all tests, and details for each test. Many include an appropriate screen shot of the measured parameter.



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N1012A OIF CEI 3_1 Test Report

powered by KEYSIGHT TECHNOLOGIES

Pass

Test Configuration Details

Test Session Details

FlexDCA SW Version	5.80.58.0
DCA Model Numbers	Frame: 86100D , Slot1: 86108B , Slot2: Not Present , Slot3: Not Present , Slot4: Not Present
DCA Serial Numbers	Frame: US54820108 , Slot1: MY51460102 , Slot2: XXXXXXXXXX , Slot3: XXXXXXXXXX , Slot4: XXXXXXXXXX
Application SW Version	4.11.3010.0
Debug Mode Used	No
Compliance Limits	Spec01.lim (official)
Last Test Date	2018-04-20 13:47:56 UTC -06:00

Summary of Results

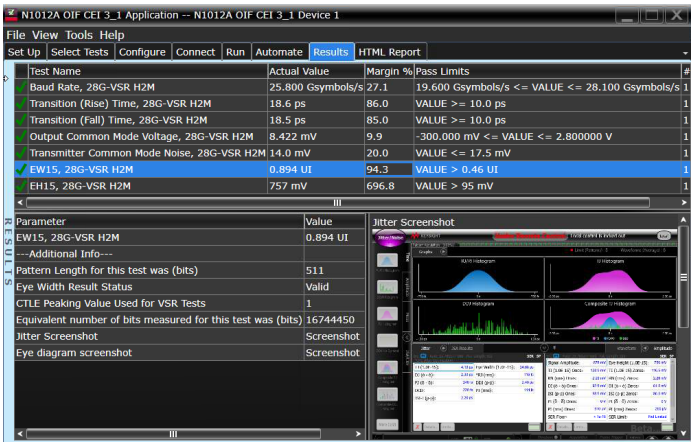
Test Statistics	
Failed	0
Passed	7
Total	7

Margin Thresholds	
Warning	< 5 %
Critical	< 0 %

Pass	# Failed	# Trials	Test Name (click to Jump)	Actual Value	Margin	Pass Limits
✓	0	1	Baud Rate, 28G-VSR H2M	25.800 Gsymbols/s	27.1	19.600 Gsymbols/s <= VALUE <= 28.100 Gsymbols/s
✓	0	1	Transition (Rise) Time, 28G-VSR H2M	18.6 ps	86.0	VALUE >= 10.0 ps
✓	0	1	Transition (Fall) Time, 28G-VSR H2M	18.5 ps	85.0	VALUE >= 10.0 ps
✓	0	1	Output Common Mode Voltage, 28G-VSR H2M	8.422 mV	9.9	-300.000 mV <= VALUE <= 2.800000 V
✓	0	1	Transmitter Common Mode Noise, 28G-VSR H2M	14.0 mV	20.0	VALUE <= 17.5 mV
✓	0	1	EW15, 28G-VSR H2M	0.894 UI	94.3	VALUE > 0.46 UI
✓	0	1	EH15, 28G-VSR H2M	757 mV	696.8	VALUE > 95 mV

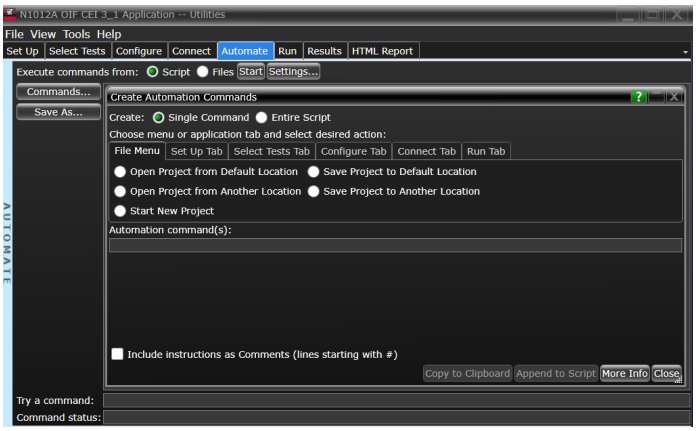
See device performance in one view

In a few minutes, you'll have test results showing which parameters passed or failed, and the margin compared to limits. These results will provide immediate insights into how you'll need to improve your design to meet the challenging tests in the OIF CEI 3.1 Implementation Agreements.



Control your device or other equipment

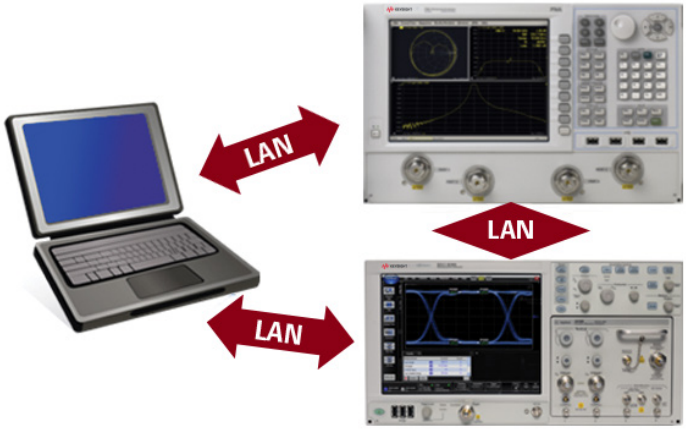
The Automation tab enables you to enter commands to control external devices or equipment, and to further sequence your tests or to control timing.



More Features to Further Streamline Your Development

Configure your solution in many ways

The hardware and software architecture provides wide flexibility. You may install FlexDCA and the N1012A on the oscilloscope, both on your PC or split between them. This enables you to use your PC for more processing power and other applications, or to have all measurement capability consolidated into a compact solution. The PNA can be controlled by the PC or by the DCA.

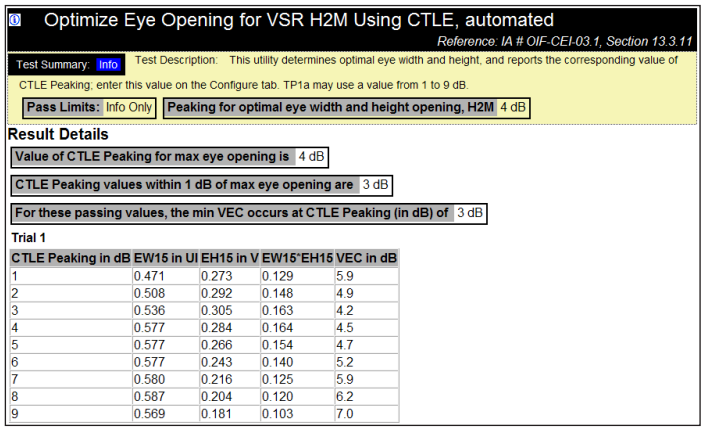


Conveniently de-skew your channel

Skew between the true and complement signals can degrade your measured performance. While you need to characterize performance with DUT skew included, the N1012A guides you to quickly de-skew your cables or remote heads for best results.

Choose CTLE peaking automatically

Enjoy the convenience of the N1012A determining the optimal value of CTLE (continuous time linear equalizer) peaking, which is required by the CEI 28G Very Short Reach for the Host-to-Module interface. The values for gain, zero and poles are calculated for you and used in the optimization.



Optimize Eye Opening for VSR H2M Using CTLE, automated

Reference: IA # OIF-CEI-03.1, Section 13.3.11

Test Summary: Info Test Description: This utility determines optimal eye width and height, and reports the corresponding value of CTLE Peaking; enter this value on the Configure tab. TP1a may use a value from 1 to 9 dB.

Pass Limits: Info Only Peaking for optimal eye width and height opening, H2M 4 dB

Result Details

Value of CTLE Peaking for max eye opening is 4 dB

CTLE Peaking values within 1 dB of max eye opening are 3 dB

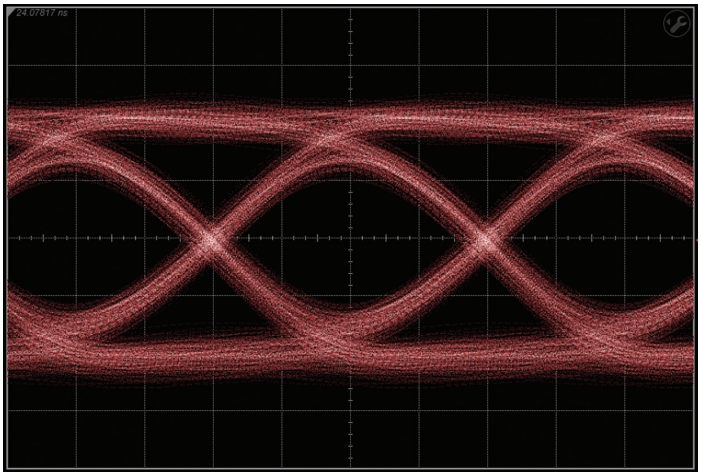
For these passing values, the min VEC occurs at CTLE Peaking (in dB) of 3 dB

Trial 1

CTLE Peaking in dB	EW15 in UI	EH15 in V	EW15*EH15	VEC in dB
1	0.471	0.273	0.129	5.9
2	0.508	0.292	0.148	4.9
3	0.536	0.305	0.163	4.2
4	0.577	0.284	0.164	4.5
5	0.577	0.266	0.154	4.7
6	0.577	0.243	0.140	5.2
7	0.580	0.216	0.125	5.9
8	0.587	0.204	0.120	6.2
9	0.569	0.181	0.103	7.0

....Or manually select the CTLE peaking

The CEI 28G Very Short Reach Module-to-Host interface requires a peaking value of 1 dB or 2 dB. Select the value from the manual utility. Whether automatic or manual, you'll put your best eye forward for your customers.



Characterize Over 140 Parameters

The OIF CEI 3.1 Implementation Agreement includes many challenging tests. The table below shows the nine interfaces and the tests required for each interface. The N1012A measures all of these parameters; empty cells indicate that parameter is not required for that interface.

	Parameter	CEI 6G-SR	CEI 6G-LR	CEI-11G-SR	CEI-11G-LR/MR	CEI-28G-SR	CEI-25G-LR	CEI-28G-VSR H2M/M2H	CEI-28G MR
Measured on DCA	Baud rate	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1	13.1	14.3.1
	Rise times / fall times	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1	13.3.2 / 3	14.3.1
	Differential output voltage		7.4.1	8.3.1	9.3.1	10.3.1	11.3.1	13.3.2 / 3	14.3.1
	Output common mode voltage	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1	13.3.2 / 3	14.3.1
	Single-ended output voltage								14.3.1
	Transmitter common mode noise	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1	13.3.2 / 3	14.3.1
	Eye mask	6.4.1	7.4.1	8.3.1	9.3.1				
	Uncorrelated unbounded Gaussian jitter (RJ)			8.3.1	9.3.1	10.3.1	11.3.1		14.3.1
	Uncorrelated bounded high probability jitter	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1		14.3.1
	Duty cycle distortion	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1		
	Total jitter	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1		14.3.1
	Even-odd jitter								14.3.1
	UUGJ – FIR off and on					12.1	12.1		
	UBHPJ – FIR off and on					12.1	12.1		
	DCD – FIR off and on					12.1	12.1		
	Total jitter – FIR off and on					12.1	12.1		
	Eye width (EW15)							13.3.2 / 3	
	Eye height (EH15)							13.3.2 / 3	
	Vertical eye closure								
	Jitter transfer BW			8.4					
	Jitter transfer peaking			8.4					
PNA	Differential output return loss	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1	13.3.2 / 3	14.3.1
	Common mode output return loss	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1		14.3.1
	CM to differential conversion loss							13.3.2 / 3	
	Differential to CM conversion loss							13.3.2 / 3	
DMM	Differential resistance	6.4.1	7.4.1	8.3.1	9.3.1	10.3.1	11.3.1		14.3.1
	Differential termination mismatch			8.3.1	9.3.1	10.3.1	11.3.1	13.3.2 / 3 (1 MHz)	14.3.1

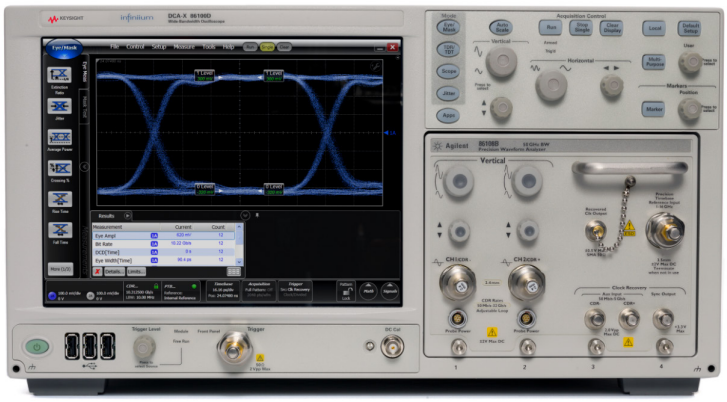
Choose Industry-Leading Solutions

Keysight offers a wide range of electrical and optical test solutions to address current and emerging communications standards. For OIF-CEI-3.1, you may choose a hardware combination that addresses your test needs for today, and into the future:

1. 86100D DCA-X with 86108B (Integrated “One-Box” solution) - recommended
2. 86100D DCA-X with DCA module and external clock recovery
3. N109X Electrical DCA-M with external clock recovery

Solution 1: Keysight 86100D DCA-X mainframe + 86108B “MegaModule” (recommended)

- Highest accuracy
- Easy setup
- Integrated solution

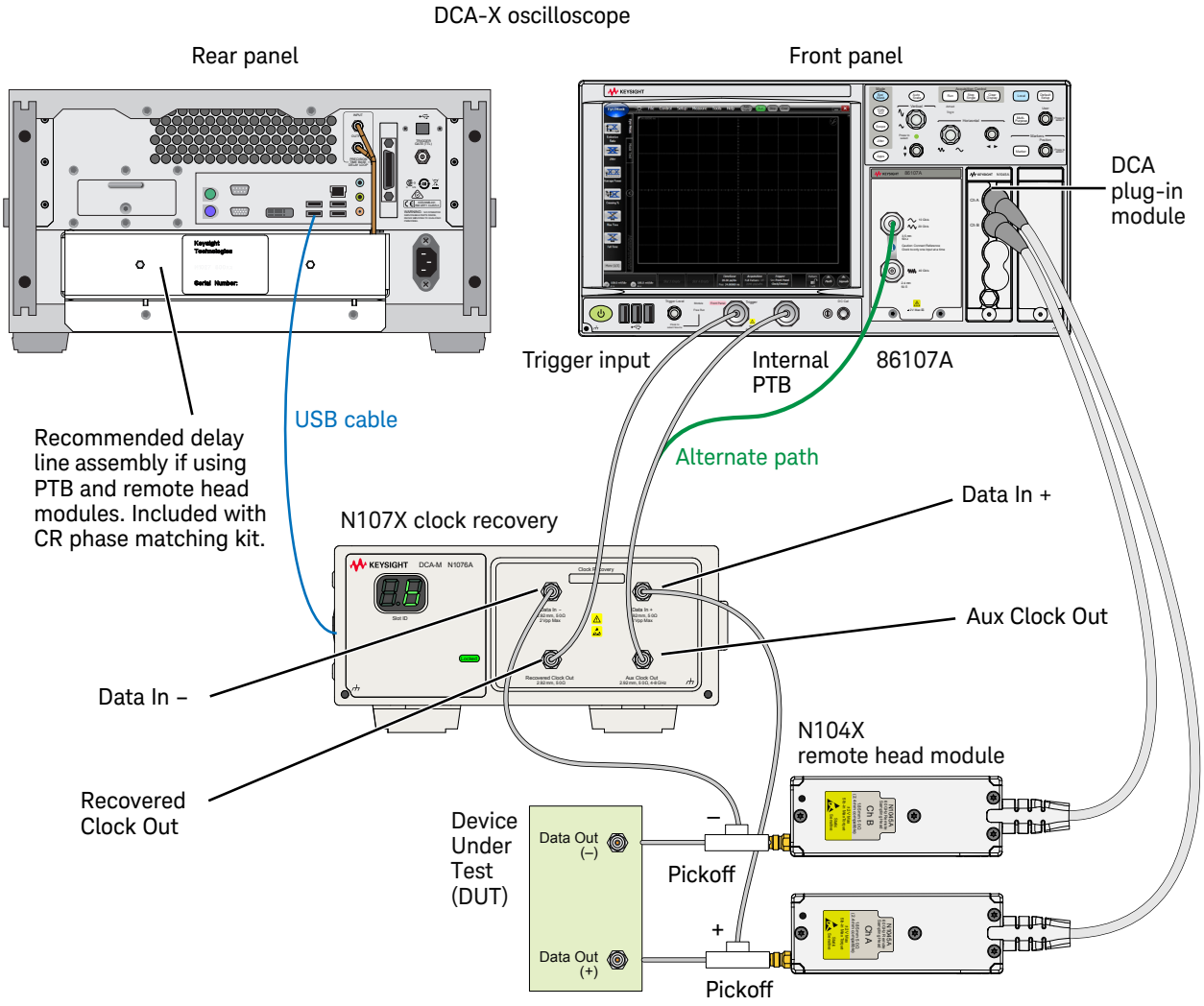


	Mainframe model number	Mainframe hardware options	Mainframe software options (Fixed or transportable licenses)
TX Test using Digital Communications Analyzer (DCA)	86100D DCA-X	Required: ETR	Required: 200, 201, 401
		Optional: PTB (not used with 86108B)	Optional: SIM (for de-embedding cables)
	Plug-in module model number	**Plug-in module options**	**Max # of modules/Diff channels**
	86108B	216/LBW (for CEI 6G/11G)	1/1
		232/HBW (for CEI 6G/11G/25G/28G)	
	Software		
	N1012A	OIF-CEI-3.1 Compliance and Debug Application	
	N1010A	FlexDCA FW Rev 5.8 or later (included with 86100D mainframe)	
	Keysight IO Libraries	Rev 16.3 or later, automatically installed with FlexDCA installation	
	86100DU-400	PLL and Jitter Transfer SW (a “no cost” download from **www.keysight.com/find/jtf**)	
	Accessories		
	86108B-PT2, N1027A-PT2	Phase trimmers (Qty 2), for modules with 2.4 mm connectors (86108B)	
	86108B-DC2, N9399F, or N9399F	DC blocks, 50 GHz (Qty 2)	
	86108B-DC3, N9398C, N9399C or 11742A	DC blocks, 26.5 GHz (Qty 2)	
	86108B-CA3	Matched cable set (Qty 1)	
Impedance measurements	**Model number (Pick ONE)**	**Description**	
	N1055A TDR/TDT	35/50 GHz 2/4 Port TDR/TDT remote sampling head for the 86100D DCA-X (any option)	
	Economy Network Analyzer (ENA)	Any 4-port model with a frequency range of at least 12 GHz (also covers the 1 MHz termination mismatch test)	
	Performance Network Analyzer (PNA)	Any 4-port model with frequency range of at least 12 GHz	

Choose Industry-Leading Solutions (Continued)

Solution 2: Keysight 86100D DCA-X mainframe with DCA module + external clock recovery

- Highest flexibility
- Scalable solution
- High fidelity – remote heads minimize loss between DUT and oscilloscope



Choose Industry-Leading Solutions (Continued)

Equipment configuration for solution 2: Keysight 86100D DCA-X mainframe with DCA module + external clock recovery

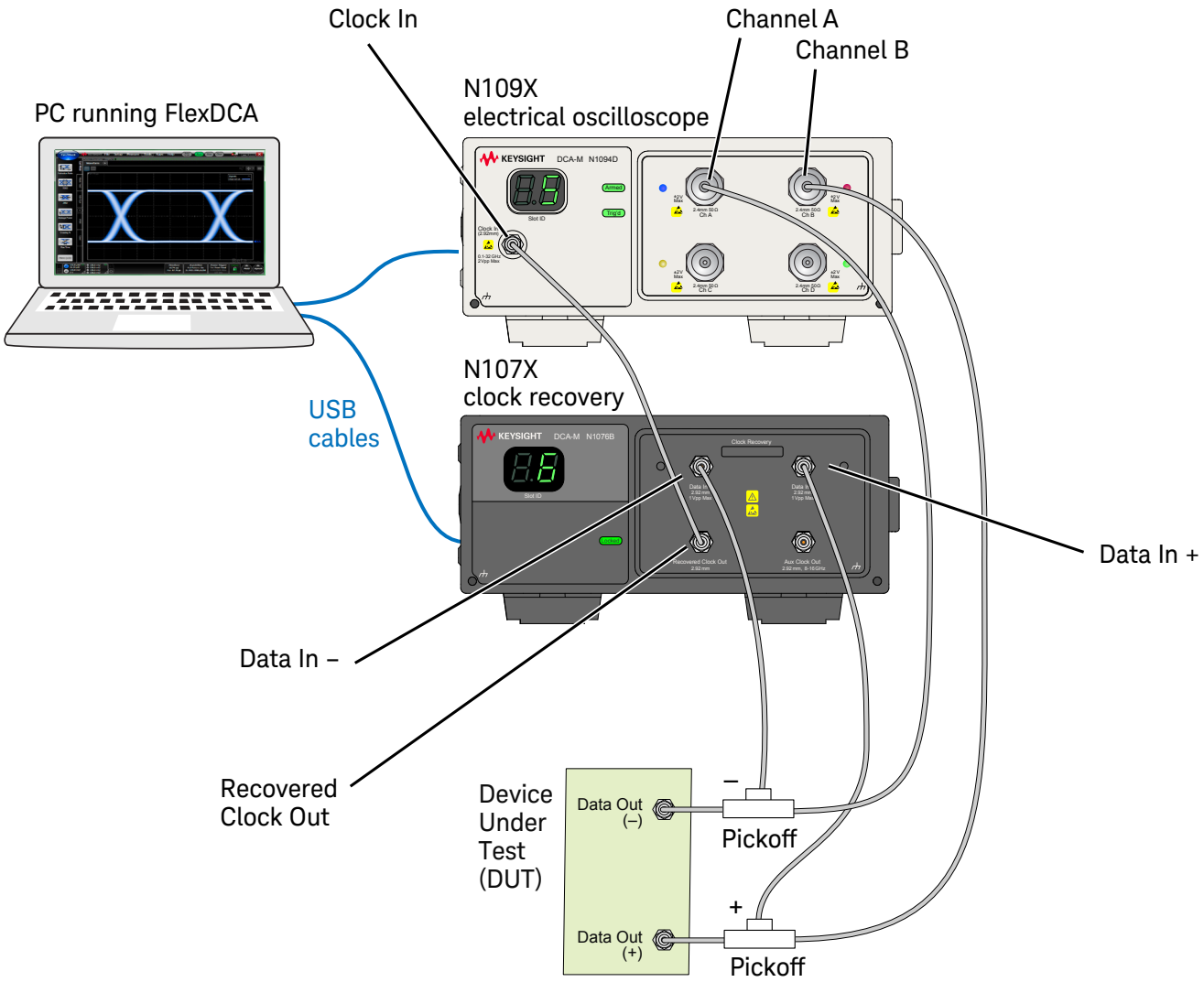
	Mainframe model number	Mainframe hardware options	Mainframe software options (Fixed or transportable licenses)
TX Test using Digital Communications Analyzer (DCA)	86100D DCA-X	Required: ETR, PTB	Required: 200 201, 401
			Optional: SIM (for de-embedding cables)
	Plug-in module model number (Pick ONE)	**Plug-in module options**	**Max # of modules/Diff channels**
	86112A	Any	2/2
	86117A	Any	2/2
	86118A	H01	2/2
	54754A	Any	2/2
	N1045A	Any	4/8
	N1055A	Any	4/8
	N1046A	12F, 14F, 72F, 74F, 82F, 84F (any 2 or 4 channel config)	4/8
	Clock recovery model number (Pick ONE)	**Clock recovery options (Pick ONE; Option 232 or higher required for CEI 25G/28G)**	
	N4877A	216/232	
	N1076A	216/232	
	N1076B	216/232/264	
	N1077A	216/232	
	N1078A	216/232/264	
	Software		
	N1012A	OIF-CEI-3.1 Compliance and Debug Application	
	N1010A	FlexDCA FW Rev 5.8 or later (included with 86100D mainframe)	
	Keysight IO Libraries	Rev 16.3 or later, automatically installed with FlexDCA installation	
	86100DU-400	PLL and Jitter Transfer SW (a "no cost" download from **www.keysight.com/find/jtf**)	
	Accessories [1]		
	N1027A-76B (recommended)	Clock Recovery Phase Matching Kit for use with N104XA remote head and external N4877A/ N107X clock recovery	
	N1027A-76A	Clock Recovery Phase Matching Kit for use with N104XA remote head and external N4877A/ N107X clock recovery	
	N1027A-MC1	Clock Recovery Phase Matching Kit for use with N104XA remote head and external N4877A clock recovery	
	N1027A-2P2	Pick-Off Tees (Qty 2), for remote head modules with 1.85 mm/2.4 mm connectors (N1045A, N1046A, N1055A), (included in N1027A-76A/76B Kit)	
	N1027A-PT2	Phase trimmers,50 GHz (Qty 2), for 861XX DCA modules with 2.4 mm connectors (86117A)	
	N1027A-PT3	Phase trimmers, 26.5 GHz (Qty 2), for 54754A/861XX DCA modules with 3.5 mm connectors (54754A, 86112A)	
	N9399F, N9399F	DC block, 50 GHz (Qty 2)	
	N9398C, N9399C or 11742A	DC block, 26.5 GHz (Qty 2)	
Impedance measurements	**Model number (Pick ONE)**	**Description**	
	N1055A TDR/TDT	35/50 GHz 2/4 Port TDR/TDT Remote Sampling Head for the 86100D DCA-X (any option)	
	Economy Network Analyzer (ENA)	Any 4-port model with a frequency range of at least 12 GHz (also covers the 1 MHz termination mismatch test)	
	Performance Network Analyzer (PNA)	Any 4-port model with frequency range of at least 12 GHz	

1. For more information on clock-to-data delay matching, refer to the Keysight N1076A/B, N1077A, and N1078A Clock Recovery DCA-M User Guide.

Choose Industry-Leading Solutions (Continued)

Solution 3: Keysight N109X electrical DCA-M + external clock recovery

- Flexible configuration
- Lowest cost
- Scalable



Choose Industry-Leading Solutions (Continued)

Equipment configuration for solution 3: Keysight N109X electrical DCA-M + external clock recovery

TX Test using Digital Communications Analyzer (DCA)	**Software model number (For user-supplied PC)**	**Software options (Install on PC, or purchase fixed SW licenses for the DCA-M)**	
	N1010A FlexDCA	Required: 200, 201, 401	
		Optional: SIM (for de-embedding cables)	
	Model number (Pick ONE)	**DCA-M options**	**# of diff channels**
	N1092C	Required: LOJ, PLK	1
		Optional: FS1	
	N1092E	Required: LOJ, PLK	1
		Optional: FS1	
	N1094A	Required: LOJ, PLK, 030 or 050	1
		Optional: FS1	
	N1094B	Required: LOJ, PLK, 030 or 050	2
		Optional: FS1	
	Clock recovery model number (Pick ONE)	**Clock recovery options (Pick ONE; Option 232 or higher required for CEI 25G/28G)**	
	N4877A	216/232	
	N1076A	216/232	
	N1076B	216/232/264	
	N1077A	216/232	
	N1078A	216/232/264	
	Software		
	N1012A	OIF-CEI-3.1 Compliance and Debug Application	
	N1010A	FlexDCA FW Rev 5.8 or later (included with 86100D mainframe)	
	Keysight IO libraries	Rev 16.3 or later, automatically installed with FlexDCA installation	
	86100DU-400	PLL and Jitter Transfer SW (a "no cost" download from **www.keysight.com/find/jtf**)	
	Accessories [1, 2]		
	N1027A-2P2	Pick-off tees (Qty 2), for remote head modules with 1.85 mm/2.4 mm connectors (N1045A, N1046A, N1055A), (included in N1027A-76A/76B Kit)	
	N9399F, N9399F	DC block, 50 GHz (Qty 2)	
	N9398C, N9399C or 11742A	DC block, 26.5 GHz (Qty 2)	
Impedance measurements	**Model number (Pick ONE)**	**Description**	
	N1055A TDR/TDT	35/50 GHz 2/4 Port TDR/TDT Remote Sampling Head for the 86100D DCA-X (any option)	
	Economy Network Analyzer (ENA)	Any 4-port model with a frequency range of at least 12 GHz (also covers the 1 MHz termination mismatch test)	
	Performance Network Analyzer (PNA)	Any 4-port model with frequency range of at least 12 GHz	

1. For more information on this hardware configuration, including clock-to-data delay matching, refer to the Keysight N1076A/B, N1077A, and N1078A Clock Recovery DCA-M User Guide.
2. No clock-to-data delay phase matching kit is available for this hardware configuration.

Learn more at: www.keysight.com

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