N6475A DDR5 Tx Compliance Test Software

The Keysight Technologies, Inc. N6475A DDR5 compliance test application software provides a fast and easy way to test, debug and characterize your DDR5 designs. DDR5 technology ofers high data rate of up to 6.5GT/s which enables higher bandwidth for data transfer with lower power. The N6475A offers full test coverage to enable testing of transmitter physical layer of the DDR5 DRAM, data buffer and register chips.



Features

The DDR5 compliance test application offers several key features to simplify the validation of your designs.

- Intuitive setup wizard for quick test setup, configuration and test.
- Comprehensive test coverage of the DDR5 electrical, timing, eye diagram and jitter tests,
- Offline testing capability with waveform files for completeness of simulation to validation workflow.
- Easy interface to export the test result to the KS6800A series Data Analytics repository for quick analysis with the web-based visualization tool.

Comprehensive Test Coverage

The DDR5 compliance test application allows you to use the oscilloscope to perform automated testing and margin analysis on your DDR5 design. The application automatically configures the oscilloscope for each test and provides informative test result. It includes margin analysis indicating how close your device comes to passing or failing each test parameter. The DDR5 compliance test automates the measurement as specified in the specification, which helps save test time and design cost.



Easy Test Definition

The test application enhances the usability of Keysight Infiniium oscilloscopes for testing DDR5 devices which includes the DRAM, register and data buffer. The Keysight automated test framework guides you quickly through the steps required to define the setup, perform the tests and view the test results. You can select a category of tests or select an individual test. The user interface is designed to minimize unnecessary reconnections, which will help save test time and minimize potential operator error. You can save the tests and configurations as project files and recall them for quick testing and review previous results. You can also run the compliance test with saved waveform files from the oscilloscope or the ADS simulation tool on your PC, freeing the oscilloscope for other data acquisition purposes.

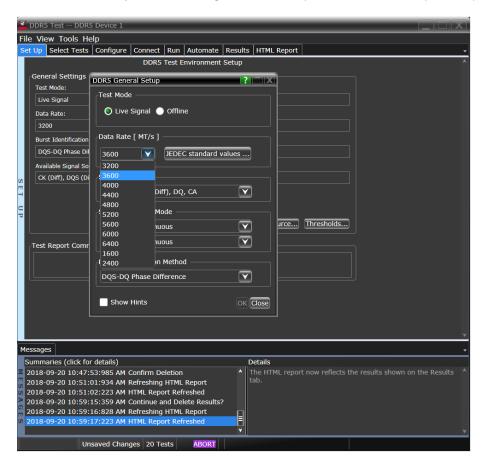


Figure 1: DDR5 application test setup screen. Select the speed grade of your device.

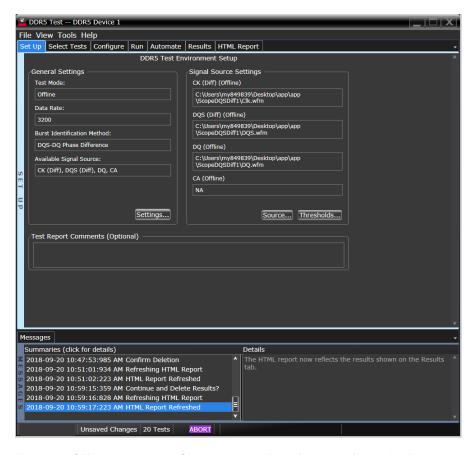


Figure 2: Offline test setup. Supports waveform files in .wfm or .h5 format.

Configurability and Guided Connection

The DDR5 compliance test application provides flexibility in your test setup. The application lets you define controls for critical test parameters such as voltage threshold values, number of waveforms used for analysis and customizable violation settings. Once you have configured the tests, the connection page will display the connection diagram for the test you have selected.

You can also specify the number of test trials and only stop running selected tests when the stop condition is met. The application will save the worst-case test result to help you track down the anomalies in your signals.

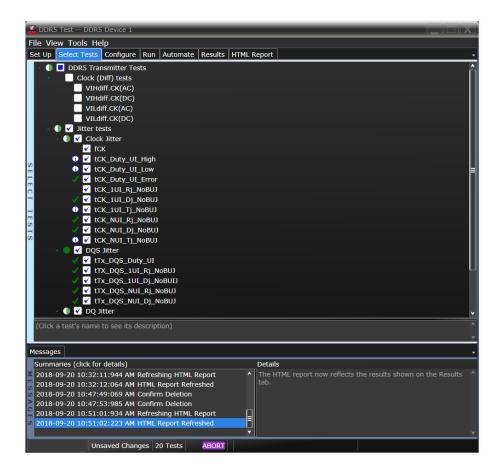


Figure 3: The Select Tab list all the tests available in your setup. You can easily setup the individual test or groups of tests.

Comprehensive Result Analysis

In addition to providing you with measurement results, the DDR5 compliance test application reports how close your test results are to the specified test limit. You can specify the level at which warnings are to be issued. You are provided a full array of statistics for each measurement.

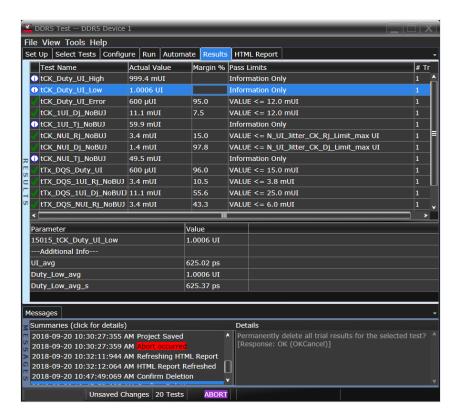
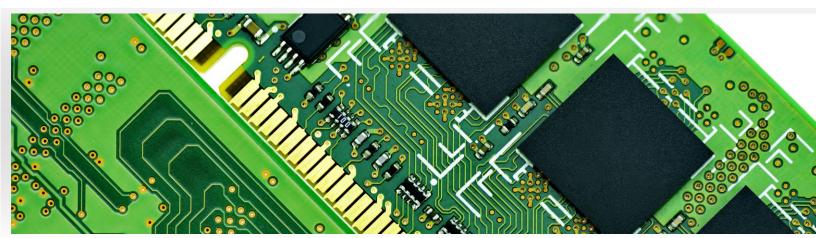


Figure 4: The DDR5 test application documents your test parameters, pass or fail status, test limits, measured values and margin.

Data Analytics

The KS6810A data analytics web service software provides an interface that allows the user to export the test result directly to the data repository server via a URL. The user can retrieve the aggregated measurement to be viewed using the visualization tool included in the KS6810A software. The visualization tool provides graphical plots in histogram or line mode that describes the measurement in different test conditions or properties.



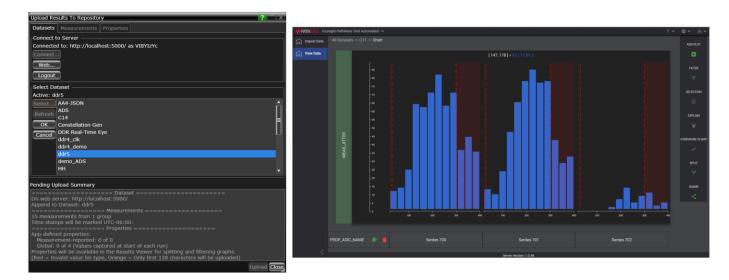


Figure 5: User can import test results to the KS6810A software from the N6475A Tx Compliance Test software and view the test results with the visualization tool.

Thorough Performance Reporting

The DDR5 compliance test application generates HTML reports that captures the performance, status and margins of your device under test. It also captures screenshots of critical measurements of your reference and documentation. This report is suitable for printing and sharing with your test vendors, customers and suppliers.

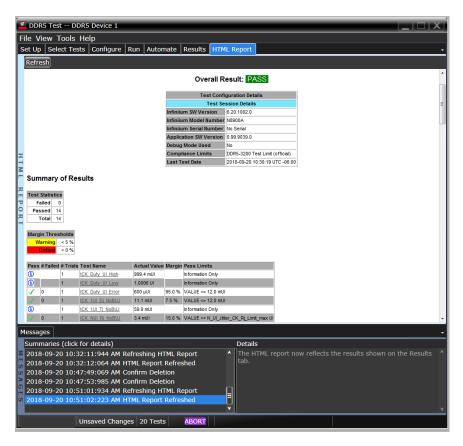


Figure 6: The DDR5 test application generates a summary report for quick results viewing. The report includes details such as test limits, test description and test results.

Recommended oscilloscope

The N6475A DDR5 software is compatible with the Infiniium series oscilloscopes with operating software revision of 6.30 or higher.

Data rate	Minimum Bandwidth	Minimum Channels	Description
Up to 6500 MT/s	16 GHz	2	Infiniium V- series, Z- series, UXR oscilloscopes

Ordering Information Software

Application	License Type		Model number
DDR5 Tx Validation software	Fixed	Factory-Installed	N6475A-1FP
		User-Installed	N6475A-1FP
	Floating	Transported	N6475A-1TP
		Server based	N5435A-140
		User-Installed	N5461A-1FP
	Floating	Transported	N5461A-1TP
		Server based	N5435A-025

Note:

Firmware 6.30 includes Serial Data Equalization software as standard.

Required software for DDR5 Tx Validation software:

• D9020JITA EZJit Complete for V/Z/UXR-Series

Optional software

- D9020ASIA Advanced Signal Integrity Software (EQ, InfiniiSim Advanced)
- KS6810A Data Analytics Web Service software

Example of Hardware Configuration

Model number	Description	Quantity
UXR0164A	16 GHz Infiniium oscilloscope	1
1169B	12GHz InfiniiMax II probe amplifiers	Minimum 2
N5442A	Precision BNC adapter	2
MX0100A	Micro probe head	1 (includes 5 probe heads, 1 bullet adapter)
MX0103A	Bullet adapter	1

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