

Keysight EDA Electronic Design Automation Software

The Next Generation of EDA Design High-Frequency Analog and High-Speed Digital products

Introduction

Keysight EDA software is a comprehensive suite of design and simulation tools for the development of high-frequency analog and high-speed digital products to meet current and future requirements such as 5G, 6G, automotive radars, high data rate wired/wireless channels for in commercial/military applications and quantum electronics.

Keysight EDA is the leading high-frequency (up to 300 GHz sub-terahertz) and high-speed digital physical layer design tool used by circuit and system designers to develop components for RF-Microwave (RFMW), High-Speed Digital (HSD), Photonics, RF Systems, Power Electronics (PE) and Quantum Electronics (QE) industries. These powerful capabilities are proven in Keysight's state-of-the-art, high-performance instruments that serve these industries because the cutting-edge components inside are designed with Keysight EDA tools. Keysight EDA also enables you to develop superior products to meet tight competitive market windows.

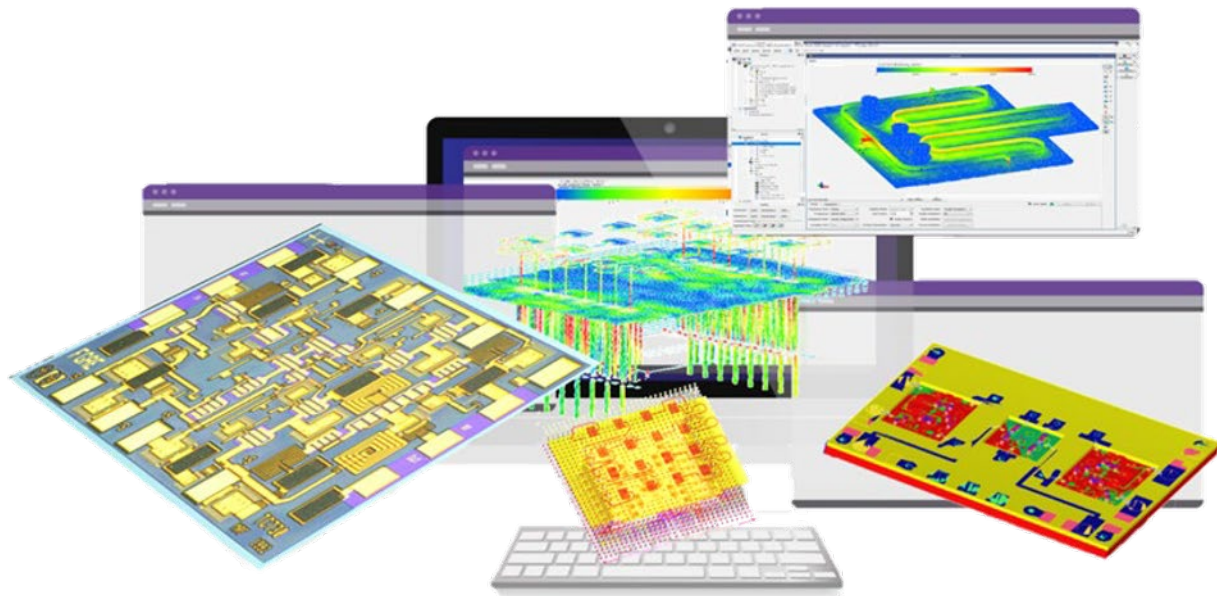


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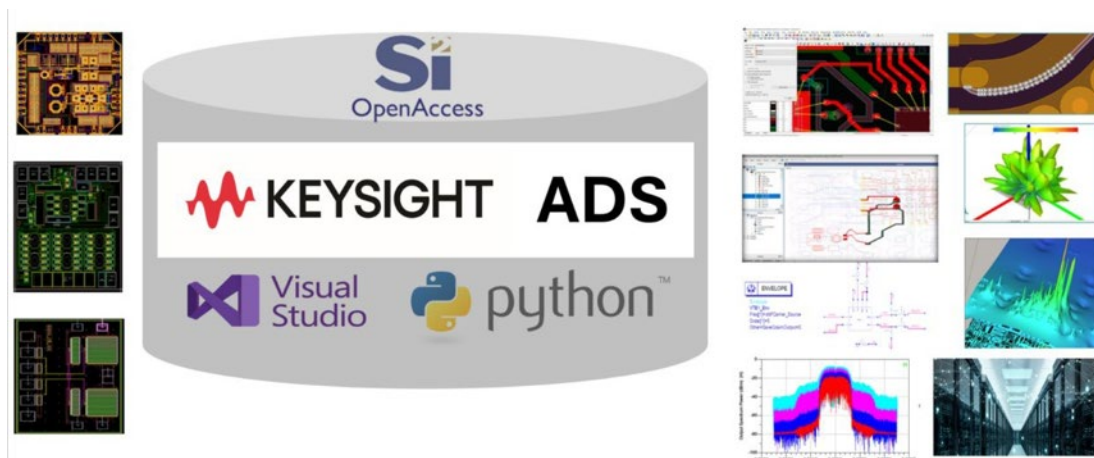
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RF Circuit Simulation Professional revamps circuit simulation and optimization technology for efficient and accurate design of the most challenging RF-microwave-mmWave circuits, RFICs, and RF 3D Heterogeneous Integrated (3DHI) modules for 5G/6G, wireless, radar, and aerospace- defense applications.	14
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EDA Platform for Multi-Technology Simulation

Keysight ADS is the premier EDA platform for the design of high-frequency and high-speed digital physical layer components from single purpose amplifiers to complex RF modules and high-speed digital boards. The ADS platform forms the basis for RF/microwave, Signal/Power Integrity, Power electronics and Quantum electronics design software bundles that contain the powerful capabilities to accomplish challenging design tasks for these applications.

The ADS EDA platform is chosen by leading companies after careful evaluation of its unique capabilities not found in other design tools:

- 3D integration of multi-technology including chips, packaging, interconnects, shielding, boards, and input/output connectors. This represents the most realistic assembly of the product to be designed and electrically simulated. Other design tools can only design chips, packaging, or boards separately but not together as the assembled product, requiring subsequent tedious, error prone manual integration of separate design databases to obtain the required composite simulation results.
- Superior simulation accuracy with instrument grade signal generation, simulation algorithms and data processing, leveraged from Keysight's rich history of developing state-of-the-art test instruments and sponsoring university research. ADS simulation flexibly duplicates measurements on the test bench, except it is done on the product that is yet to be built, so that the design can be optimized for performance, yield, and spec compliance before hardware prototyping to eliminate wasteful iterations.
- Flexible deployment into your design process with Python scripting support to automate integration with external applications such as AI and machine learning to generate simulation models from measured or published data. The ADS design database itself is architected on the OpenAccess standard, the world's most widely used, open reference database for IC and electronic design, for 3rd party tool interoperability without lossy database translation.



Configuration

Advanced Design System (ADS)

<https://www.keysight.com/find/ads>

<https://www.keysight.com/find/ads-config>

Learn More About:

[Circuit Design and Simulation](#)

[Python customization in ADS](#)

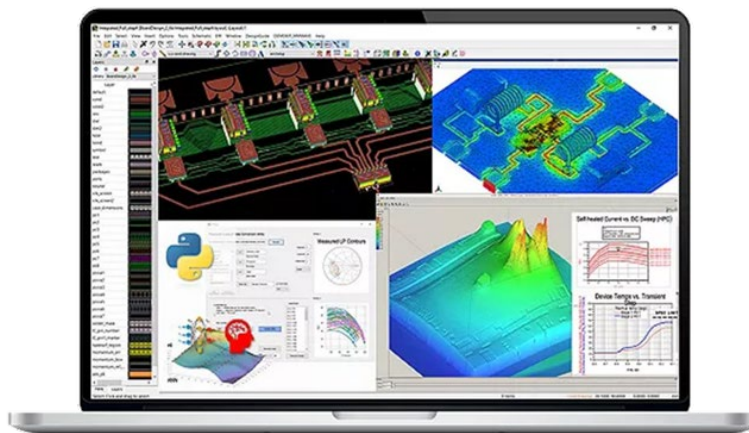
[Artificial Intelligence \(AI\) and Machine Learning \(ML\)](#)

RF Suite

ADS is the trusted design and simulation tool used by 70% of the world's RF and Microwave component designers, according to a recent Pedestal Research EDA industry report.

This is because it is uniquely able to easily integrate 3D mixed technologies, such as RFICs, chip packaging, boards, interconnects, antennas, and input/output connectors to accurately represent and simulate what will eventually be built. This is also known as 3D Heterogeneous Integration (3DHI).

All simulation technologies, including time-frequency-envelope domain circuit, electromagnetic (EM), electrothermal (ETH), system and statistical, are seamlessly integrated for accurate analysis and automated optimization of performance to meet specs.



These are the unique capabilities offered by ADS for RF and Microwave component design:

- 3DHI multi-technology assembly and simulation
- Circuit-Electromagnetic (EM)-Electrothermal (ETH) co-simulation for optimal accuracy.
- RFIC, MMIC, RF Module, RF Board, 3D Packaging, Passive component design.
- Error Vector Magnitude (EVM) optimization for meeting digitally modulated RF signal specs.
- Modulated Signals Virtual Test Bench (VTB) libraries for evaluating circuit design compliance to system wireless, radar, and networking industry standards specs.
- Nonlinear stability to guarantee stable amplifier design.
- Electrothermal for optimal circuit performance and reliability simulation accuracy under actual deployment conditions.
- Statistical design for robust production yield.
- Python support for AI/Machine Learning model training and workflow deployment customization.

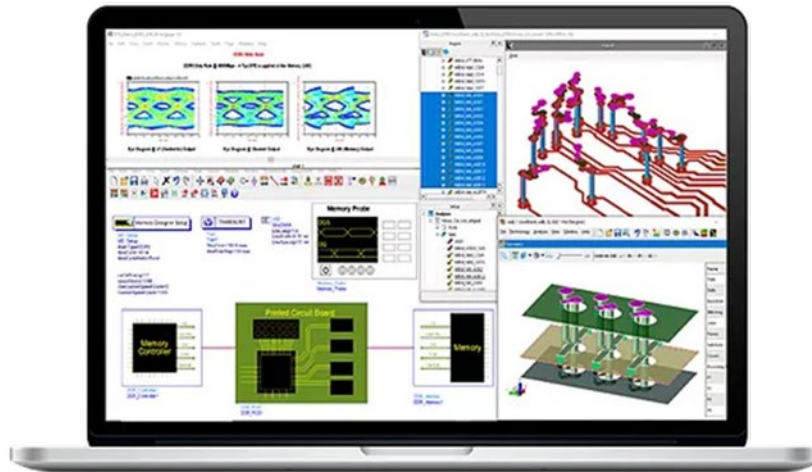
Recommended ADS Bundles for RF and Microwave (RF/MW) Circuit Design

ADS Core, EM Design, Layout, RF Ckt Sim	W3603B
ADS Core, EM Design, Layout, RFPro, RF Ckt Sim	W3606B
ADS Core, EM Design, Layout, RFPro, RF Ckt Sim, Sys-Ckt Verification, Complete VTBs	W3608B

Learn more: <https://www.keysight.com/find/whats-new-in-rf-microwave> RF and Microwave Circuit Design Software | Keysight

High Speed Digital Suite

ADS high-frequency simulation expertise is also ideally suited for High-Speed Digital design of components and circuit boards for high data rate multi-gigabit applications such as High-Performance Computing (HPC), data center servers, memory systems, chiplets, and AI/ML systems in robotics and autonomous vehicles. Libraries of industry digital standards enable convenient compliance verification.



ADS offers the following powerful capabilities for HSD design:

Signal Integrity (SI) for simulating digital data channels, such as PCB traces and interconnects, for eye-diagram opening margin requirements in both pre-layout exploration and post-layout verification. It includes channel simulation for pulsed amplitude modulation (PAM-4/3/N), high data rate signaling, and rapid scanning of trace impedance to avoid SI issues in HSD boards.

Power Integrity (PI) for verifying and optimizing power delivery networks in HSD systems to ensure stable, noise-free voltages and currents are delivered to ICs on PCBs to maintain reliable operations. PI issues such as voltage droops, noise and signal distortions are simulated to avoid wasteful hardware iterations.

Memory design in modules or memory systems on server boards to ensure compliance with industry standards such as DDR, GDDR, and multi-level modulation of PAM-N and NRZ signaling. Pre- and post-layout analysis of memory bus channels ensures reliable memory hardware downstream.

Chiplet Design: Design and verify UCle- and BoW-based chiplet architectures with end-to-end die-to-die channel simulation, PHY modeling, compliance measurements, and interoperability analysis for modular SoC development.

3DIC Interconnect Design: Model, route, and simulate 2.5D/3DIC packages with support for silicon and organic substrates, micro bumps, hatched and waffle ground plane modeling, and die-to-die interconnect optimization to accelerate advanced packaging workflows.

PCIe Design: Automate the design and validation of PCI Express® Gen5, Gen6, and Gen7 systems with intelligent multi-lane system setup, AMI model generation, pathfinding, simulation-driven compliance testing, and signal integrity analysis.

USB Design: Design and validate USB4® Gen2, Gen3, and Gen4 systems using a smart design environment with AMI model generation, pathfinding, simulation-driven compliance testing, SIPro integration, and Infiniium Probe for advanced measurement.

Ethernet Design: Develop high-speed Ethernet interfaces, including multi-lane and PAM4 systems, using integrated AMI modeling, COM (Channel Operating Margin) analysis, pathfinding and photonic E-O-E simulation.

SerDes Design: Create and validate multi-gigabit SerDes links for standards including PCIe, USB, Ethernet, and MIPI, with advanced equalization, AMI modeling, waveform analysis, and simulation-driven compliance testing.

Electrical Performance Scan (EP-Scan): Perform fast, automated PCB-level Signal Integrity and Power Integrity screening to identify design issues early through layout visualization, impedance analysis, and design rule validation before detailed simulation.

Photonics Design: For designing Photonic Integrated Circuits (PICs), including pure optical, Electro-Optical (EO), and Optoelectrical (OE) circuits. By combining physics-based simulations with measurement-calibrated models, the software enables engineers to design, simulate, and validate PICs with exceptional accuracy and confidence.

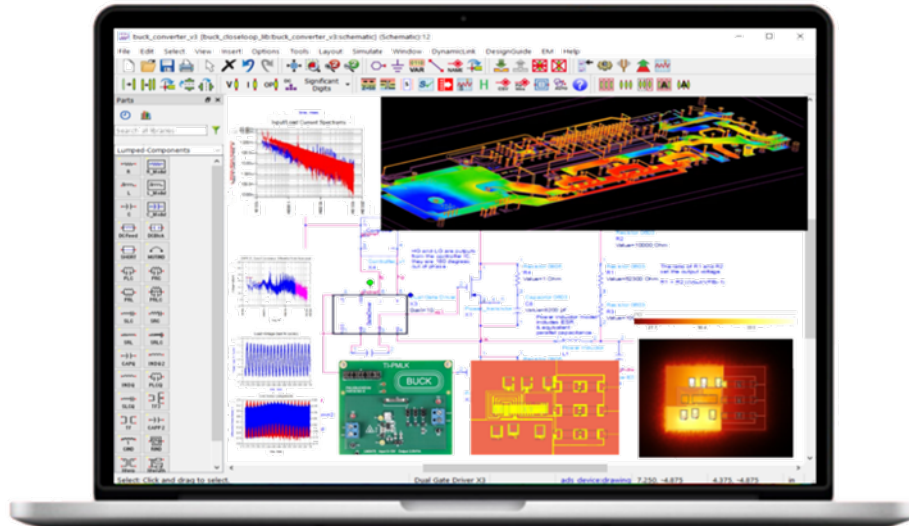
Recommended ADS Bundles for High-Speed Digital (HSD) Design

ADS Core, EM Design, Layout, HSD Ckt Sim, SIPro	W3624B
ADS Core, EM Design, Layout, HSD Ckt Sim, SIPro, PIPro	W3625B
ADS Core, EM Design, HSD Ckt Sim, Memory Designer	W3622B
Chiplet PHY Designer Bundle	W3650B
PCIe Designer Bundle	W3651B
Chiplet 3D Interconnect Designer	W3510E
3D Interconnect Designer	W9100E
Memory Designer	W3622B
Memory Designer + SIPro/PIPro	W3626B
System Designer for USB	W3652B
System Designer for Ethernet	W3653B
VPI Optical Link	W3071E
Photonic Designer	W3803B
USB4 AMI Modeler	W3081E
PCIe AMI Modeler	W3082B
Ethernet AMI Modeler	W3083B
Electrical Performance Scan	W9001E
Learn more: High Speed Digital Suite Keysight	

Power Electronics EM Professional

Designing compact, high-power density and reliable Switched Mode Power Supply (SMPS) requires high switching speed, suppression of Electromagnetic Interference (EMI) emissions, and thermal reliability.

Accurate models required for SMPS design and simulation, such as Gallium Nitride (GaN) transistors, can be created with Python based AI/ML automated training from published datasheet information.



ADS offers the following unique capabilities for Power Electronics (PE) design:

- Design compact, high-power density SMPS with EMI emission compliance.
- Thermal simulation to identify hotspot locations that degrade reliability.
- Build accurate custom models for GaN, SiC, Si, and IGBT devices using the Power Electronics Model Builder (PEMB)
- Accurate Transformer and magnetics modeling for power converters
- Accelerate power converter design using the Shooting Newton Steady-State Solver

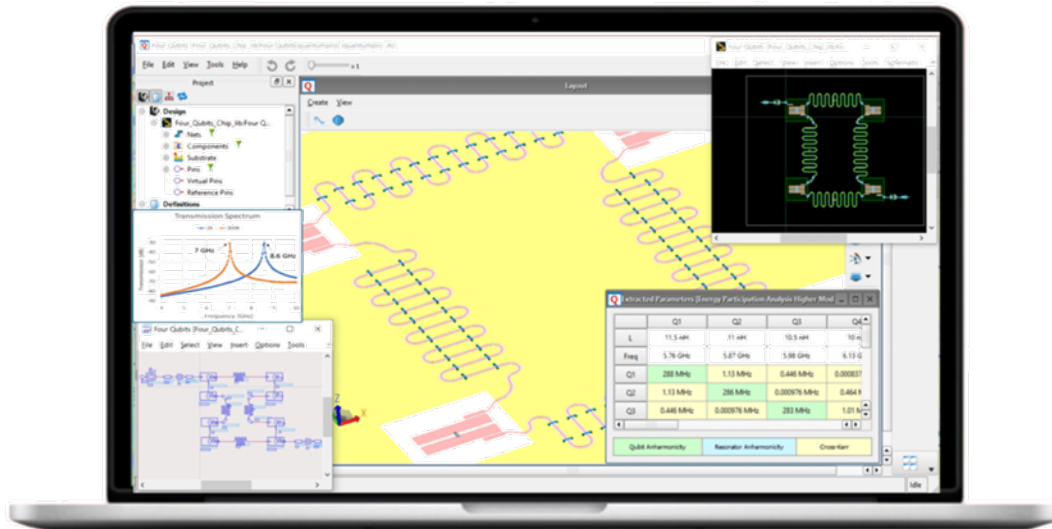
Recommended ADS Bundles for Power Electronics (PE) Design

ADS Core, EM Design, Layout, PEPro, PE Ckt Sim	W3631B
PEPro	W3035E
PE circuit simulation	W3026E

Learn more: <https://www.keysight.com/us/en/products/software/pathwave-design-software/pathwave-advanced-design-system/pathwave-ads-power-electronics.html>

Quantum Design Suite

Keysight EDA offers the first integrated design flow for superconducting qubit design to enable faster development of quantum chips with five unique capabilities in one platform:



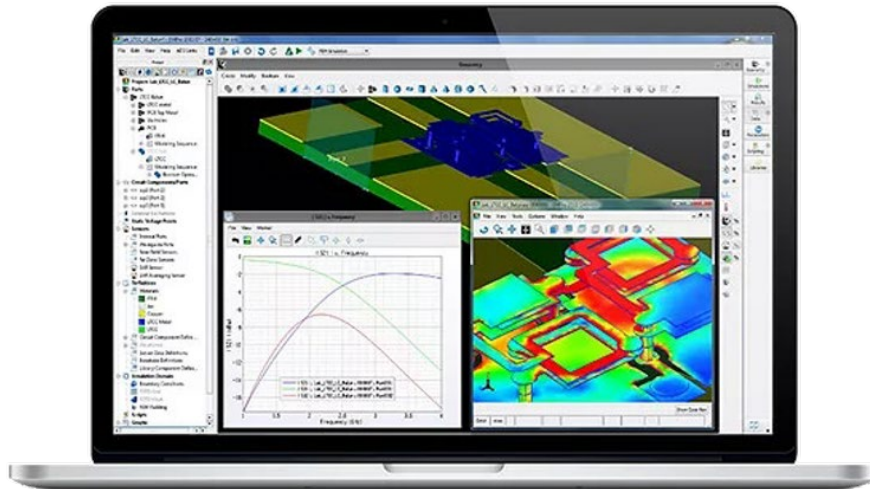
- **Quantum Layout:** Library of commonly used quantum artwork -- fully parametrized for convenient optimization and quantum circuit buildup.
- **Electromagnetic:** Full EM solution in both frequency domain and eigenmode (with kinetic inductance) for quantum parameter analysis
- **Quantum Parameters Extraction:** Automatic extraction using the three standard methods, Blackbox Quantization, Energy Participation Ratio (EPR), and Quasi-Static.
- **Nonlinear Circuit Simulation:** Market-leading harmonic balance nonlinear circuit simulation with superior convergence for complex quantum amplifier design with 1000s of Josephson junctions.
- **EM-Nonlinear Circuit Co-Sim:** Ability to inspect the power dependence response of the quantum chip by combined EM and nonlinear circuit co-simulation.

Recommended ADS Bundles for Quantum Design

ADS Core, EM Design, Layout, QuantumPro	W3704B
ADS Core, EM Design, Layout, QuantumPro, RF Ckt Sim	W3706B
ADS Core + EM Design + QuantumPro + HB	W3715B
Quantum System Analysis Element (Time Dynamics and Dilution Fridge Input Line Designer)	W3056E
Learn more: Quantum Electronics Design Software Keysight	

Electromagnetics Professional

Keysight EMPro is a dedicated 3D modeling and electromagnetic (3DEM) simulation environment for 3D components in electronic designs. It can also analyze radar cross sections of vehicles and aircraft, and Specific Absorption Rate (SAR) of the human head and body from smartphone EM radiation.



EMPro offers the following capabilities:

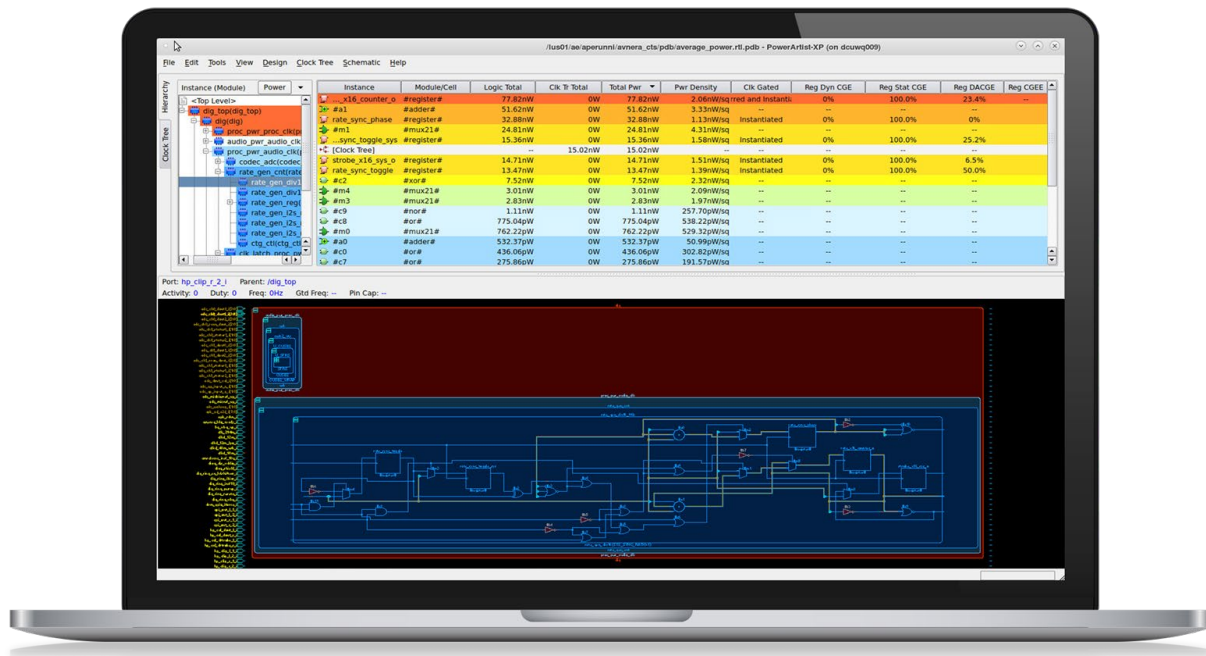
- 3D modeling environment to draw or import CAD files of complex 3D shapes such as packaging, antennas, connectors, vehicles, and the human body. Material properties and ports are then defined for running 3DEM simulation.
- Finite Element Method (FEM) simulator for electrically small structures such as packages, shields, and connectors to determine S-parameters, EM fields, and cavity resonant frequencies.
- Finite Difference Time Domain (FDTD) simulator for electrically large structures such as vehicles, aircraft, and the human body to determine radar cross sections and biological EM field absorption.
- Output includes S-parameters, surface currents, EM fields, antenna patterns, and cavity resonant frequencies.
- Simulate smartphone antenna radiation with human body model for compliance with Hearing Aid Compatibility (HAC) and Specific Absorption Rate (SAR) standards.
- Export 3D components to ADS for Circuit-EM co-simulation to thoroughly analyze the interaction of active and passive components before building hardware to eliminate iterations.

Recommended EM Design (EMPro) Bundles

EM Design Core (EMPro) + FEM + ADS RFPro UI	W4301B
EM Design Core (EMPro) + FDTD + Compliance Module	W4302B
EM Design Core (EMPro) + FEM + FDTD + Compliance Module + ADS RFPro UI	W4303B
EM Design (EMPro) Configuration	https://www.keysight.com/find/em-design https://www.keysight.com/find/em-design-config

PowerArtist RTL Power Analysis

Power efficiency is critical for semiconductor design. Whether you're building mobile, Central Processing Units (CPUs), networking, or automotive Integrated Circuits (ICs), the industry-leading PowerArtist platform helps you tackle power challenges early in the development cycle for the highest impact.



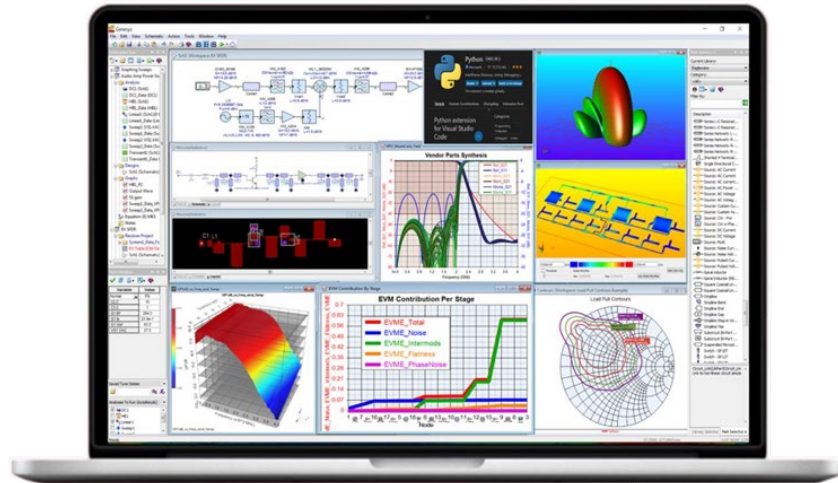
With Keysight PowerArtist, your team can:

- Model physical implementation effects to automatically find power reduction opportunities.
- Quickly debug power hotspots using an intuitive, interactive interface.
- Profile real workloads from emulators while analyzing tens of milliseconds of activity in hours, not days.
- Stream millions of Register-Transfer-Level (RTL) cycles efficiently for deep power analysis and early decision-making.
- Identify testbench gaps and drive Intermediate Representation (IR) flows by ranking vectors for maximum coverage with minimum simulation cycles.

[Learn more PowerArtist | Keysight](#)

RF Synthesis

Keysight Genesys is an affordable and easy-to-use RF circuit and system design tool. It offers 13 types of automatic RF circuit synthesis, such as filters and impedance matching networks, for RF engineers to quickly get a starting sub-circuit design for integrating into a bigger design. It also includes circuit, system, and EM simulators for designing single-technology RF boards.



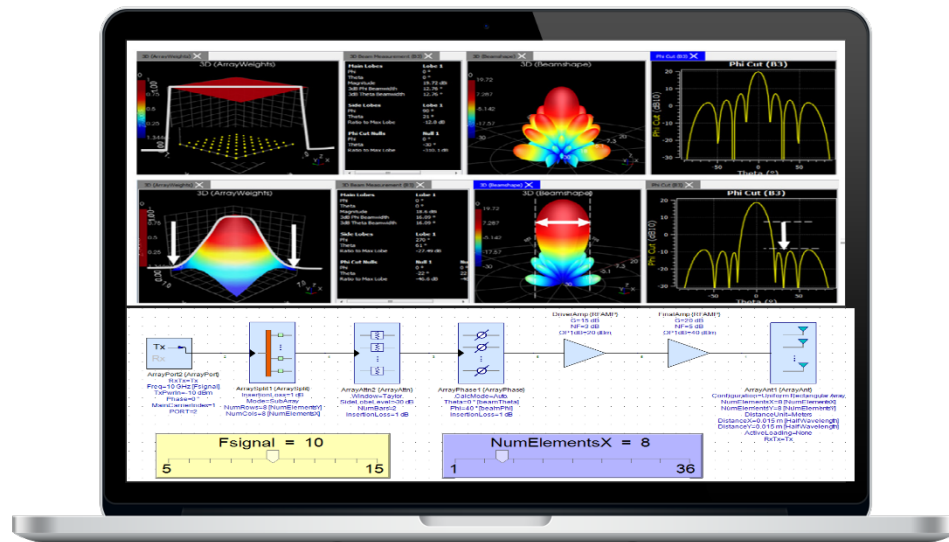
- Affordable, Accurate, Easy-to-Use RF circuit synthesis, Circuit, EM, System simulation integrated design tool.
- Widest coverage of 13 RF circuit synthesis categories in the industry. Ideal as a personal RF toolbox for designing sub-circuits as part of a bigger design.
- Filter synthesis – passive, active, distributed, hybrid lumped-distributed, custom response direct synthesis.
- Impedance matching networks for measured complex, frequency dependent impedance (antennas) and multi-stage complex impedance broadband matching.
- Mixers, Oscillators, Transmission lines, transitions, signal control splitters, combiners, attenuators.
- RF System line up cascaded block budget analysis with root cause detective pinpoint culprit block causing system degradation.
- RF System design for modulated RF signals with fast Error Vector Magnitude (EVM) analysis to select the optimal component line-up to meet system EVM specs.
- System Sys-Parameters modeling of published datasheet nonlinear amplifiers and passive components.
- Momentum 3D Planar EM simulator for microstrip and arbitrary planar layout.

Recommended RF Circuit Synthesis (Genesys) Bundles

RF Synthesis Core (Genesys) + Synthesis	W5301B
RF Synthesis Core (Genesys) + Synthesis + EM	W5304B
RF Synthesis Core (Genesys) + Synthesis + Circuit + System + Modulated-RF + EM	W5309B
RF Synthesis (Genesys) Configuration	https://www.keysight.com/find/rf-synthesis https://www.keysight.com/find/rf-synthesis-config

RF System Design

Keysight RF System Design is an RF simulation platform for designing and verifying communication, defense/aerospace systems at the physical layer (PHY) where Digital Signal Processing (DSP) meets RF. RF System Design brings together baseband DSP algorithm modeling, accurate RF, trusted reference IP (such as SATCOM, 5G/6G, Wi-Fi, automotive and radars), and measurement automation in a single environment for optimal baseband DSP and RF co-design to eliminate wasteful iterations.



- RF design environment with integrated baseband DSP and RF simulators for designing physical layer (PHY) of communications and aerospace-defense systems.
- Industry first Phased Array Antenna system design with integrated RF system budget, antenna element-array configuration, and beamforming DSP algorithm co-simulation.
- Real-world libraries for Radar, EW, satellite, phased array, 5G NR-NTN, Wi-Fi, and automotive.
- Integration with ADS, Python, MATLAB and STK for complex, scenario-based RF System designs.
- Seamless AI ML integration for AI model import and training data generation for 6G and others.

Recommended RF System Design Bundles

System Design Core + Comms/DSP	W4802B
System Design Core + RF + Comms/DSP + Phased Array	W4804B
System Design + Aerospace Defense	W4811B
RF System Design + 5G and Cellular	W4812B
RF System Design + Automotive	W4813B
RF System Design + WiFi and Connectivity	W4814B
RF System Design SATCOM	W4815B
RF System Design Configuration	https://www.keysight.com/find/system-design https://www.keysight.com/find/system-design-config
Learn more	System Design (SystemVue) Keysight

RF Circuit Simulation Professional

RF Circuit Simulation Professional revamps circuit simulation and optimization technology for efficient and accurate design of the most challenging RF-microwave-mmWave circuits, RFICs, and RF 3D Heterogeneous Integrated (3DHI) modules for 5G/6G, wireless, radar, and aerospace-defense applications.



- Model components on silicon chips accurately.
- Optimize designs with sweeps and load-pull analysis.
- Simulate RF designs in Keysight Advanced Design System (ADS), Cadence Virtuoso, and Synopsys Custom Compiler environments.
- Increase performance using Monte Carlo and yield analysis.
- Assess Error Vector Magnitude (EVM) for the latest communication standards early in the design phase.
- Use the latest foundry technology immediately.

Recommended RF Circuit Simulation Pro Products Bundles

RF Circuit Simulation Professional (RF Circuit Sim Pro)

W5600E

RFPro Circuit Sim Pro Configuration

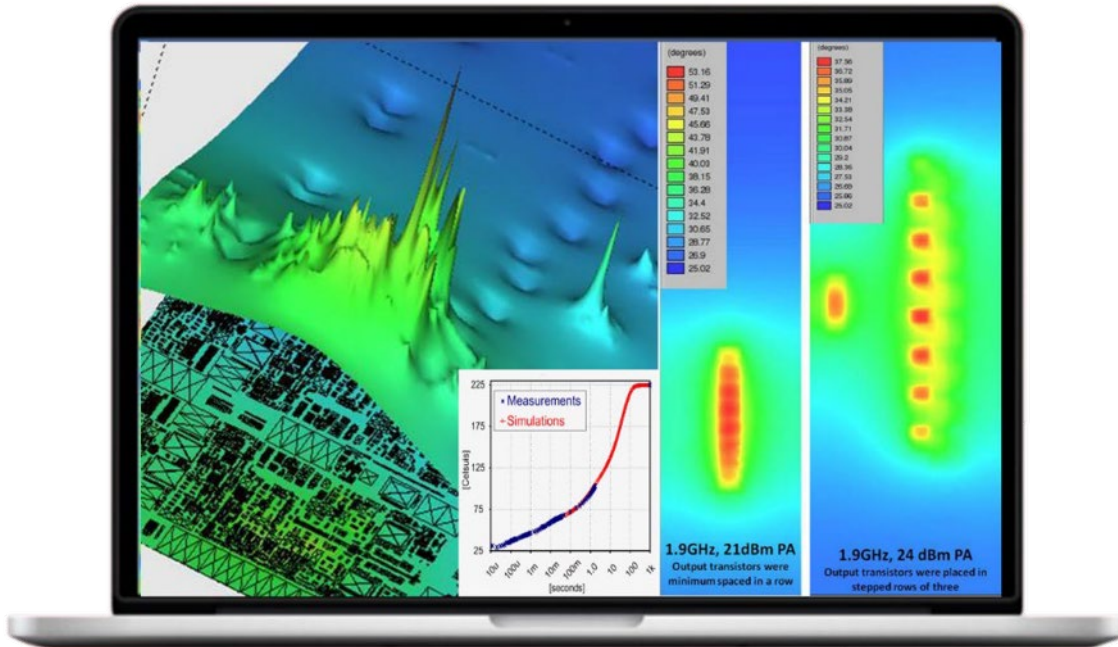
RF Circuit Simulation Professional |
Keysight

Learn more about IC foundry support for Process Design Kits (PDKs)

<https://www.keysight.com/find/design-software-foundries>

Thermal Design for IC (ADS ETH and Heatwave)

ADS Electrothermal (ETH) simulation suite integrates with ADS transient, steady-state, and envelope circuit simulators to account for layout dependent localized ETH heating from active devices during operation. ETH heating effects alter device characteristics, degrade circuit performance, and reduce reliability. Heatwave is an IC ETH simulator for chips and stacked-die System-in-Package (SIP) designs and works with non-Keysight circuit simulators such as Cadence Spectre and Synopsys HSPICE.



- Most accurate Electrothermal (ETH) simulator and can resolve fine temperature variations on ICs across device spacing and between layers.
- Predicts reliability, electrical performance degradation from self-heating in amplifiers, packaged RF modules, and full chip operation.
- Steady state, transient and circuit envelope electrothermal simulation with HPC acceleration.
- Improves accuracy of circuit simulation by accounting for layout dependent ETH heating effects during operation in actual deployment conditions.

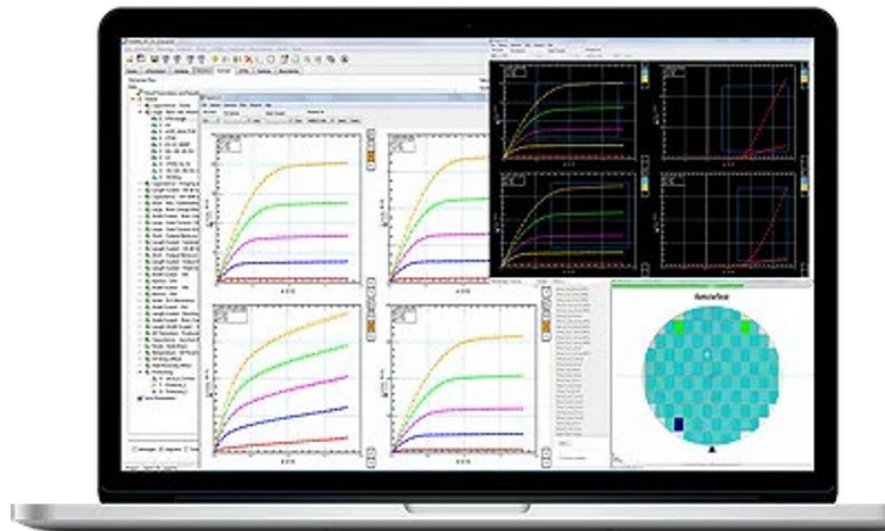
Recommended Thermal Design Bundles

Electrothermal RF (ADS Electrothermal ETH)	W3050E
Thermal Design Core (Heatwave)	W5900E
Thermal Design Steady-State Analog (Heatwave)	W5901E
Thermal Design (HeatWave) Configuration	

Device Modeling Suite (IC-CAP, MBP, MQA, WaferPro)

The accuracy of circuit simulation depends on the availability of accurate mathematical models to represent the active and passive devices in the circuit. Device modeling tools enable these models to be created by measuring the electrical characteristics of the devices; followed by mathematically adjusting the parameters in the model equations, until the simulated characteristics match the measured data. These tools are typically used by semiconductor foundries to create a library of accurate models in their Process Design Kit (PDK) for use by their customers to design ICs with.

Keysight Device Modeling tools include:



- **IC-CAP** (Integrated Circuit Characterization and Analysis Program) for modeling semiconductor devices built with Silicon CMOS, Bipolar, Gallium Arsenide (GaAs), Gallium Nitride (GaN), and other compound III-V foundry processes.
- **MBP** (Model Builder Program) for creating Silicon device models such as BSIM-BULK, BSIM-CMG, BSIM-IMG, UTSOI and others for logic, analog, and RF design applications. It features an automated model extraction flow for high volume silicon device model extractions.
- **MQA** (Model Quality Assurance) automates device model validation by comparing the results from simulating the same model with multiple industry standard SPICE simulators. It reports model issues so that they can be corrected by the foundry before releasing it in their PDK.
- **WaferPro** performs automated wafer-level probing, measurements, and high-volume data analysis to validate that wafer electrical characteristics are uniform across the entire wafer and across batches of wafers. This wafer-level testing is critical for semiconductor yield monitoring.

Recommended Device Modeling Bundles

IC-CAP device modeling platform bundle software license plus	W7300B
MBP model builder program core software license plus	W6300B
MQA model quality assurance core software license plus	W6800B
WaferPro Express A-LFNA measurement and programming bundle software license	W7802B
Advanced Low Frequency Noise Analyzer (A-LFNA)	W4727B
Device modeling (IC-CAP) configuration	https://www.keysight.com/find/device-modeling https://www.keysight.com/find/device-modeling-config
Model builder (MBP) configuration	https://www.keysight.com/find/model-builder https://www.keysight.com/find/model-builder-config
Model QA (MQA) configuration	https://www.keysight.com/find/model-qa https://www.keysight.com/find/model-qa-config
WaferPro (WaferPro Express) configuration	https://www.keysight.com/find/waferpro https://www.keysight.com/find/waferpro-config
Learn more about device modeling	https://www.keysight.com/find/whats-new-in-device-modeling
Learn more about device modeling service	Global Device Modeling Services Keysight
Contact Keysight about device modeling service	eda.training@keysight.com

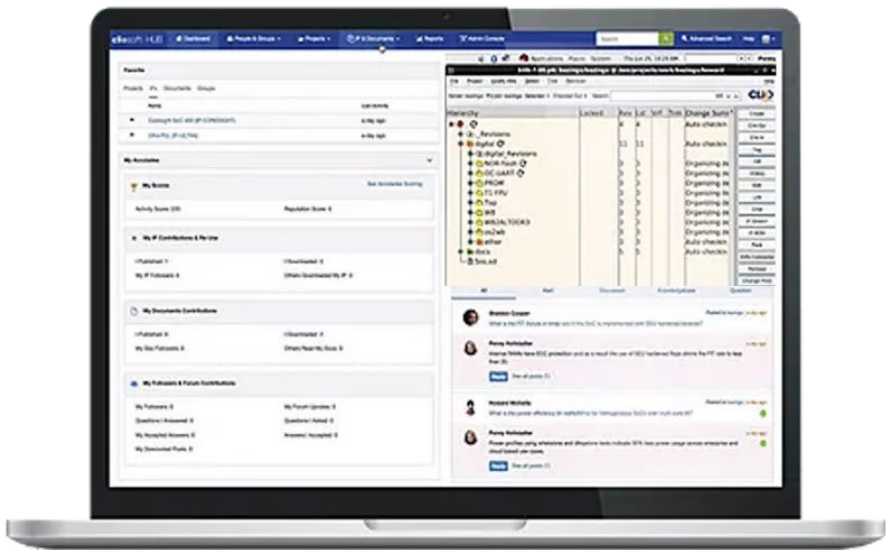
PDK Development

The Keysight PDK development team is there to support development of Process Design Kits (PDKs) for internal or external use. Our experts provide training, support, and development for PDKs to enable accurate design and simulation of RFICs and MMICs using ADS. Reach out to your EDA sales team or services manager to learn more.

Engineering Data Management - SOS

Engineering teams are overwhelmed by data chaos, design files scattered across systems, IP lost in email threads, and global teams working from different versions of the truth. This fragmentation leads to costly delays, compliance risks, and missed opportunities, while limiting the value of AI, which depends on clean, trusted, and structured data. Engineering data management is the foundation for speed, accuracy, collaboration, and AI readiness, transforming chaos into control and enabling innovation at scale.

Keysight Engineering Data Management (EDM) transforms design data into a structured, governed, and reusable asset across your organization.



- **SOS Core** empowers small to mid-sized teams with high-performance version control, EDA native workflows, and metadata-driven IP reuse.
- **SOS Enterprise** extends those capabilities to global teams, providing governance, traceability, and AI-ready infrastructure.

Together, they enable organizations to manage design data as a connected, intelligent asset, not just a collection of files. With industry certifications, a strong heritage, and proven scalability, Keysight SOS is the best choice for companies looking to move from control to intelligence, managing design data, connected and ready for the future.

Recommended Data and IP Management Bundles

SOS Core		WW1500B
SOS Enterprise		WW3500B
Engineering data management	Engineering Data Management	
SOS Core	SOS Core Keysight	
SOS Enterprise	SOS Enterprise Keysight	

High-Performance Computing (HPC)

High-Performance Computing (HPC) enables simulation to be performed significantly faster with up to 80%-time savings by using parallel simulations and powerful cloud computing resources. Keysight EDA offers HPC license for accelerating circuit, EM, and system simulation. The number of HPC licenses can be scaled up to increase parallelism for attaining the achievable time savings.



- Accelerate analysis with parallel simulation for up to 80%-time savings.
- Scale parallel simulation by adding HPC accelerators to the base simulator.

Base Simulator	HPC Accelerators	Part Number
3DEM	EM HPC Accelerator	W3039E
Linear, transient, harmonic balance	Circuit Sim HPC Accelerator	W3029E
Data flow	System Analysis HPC Accelerator	W4509E
Heatwave	ETH HPC Accelerator	W3059E
Learn more about HPC	https://www.keysight.com/find/design-cloud	

Winning with Keysight EDA

Guided Success

Design and simulate with confidence. Keysight Guided Success is a structured, expert-led solution designed to protect and maximize investment by accelerating time to value and ensuring continued performance year after year. Through guided onboarding, best-practice implementation, and ongoing optimization, our software specialists help reduce inefficiencies, limit rework, and drive consistent return across the full software lifecycle, transforming software spend into sustained business value rather than a one-time expense.

ACCELERATE PROFICIENCY. EMPOWER YOUR TEAM. MAXIMIZE ROI.

- A comprehensive, guided, outcome-driven journey that helps accelerate software adoption and maximize value for your Keysight Software investment.
- Expert guidance, tailored training, and ongoing support, ensuring every user works efficiently, confidently, and productively toward your business objectives. From first login to measurable results and faster outcomes.

Our program includes:

- **Success journeys meetings** – Ongoing alignment sessions to define goals, track progress and address challenges
- **On demand learning** – Self-paced training modules to build foundational knowledge and user confidence
- **Remote reviews** – Expert-led sessions to reinforce On Demand e-Learning and accelerate skill application
- **Professional services and support hours** – Direct access to Keysight specialists when and where you need it

Professional Services

- **Workflow integration** – Our team can implement or teach how to create automations to integrate Keysight EDA software into your business and engineering workflows.
- **Custom training** – Does your team need something not covered by our standard eLearning or instructor-led classes? Our experts will prepare what your team needs to succeed.
- **Professional services** – Need an expert? Have one of our experts work with your team to make the best decisions on the toughest problems. Our team will help your team use Keysight EDA tools to their fullest.

Recommended Keysight EDA Services:

Guided Success	E4747ED1 and E4747EL1
Remote dedicated training by Keysight Experts	N3249J
In-person dedicated training by Keysight Experts	N3249G
Custom training	N3249D
Custom solution service	E4725A
Learn more about Device Modeling Service	Global Device Modeling Services Keysight
Contact Keysight about the above services	eda.training@keysight.com

Conclusion

This Keysight Electronic Design Automation (EDA) technical overview introduces the distinct categories of EDA solutions needed to accurately design physical hardware and components in today's high frequency and high data bandwidth applications such as 5G/6G wireless, data center for AI/ML training, and satellite networks.

Each EDA category contains recommended bundles for getting budgetary quotes. Please discuss with your Keysight EDA specialist to verify your product selection and optimize your investment with Keysight Technologies, like thousands of satisfied customers around the world for over three decades

As a mission-critical design enablement partner, Keysight connects design, emulation, and test so customers can solve the world's most complex engineering challenges. Accelerate innovation at www.keysight.com.



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