

# M5302A Digital I/O

Two-slot PXIe high-speed arbitrary waveform generator module



## High-Performance Digital I/O

The M5302A is a single-slot digital I/O PXIe module with 28 programmable LVDS channels and 8 single-ended channels. The LVDS channels can be used to communicate to the device under test or can be used to control other devices by emulating protocols. The single-ended channels are suitable for event triggers or other general-purpose I/O applications.

There are 28 bidirectional differential channels included on a 100-pin, front panel connector. These differential signals are LVDS with a 1.2 Gbps maximum toggle rate. Eight SMBs are available as single-ended channels for instrument triggers or other general purpose I/O signals.

The M5300A is part of the Keysight M5000 Series – a high-performance PXI system that enables the latest innovations in quantum, aerospace defense, and more.

## Key characteristics

### Specification

LVDS Channels	
Number of channels	28
Channel impedance	100 Ohm differential
Maximum toggle rate	1.2 GBps
Trigger Channels	
Number of channels	8
Maximum output toggle rate	150 MHz
Maximum input toggle rate	75 MHz
PathWave FPGA and PathWave Test Sync Executive compatible	

For more information visit [www.keysight.com/find/M5000-Series](http://www.keysight.com/find/M5000-Series).