D9010MCDP

MIPI C-PHY and D-PHY protocol trigger and decode software for Infiniium oscilloscopes





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Product Overview

C-PHY and D-PHY are MIPI Alliance's physical layer (PHY) standards that provide high-throughput performance over bandwidth-limited channels to connect displays and cameras to an application processor. The standards provide a PHY for the MIPI Alliance's various Camera Serial Interface (CSI) and Display Serial Interface (DSI) specifications. This enables engineers to scale their implementations to support a wide range of higher resolution image sensors and displays, while keeping power consumption low. However, because both D-PHY and C-PHY support two application layers, engineers implementing a D-PHY or C-PHY interface must be able to see a device's signal integrity and protocol data to ensure it conforms to the MIPI specification.

MIPI serial buses are the backbone for communication in mobile products. The serial bus interface provides contentrich points for debug and test. However, since these protocols transfer bits serially, using a traditional oscilloscope has limitations. Manually converting captured 1's and 0's to protocol requires significant effort, can't be done in real-time, and includes potential for human error. As well, traditional scope triggers are not sufficient for specifying protocol-level conditions.

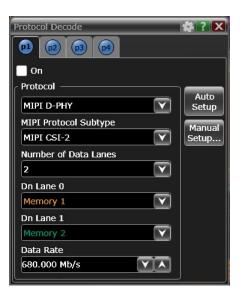


Figure 1. Configure the Infinitum oscilloscope to display D-PHY protocol decode in under 1 minute.

Key Features

Protocol searching

Included in Keysight's D9010MCDP software is a suite of configurable, protocol-level conditions for CSI-2 or DSI-2 interface buses that give engineers access to a rich set of integrated protocol-level searching capabilities. This protocol searching feature enables engineers to find and isolate error packets or specific patterns of interest from acquired data.

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Figure 2. Search specific CSI-2 or DSI-2 packets from the acquired protocol listing.

Protocol decode

D9010MCDP supports CSI-2 and DSI-2 protocol decode based on MIPI D-PHY and C-PHY. Perform and view decode information on both live and saved waveforms. Decode up to any combination of 4 live or saved waveforms.

The protocol list window is correlated between the waveform and selected packet on the list. The selected packet (highlighted blue row), is time-correlated with the blue line in the waveform display so that engineers can easily see the signal shape of specific packet data.

Keysight provides a unique C-PHY triggered eye diagram function for simultaneously monitoring signal quality and protocol. With this feature, engineers gain greater insight into the C-PHY signal and can better determine whether issues are due to signal quality or protocol data.

Keysight MIPI D-PHY protocol decoder software provides multilane decode up to 4 data lanes. User can select number of data lane from 2 to 4 lanes. However, due to oscilloscope's channel limitation, multilane decode will use recovered clock instead of existing clock in D-PHY. Also, same reason, multilane decode will support only HS data decoding, not LP data.



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Figure 3. The C-PHY protocol list window

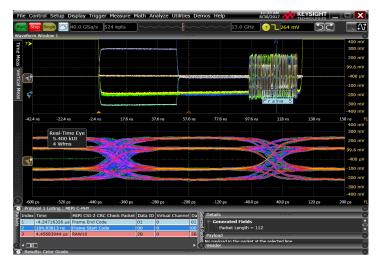


Figure 4. The C-PHY triggered eye diagram



Figure 5. The MIPI D-PHY multilane can decode up to 4-lane design implementation.



MIPI C-PHY

Overview

C-PHY requires few connectors than D-PHY, also does not require a separate clock lane, and provides flexibility to assign individual lanes in any combination to any port on the application processor via software control. Due to similarities in basic electrical specifications, C-PHY and D-PHY can be implemented on the same device pins. 3-phase symbol encoding technology delivers approximately 2.28 bits per symbol over a three-wire group of conductors per lane. This enables higher data rates at a lower frequency, further reducing power.

Recommended connection

To achieve a higher symbol rate from the DUT, Keysight recommends connecting 3 differential active probes for single-end probing. It is also possible to probe differentially so that data can be obtained without having to configure functions. However, due to the long length between positive and negative pins on the differential active probe, the probe bandwidth may not fully cover the signal's bandwidth range as required to see the right signal shape.

The C-PHY decoder software requires 3 differential signals for decoding C-PHY data. A single-end probed signal must therefore be changed to a differential signal for decoding. To accomplish this, use the "Subtract" function to make 3 differential signals from a 3-wire signal before starting to decode the C-PHY protocol. The required signals will be VAB(VA-VB), VBC(VB-VC) and VCA(VC-VA).

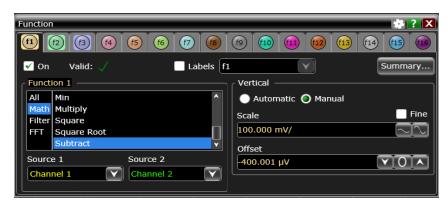


Figure 6. Channel configuration using waveform math.



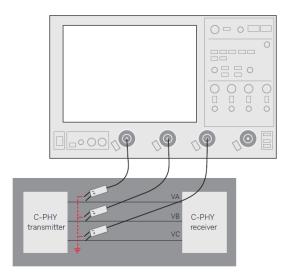


Figure 7. Recommended probe connection

MIPI C-PHY specifications and characteristics

Signal sources (VA, VB, VC)	Any analog channel Any waveform memory
Protocols supported	CSI-2 v1.3 DSI-2 v1.0
Data rate	Up to 3.5 Gsps
Supported lanes	One lane (3 wires) only
Auto setup	Automatically configures trigger levels, decode thresholds, sample rate, memory depth, holdoff, and trigger
Decode options (via search)	Short packet Long packet Errors: any error, bad CRC, bad PHCRC, unknown packet

MIPI C-PHY specifications and characteristics

MIPI D-PHY

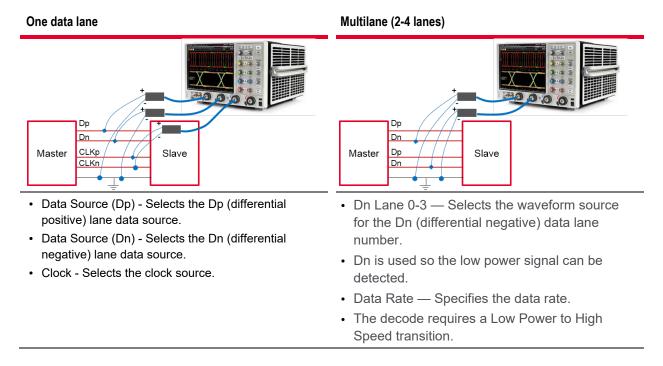
Overview

D-PHY is a serial interface technology using differential signaling for bandlimited channels with scalable data lanes and a source synchronous clock to support power efficient interfaces for streaming applications such as displays and cameras. It offers half-duplex behavior for applications that benefit from bidirectional communication at transmission rates up to 4.5 Gigabit per lane.



Recommended connection

Keysight MIPI D-PHY decoder supports both one lane and multilane decoding, based on your DUT configuration, each decoding mode require different connection. The table below describes each setup.



For more information, please refer oscilloscope's help file within the oscilloscope software.

MIPI D-PHY specifications and characteristics

Signal sources (clock, data)	Any analog channel Any waveform memory
Protocols supported	CSI-2 v1.3 DSI v1.01, v1.02, v1.30.10
Data rate	Up to 4.5 Gbps
Supported lanes	One data lane with clock (LPDT supports) 2-4 data lanes without clock (internal clock recovery)
Auto setup	Automatically configures trigger levels, decode thresholds, sample rate, memory depth, holdoff, and trigger
Decode options (via search)	Short packet Long packet Low power data transmission (one data lane case only) Errors: Any error, Bad CRC, Bad ECC, Unknown packet

MIPI D-PHY specifications and characteristics



Ordering Information

Recommended oscilloscopes

This protocol decode software is compatible with Keysight's Infiniium-Series oscilloscopes running software version 6.30 or higher. Bandwidth recommendations are as follows:

Standard	Data rate	Minimum bandwidth	Minimum channels	Recommended models
C-PHY	< 1.5 Gsps	4 GHz	3	MXR, EXR, or S-Series
C-PHY	< 2.5 Gsps	6 GHz	3	MXR, EXR, S, V, or UXR-Series
C-PHY	< 3.5 Gsps	10 GHz	3	V or UXR-Series
D-PHY	< 1.5 Gbps	4 GHz	3	MXR, EXR, or S-Series
D-PHY	< 2.5 Gbps	6 GHz	3	MXR, EXR, S, V, or UXR-Series
D-PHY	< 4.5 Gbps	12 GHz	3	V or UXR-Series

Recommended probes

Oscilloscope used	Probes recommended	Accessories recommended
MXR-Series EXR-Series S-Series	Model: 1169B 12 GHz InfiniiMax II	
V-Series Z-Series UXR-Series ¹	C-PHY: 3 active probes D-PHY: 4 active probes	N5442A 50Ω Precision BNC adapter

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¹ Depending on the bandwidth of UXR-Series, the channel connector size may vary, requiring different adapters. See UXR-Series datasheet for details.



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Node-locked	D9020ASIA	Advanced Signal Integrity Software (EQ, InfiniiSimAdv, Crosstalk)
		Node-locked perpetual license
Perpetual license	SW1000-LIC-01	Node-locked KeysightCare software support subscription with
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Test Suite	M-PHY TX Validation, HS-GEARs 1-4 (D9040MPHC)					
	 M-PHY TX Validation, HS-GEARs 1-5 (D9050MPHC) C-PHY and D-PHY Protocol Validation (D9010MCDP) 					
	M-PHY Protocol Validation (D9010MPMP)					
	LowSpeed Protocol Validation (D9010MPLP)					

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