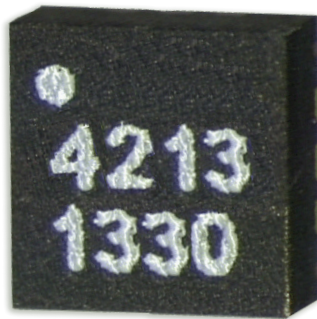


Keysight 1GG5-4213

0.2 – 20 GHz Packaged Integrated Directional Detector

Data Sheet



Features

- Frequency range: 0.2 to 20 GHz
- Coupling flatness: ± 1 dB
- Directivity: > 15 dB
- Return loss: > 15 dB
- Insertion loss: < 1.5 dB
- Sensitivity: 18 mV/mW
- Max Input power:
 - 25 dBm @ 70 °C
 - 2:1 source VSWR
 - Output open circuit (see Figure 5)
- RoHS Compliant SMT
- 2 mm x 2 mm QFN surface mount package

Description

The 1GG5-4213 is a low-loss, directional detector with an integrated diode, capacitor, and resistors on chip. It is fabricated using the Keysight Technologies, Inc. Modified Barrier Schottky Diode process and is packaged in a compact and easy to mount and low cost QFN package. No external resistors are required for standard operation.



- Package type:
Quad flat - no leads (SMT QFN)
- Package dimensions:
2.0 x 2.0 mm (0.079 x 0.079 in)
- Package thickness:
0.85 ±0.10 mm
(0.035 ±0.0039 in)
- Lead pitch: 0.40 mm (0.016 in)
- Lead width: 0.20 mm (0.008 in)

Absolute maximum ratings^{1 2}

Symbol	Parameters/conditions	Min	Max	Units
P_{max}	Max instantaneous input power (burn-out damage limit)		25	dBm
T_{stg}	Storage temperature		150	°C
T_{bs}	Package backside temperature	-40	+85	°C
T_{stg}	Storage temperature	-65	+150	°C
T_{assy}^3	Maximum solder reflow temp. (max. 3 cycles @ 30 sec./cycle)		+260	°C

1. Parameters specified for continuous operation at $T_{bs} \leq 85^\circ\text{C}$.

2. Operation in excess of any one of these conditions may result in permanent damage to this component.

3. Refer to JEDEC J-STD-020D for detailed reflow profile, 3 reflows max.

Application

The 1GG5-4213 is commonly used in ALC (Automatic Leveling Control) loops and power detection at device input and output ports while providing minimum insertion loss..

Moisture Compatibility

Injection mold components like the 1GG5-4213 in QFN are moisture-sensitive. The product is tested to the Moisture and Reflow Sensitivity Level 3 as per IPC/Jedec J-STD-020 and must be mounted within 168 hours of opening the shipping container. Store and handle parts for reflow and for rework per IPC/Jedec J-STD-033B. An example of the moisture sensitivity label is shown in Figure 1.

Tape and Reel

The 1GG5-4213 is available in tape and reel format to facilitate automatic pick and place manufacturing. See Figure 8.

RoHS Compliance

This device is RoHS Compliant. This means the component meets the requirements of the European Parliament and the Council of the European Union Restriction of Hazardous Substances Directive 2011/65/EU, commonly known as RoHS. The six regulated substances are lead, mercury, cadmium, chromium VI (hexavalent), polybrominated biphenyls (PBB) and polybrominated biphenyl ethers (PBDE). RoHS compliance implies that any residual concentration of these substances is below the RoHS Directive's maximum concentration values (MVC); being less than 1000 ppm by weight for all substances except for cadmium which is less than 100 ppm by weight.

CAUTION
This bag contains
MOISTURE-SENSITIVE DEVICES

See
Package

If Blank, see adjacent
bar code label

1. Calculated shelf life in sealed bag: 12 months at < 40°C and < 90% relative humidity (RH).

2. Peak package body temperature: 260 °C

If Blank, see adjacent bar code label

3. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must
a) Mounted within: See Package hours of factory

If Blank, see adjacent bar code label

conditions ≤ 30 °C/60%.

b) Stored at <10% RH.

4. Devices require bake, before mounting, if:
a) Humidity Indicator Card is >10% when read at 23± 5 °C
b) 3a or 3b not met.

5. If baking is required, devices may be baked for 48 hours at 125± 5 °C.

Note: If device containers cannot be subjected to high temperature or shorter bake times are desired, reference IPC/JEDEC J-STD-033 for bake procedure.

Bag Seal Date: See Package

If Blank, see adjacent bar code label

Note: Level and body temperature defined by IPC/JEDEC J-STD-020

Figure 1. Moisture sensitivity label

ESD and Handling Precautions

GaAs MMICs in either chip or SMT packages are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly.

MMIC ESD precautions, handling considerations, die attach and bonding methods are critical factors in successful GaAs MMIC performance and reliability.

The Keysight Technologies *GaAs MMIC ESD, Die Attach and Bonding Guidelines - Application Note* (5991-3484EN) provides basic information on these subjects.

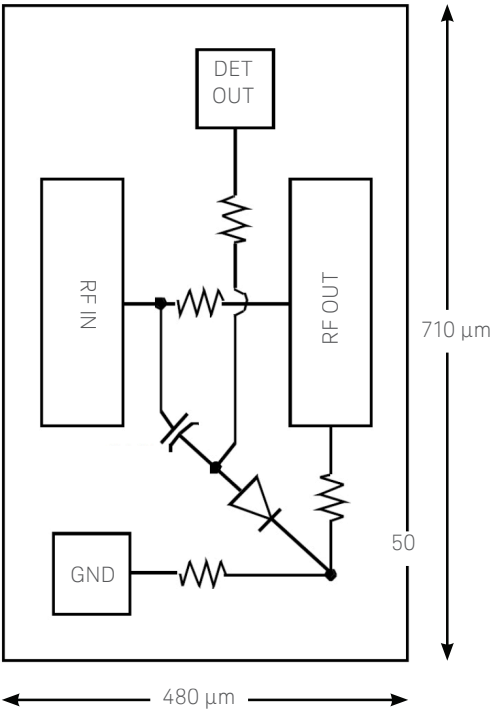


Figure 2. 1GG5-4213 schematic

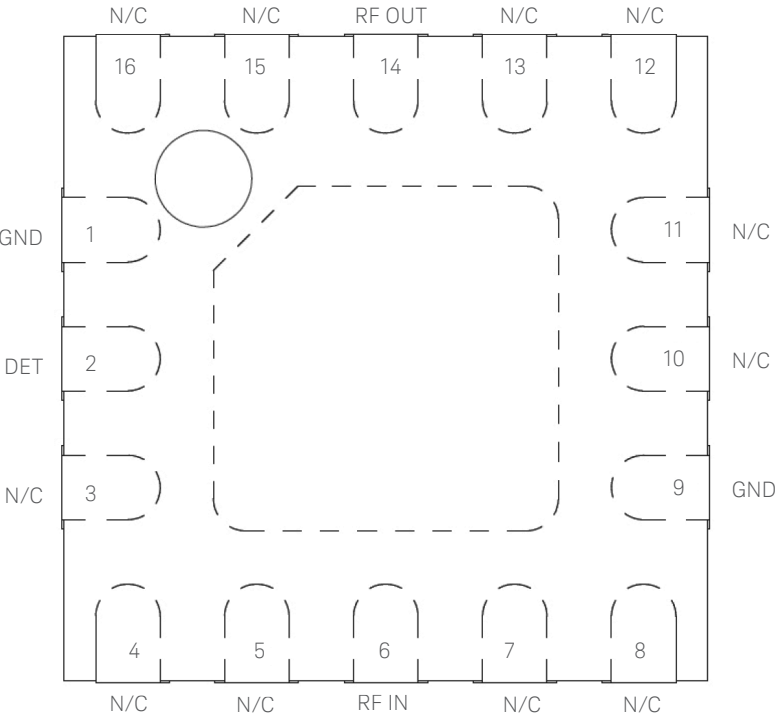


Figure 3. 1GG5-4213 pin out diagram

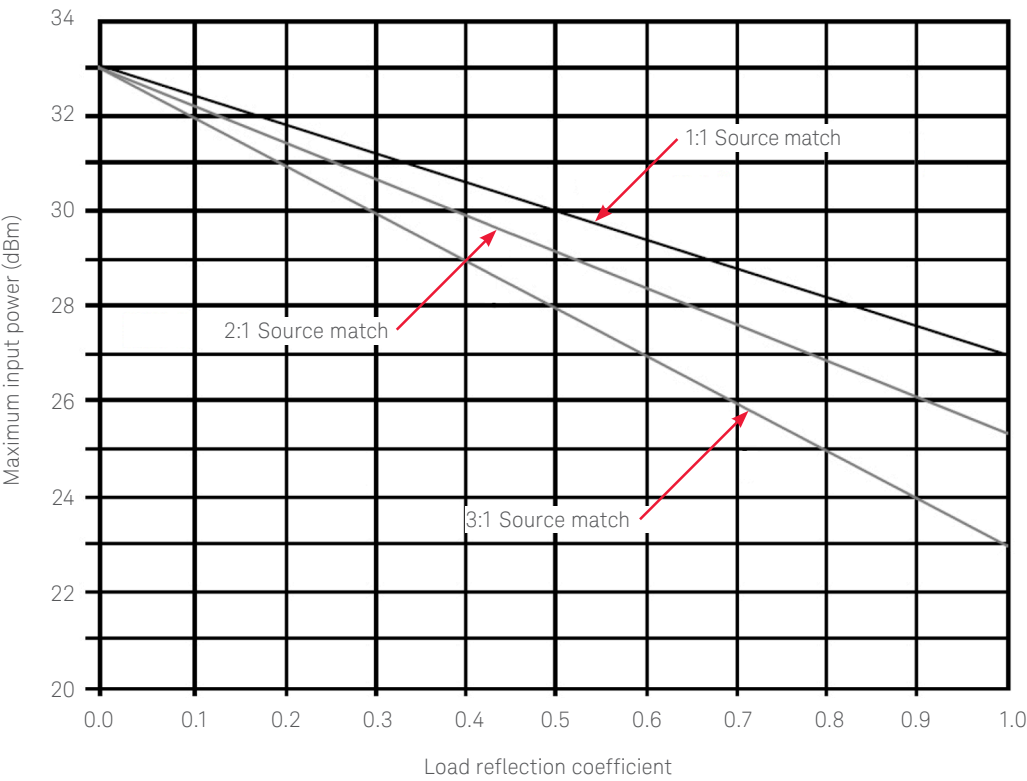


Figure 5. 1GG5-4213 safe operating region T_{case} : +70 °C

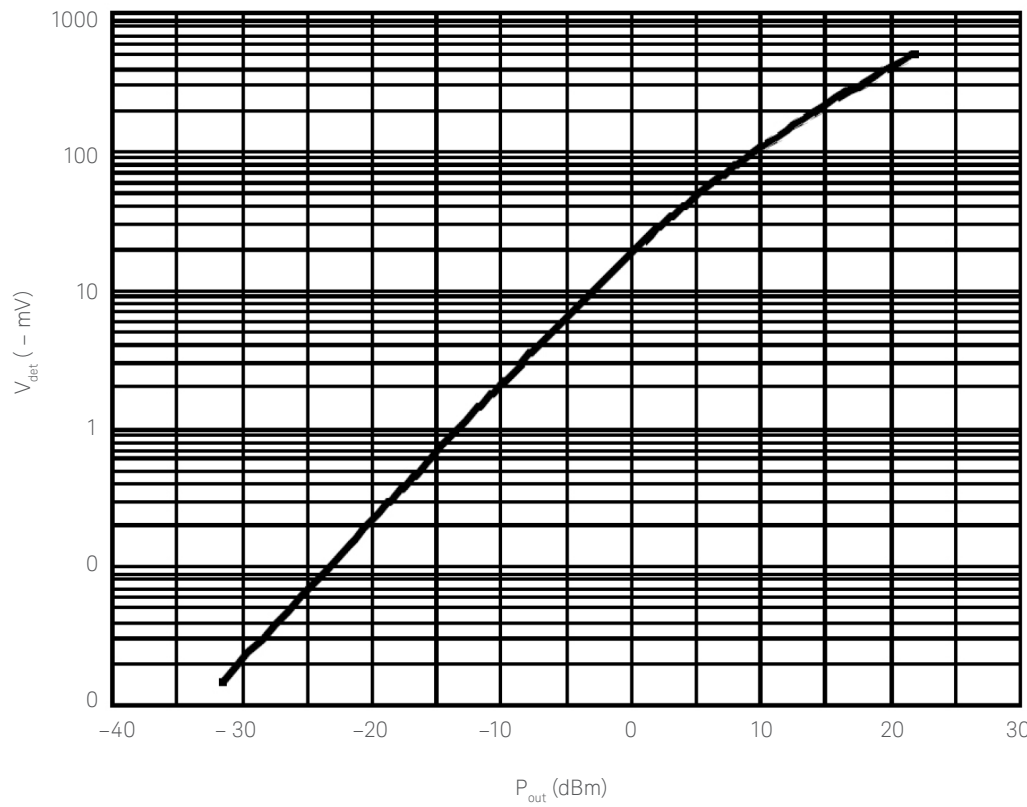


Figure 6. 1GG5-4213 Typical transfer characteristic temp = +25 °C

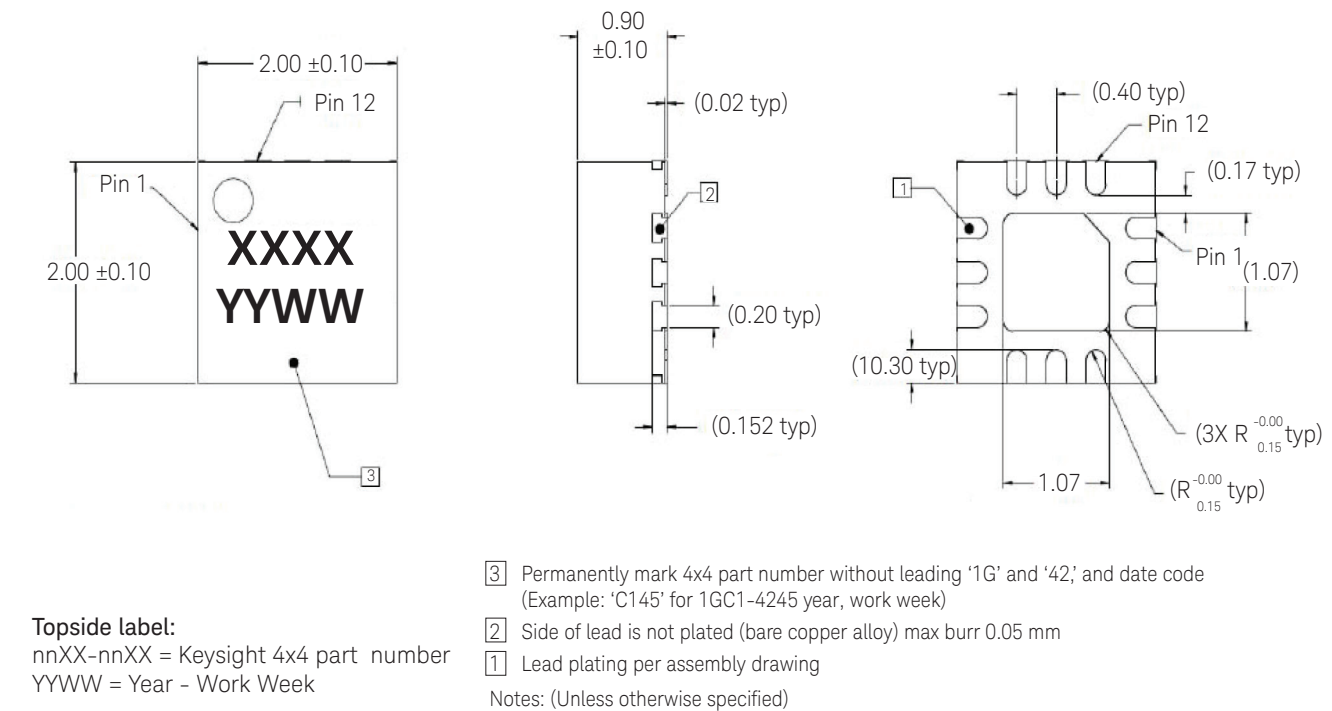


Figure 7. 1GG5-4213 dimension drawing (dimensions in mm)

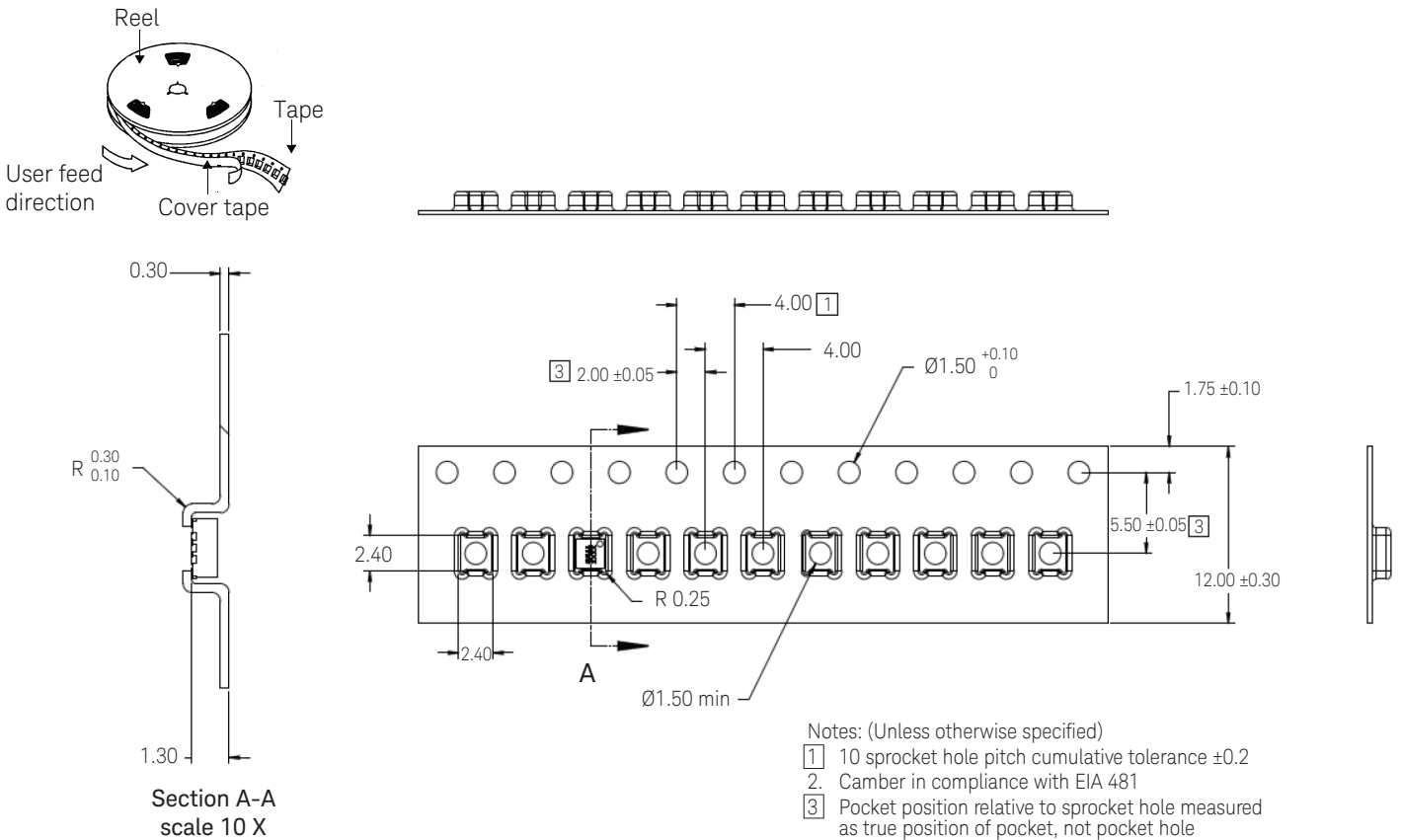
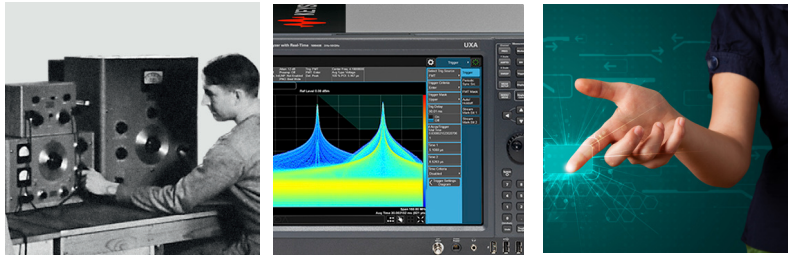


Figure 8. Tape and reel information

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This data sheet contains a variety of typical and guaranteed performance data. The information supplied should not be interpreted as a complete list of circuit specifications. Customers considering the use of this, or other Keysight Technologies GaAs ICs, for their design should obtain the current production specifications from Keysight. In this data sheet the term typical refers to the 50th percentile performance. For additional information contact Keysight at MMIC_Helpline@keysight.com.

The product described in this data sheet is RoHS Compliant and RoHS Process Compatible with a maximum temperature of 260 °C and a maximum of 3 temperature cycles.

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