

PathWave Device Modeling (IC-CAP)

Complete and accurate measurement and parameter extraction solution for semiconductor device modeling

About PathWave Device Modeling (IC-CAP)

PathWave Device Modeling (IC-CAP) is the industry standard software for characterization and modeling of DC, analog, and RF semiconductor devices. IC-CAP extracts accurate models and sub-circuits for high-speed/digital, analog, power electronics, and power RF applications. It offers device engineers and designers a state-of-the-art modeling tool that fills numerous modeling needs, including automated instrument control, data acquisition, parameter extraction, graphical analysis, simulation, optimization, and statistical analysis. IC-CAP combines these capabilities in a flexible, automated, and intuitive software environment to extract efficiently and accurately active, passive, and user-defined devices and circuits.

Today's most advanced semiconductor foundries and Integrated Device Manufacturers (IDMs) rely on IC-CAP for modeling silicon CMOS, Bipolar, compound gallium arsenide (GaAs), gallium nitride (GaN), and many other device technologies.

A New Beginning

The 2021 release introduces a new, modern look and feel. The User Interface (UI) design reflects the new company adopted Keysight PathWave UI style standards featuring new light and dark themes and redesigned dialogs, icons, fonts. New usability enhancements mark a new beginning for Keysight's flagship Modeling Platform along with the new UI. IC-CAP now provides an immersive Python 3 experience with expanded package support, integrated plotting, and custom linkage to remote installations.

Features at a glance

- Open software architecture enables you to achieve maximum accuracy by integrating your modeling expertise and methodologies and provides ultimate flexibility to create and automate measurement, extraction, and verification procedures.
- IC-CAP provides access to unique nonlinear high-frequency modeling for GaN and other semiconductor devices. Supported models include the recent Compact Modeling Council (CMC) GaN models, ASM-HEMT and MVSG, Keysight's DynaFET, NeuroFET, and Root. Models for high-frequency BJT, MOSFET, MESFET, PHEMT devices are also available.
- Python 3 and PEL Programming enable advanced data processing, automation, instrument links, and display capabilities. IC-CAP GUI Studio and PySide2 allow you to create UI to integrate any custom extraction or characterization strategy.
- Turnkey extraction solutions for industry standard CMOS models, such as BSIM3/BSIM4, PSP, HiSIM, and HiSIM_HV, minimize the learning curve and maximize model accuracy.
- Direct links to most commercial simulators (e.g., ADS, HSPICE, Spectre, and Eldo) ensure consistency between extracted models and the simulators used by circuit designers.
- The IC-CAP Wafer Professional (WaferPro) add-on enables on-wafer automated measurement and characterization.
- Robust data handling capabilities.
- IC-CAP Power Electronics Model Generator (PEMG) enables the extraction of SiC MOSFET, GaN HEMT, and IGBT power device models and works in conjunction with Keysight PD1000A Measurement System.
- IC-CAP Model Generator (MG) enables the advanced extraction for all the devices. It fully integrates Python 3 to access key components (Data import, Flows, Scaling plots, De-embedding, Re-centering, Simulator technology, etc.)

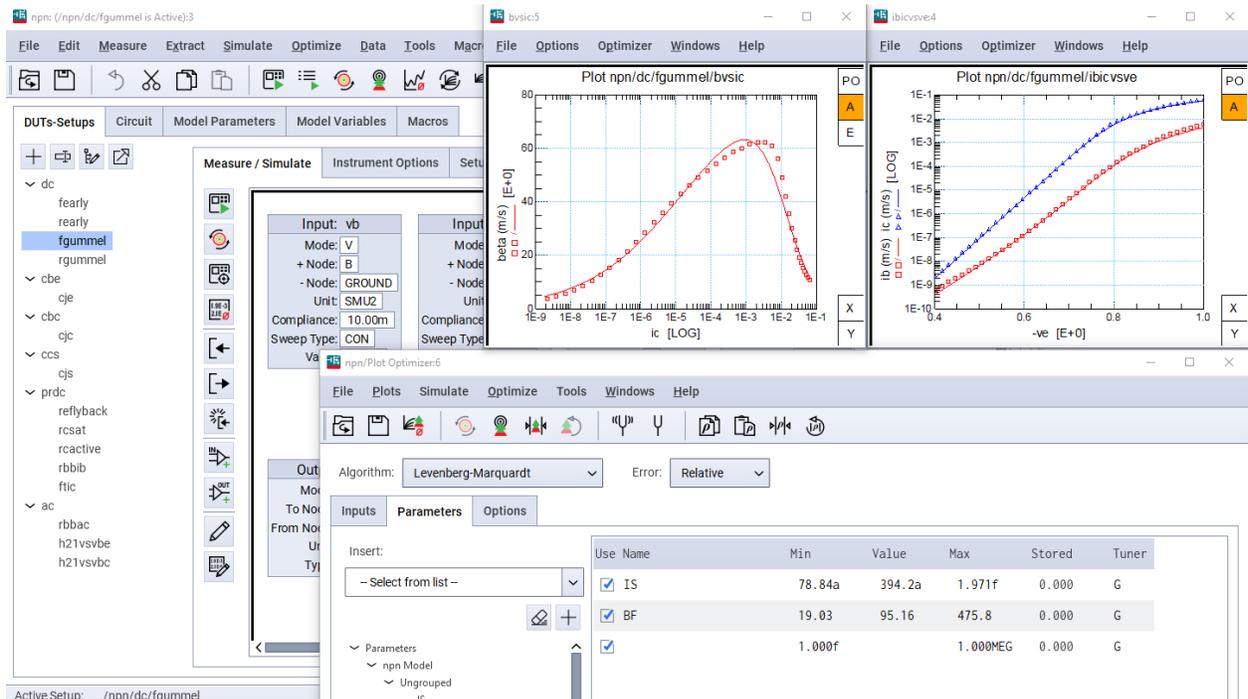


Figure 1. PathWave IC-CAP Project, Plot Optimizer, and Display windows.

The PathWave Device Modeling Platform

Industry challenges

The semiconductor industry faces continuing challenges to maximize product performance and yield, decrease time-to-market, and reduce production costs. As technology nodes get smaller and smaller, the need to use accurate models and control statistical variations in device processing performance becomes ever more critical. With 5G applications, typical circuit operating frequencies continue to advance well into the RF and microwave frequency range. Circuit designers need models that can accurately predict device behaviors at DC, RF, and millimeter-wave regions.

The amount of data measured for device modeling purposes has been increasing exponentially. With measurements taking several hours or even days, it is essential to be as efficient as possible without compromising measurement accuracy. Measurement control software must work in conjunction with the prober's native control software and each instrument to allow automated measurements across temperature.

Keysight device modeling solution

PathWave Device Modeling (IC-CAP) addresses these challenges and provides significant competitive advantages to companies within the semiconductor industry.

IC-CAP modeling software offers modular products so that you can choose precisely the modules required for your particular modeling scenario. Central to the IC-CAP platform is the IC-CAP software environment, which supports graphical analysis, programming via parameter extraction language (PEL) and Python, and custom model and user interface development. The analysis module enables simulation, optimization, statistical analysis, and interfacing to external simulators. IC-CAP supports an extensive list of measurement instruments, including industry-standard DC Analyzers, LCRZ Impedance Analyzers, and Network Analyzers.

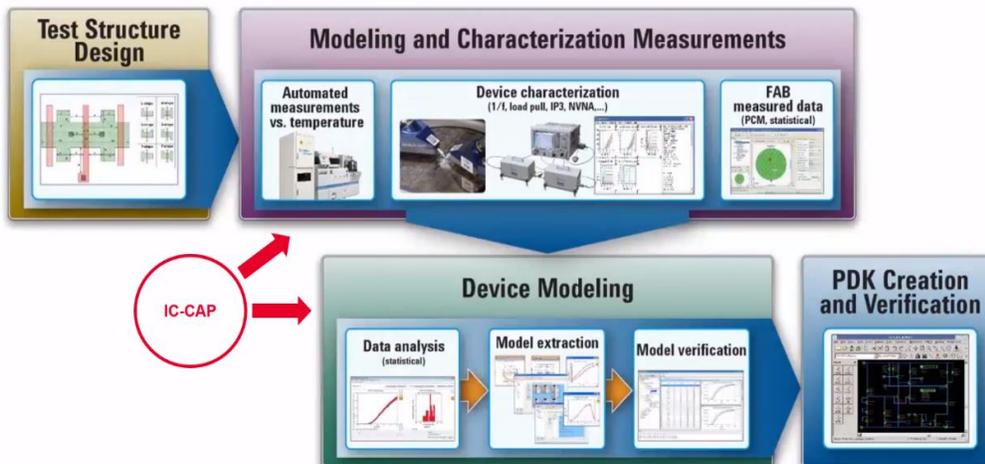


Figure 2. The Device Modeling Flow and IC-CAP.

Accurate Keysight proprietary and industry-standard models

IC-CAP enables extracting accurate models for building and maintaining up-to-date model libraries. Within a single environment, you can use IC-CAP to automate measurements, simulate device performance, extract data, optimize model parameters, perform statistical analysis, and generate best-case and worst-case models. IC-CAP provides extraction routines for industry-standard and Keysight proprietary models such as diodes, BJT, MOSFET, MESFET, HEMT, noise, thermal, and more.

Extraction modules offer complete DC to RF parameter extraction capabilities. IC-CAP also supports models and extraction routines developed by third parties and numerous other simulation software packages to accommodate a wide range of customer requirements. IC-CAP also supports the use of Verilog-A models.

RF and microwave modeling capabilities

Accurate modeling of RF effects requires reliable measurement data. Building on proven strengths in RF and microwave test and measurement, Keysight EDA provides configurations for various RF instruments such as the Keysight PNA, PNA-X, and ENA series. IC-CAP RF extraction modules for proprietary and industry-standard models include RF-dependent parameter extraction, ensuring your models are suitable for high-frequency circuit simulation.

GaN modeling

IC-CAP offers complete coverage for GaN devices, from the traditional, empirical-based, Angelov-GaN model to the most recent physics-based models recently promoted to industry standards by the Compact Modeling Council, the ASM-HEMT, and MVSG models. For maximum accuracy, Keysight offers the Artificial Neural Network (ANN)-based DynaFET model. For more details on GaN modeling, please see the GaN Modeling section in the Product Configuration chapter.

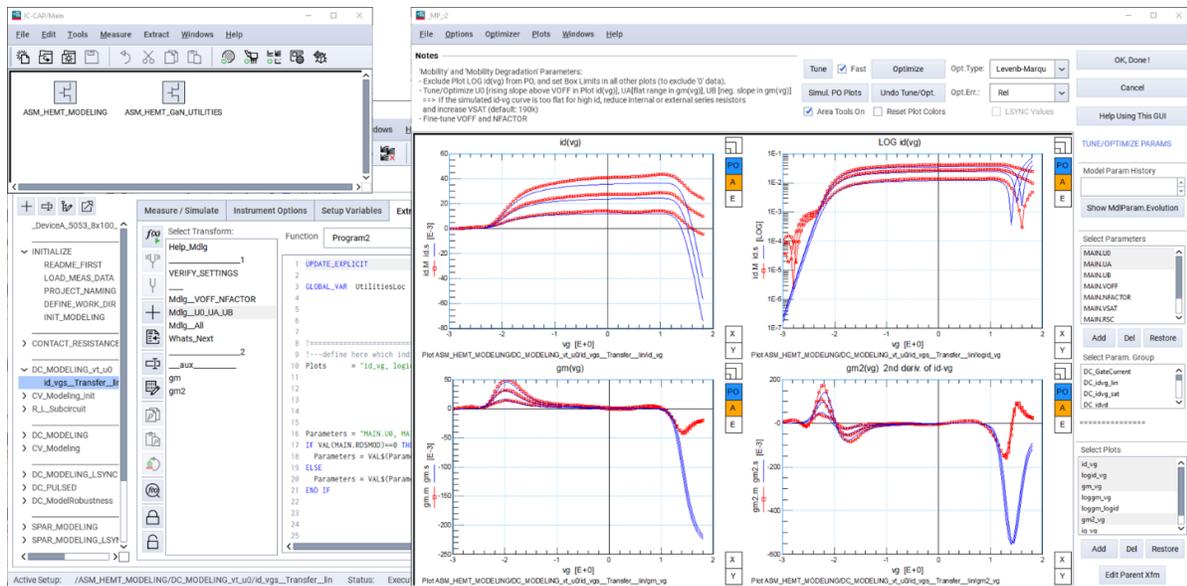


Figure 3. The GaN CMC ASM-HEMT model extraction example.

Power Electronics (PE) modeling

Keysight's Power Electronics Model Generator (PEMG) supports three different power electronics device models. These models have been implemented in Verilog-A and are available in ADS. The Si/SiC MOSFET and IGBT models are for customers who do not have access to the device's detailed process parameters, a typical case in power electronics design labs.

The most flexible software environment

IC-CAP features an open and flexible software architecture. Although we provide turnkey modeling solutions for many industry-standard and proprietary models, measurement and extraction algorithms can be customized. Using the IC-CAP parameter extraction language (PEL) or the Python programming environment, you can directly define and add your models or extraction methodologies into IC-CAP. When necessary, the IC-CAP open measurement interface allows you to write your measurement drivers to control instruments. It is also possible to design custom modeling packages by implementing custom user interface dialogs with IC-CAP GUI Studio.

The most advanced automated measurement solution

With IC-CAP WaferPro, IC-CAP provides the most advanced solution for device modeling automated measurement. A dedicated test plan environment within the IC-CAP Platform allows the test engineer to measure and post-process data using various Keysight and third-party instrumentations.

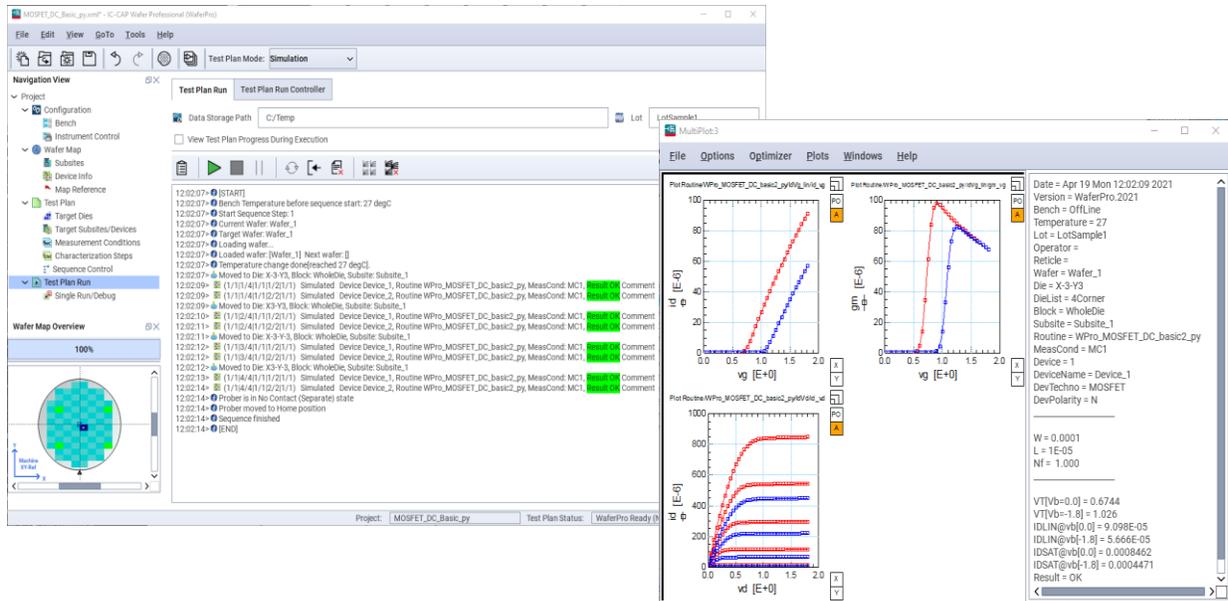


Figure 4. IC-CAP WaferPro Test Plan log information and display capabilities. Also shown, the DUT information and the calculated data (or spot data) included in this routine.

PathWave IC-CAP Platform Key Components

Advanced Graphical User Interface (GUI)

The IC-CAP GUI enables you to create and manage measurement and modeling projects, read, organize, and display data using single or multiple plot window displays. Multiplot windows allow for unlimited plots, easy navigation, and zooming capabilities.

Python 3 and PEL programming

IC-CAP organizes measured and simulated data in units called "Setups." Data are processed and displayed in real-time using Python 3 or PEL scripts. IC-CAP PEL is a simple-to-learn language similar to HP Basic. Python is an open-source, modern, object-oriented, and very efficient language that maximizes speed and productivity.

By writing PEL and Python scripts, you can process data, create extraction and measurement algorithms within the IC-CAP environment. Both PEL and Python allow you to develop new models and extraction routines interactively and modify existing extraction modules, making IC-CAP an extraordinarily open and flexible working environment.

IC-CAP now supports Python 3. IC-CAP Python 3 standard configuration now includes additional packages like Pandas, SymPy, H5Py, OpenPyXL, and the most updated version of SciPy, NumPy, Matplotlib (see the documentation for a complete list of packages). IC-CAP Python 3 features:

- Convenient Python 2 to 3 script to convert legacy Model files to Python 3
- Python demos and examples now use Python 3
- Python 3 modeling application examples to showcase the power of Matplotlib, NumPy, and Pandas

Efficient data management

The IC-CAP Data Manager allows you to import/export data in an ASCII file format. This format, which has extension .mdm, is now recognized as a standard in the device modeling community, and many measurement tools export data into this IC-CAP unique format. Data in other file formats such as .csv, .s2p, and .xls can also be exported and imported.

Wide choice of industry standard simulators

Each IC-CAP project can simulate a user-defined netlist that typically includes the model card and, optionally, a subcircuit representing the model. The IC-CAP simulator engine creates and maintains a Parameter Table based on the model and subcircuit parameters. IC-CAP includes SPICE simulators and provides direct links to several external simulators (see the complete list of supported simulators in Table 6). The analysis license includes the ability to simulate linear and transient analysis with the powerful Keysight ADS simulator at no extra charge.

Powerful optimizers

IC-CAP offers 13 optimization algorithms. Using a combination of different optimization algorithms can be a real advantage when enhancing the model's fit. Besides automated optimizers and manual tuners, which PEL or Python can invoke, a powerful tool called Plot Optimizer makes dynamic interactive optimization easy. The Plot Optimizer is a user interface that enables you to set up all parameter optimization tasks on the fly quickly. You can open the Plot Optimizer from every IC-CAP plot and automatically load the target and simulated data for quick tuning and optimization.

Automation with macros

Tasks within an extraction, or even entire extraction routines, can be automated with macros. A macro is a single programming routine that executes a series of IC-CAP commands, functions, PEL or Python programs, or calls to the user environment. With macros, extraction routines developed in R&D can be automated and leveraged in your production areas to minimize user interaction and maximize productivity.

IC-CAP GUI studio

The GUI Studio adds powerful capabilities for custom graphical user interface development to the highly flexible IC-CAP software environment. Engineers use IC-CAP Studio to develop custom user interfaces that automate and simplify an entire measurement or extraction process flow. The optimized UI modeling environment can then be shared and exchanged with other colleagues or outside customers who can easily comprehend the flow and quickly perform the necessary measurement and extraction steps.

Model generator

The Model Generator (MG) is a new workflow manager that helps device modeling engineers perform model extraction on any device faster and more efficiently. It offers several useful features:

- Automatic import of measured data (DC, CV, and S-parameters when available)
- New UI, fast data visualization, preconfigured and flexible extraction flow
- Automatic load circuit and subcircuit in each DUTs. Allow users to import or export a Circuit Netlist (.cir) to or from a Model Generator project
- Scalability plots can be efficiently generated against various parameters, including geometry, temperature, bias, and frequency
- Performing RF de-embedding, recentering, and modeling of corners with the use of plots in the extraction flow
- Easily validate the extracted model and create customized reports
- Including many examples to help users

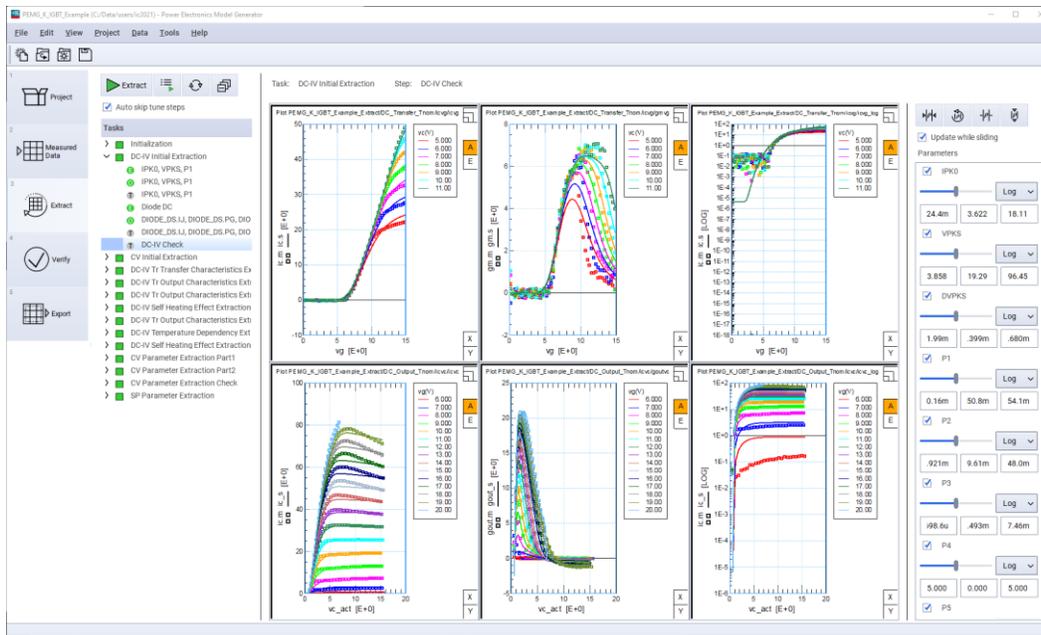


Figure 6. PathWave IC-CAP Power Electronics Model Generator (PEMG).

Making Measurements and Viewing Data with IC-CAP

Successful device modeling requires accurate measured data and a thorough understanding of the complex integration between the measurement hardware and the modeling software. The IC-CAP software is a powerful modeling tool that can automate industry-standard instruments and systems. Its measurement interface provides turnkey measurement drivers for many single instruments and modeling systems configuration for DC, CV, and RF measurements. See Table 4 for a list of supported instrument drivers.

PathWave IC-CAP WaferPro

PathWave IC-CAP WaferPro drives industry-standard measurement equipment (from benchtop instruments to parametric testers), as well as third-party probes, switch matrixes, and thermal chucks to execute efficient, automated on-wafer measurements across temperature. IC-CAP WaferPro deploys a custom library of efficient measurement routines (e.g., adaptive measurement algorithms) to significantly reduce the overall measurement time.

Since the measurement routines are in the IC-CAP environment, either complex or straightforward post-processing (such as calculating spot measurements or figures of merit, RF de-embedding, and direct extraction) applies to measured data before saving. Swept data are typically saved to IC-CAP MDM file format, while spot measurements to Excel .csv files. See Table 5 for a list of probes, switching matrixes, and thermal chucks supported by WaferPro. The W7311B PathWave IC-CAP Wafer Professional Measurement Bundle includes the IC-CAP Environment, WaferPro, and all measurement drivers.

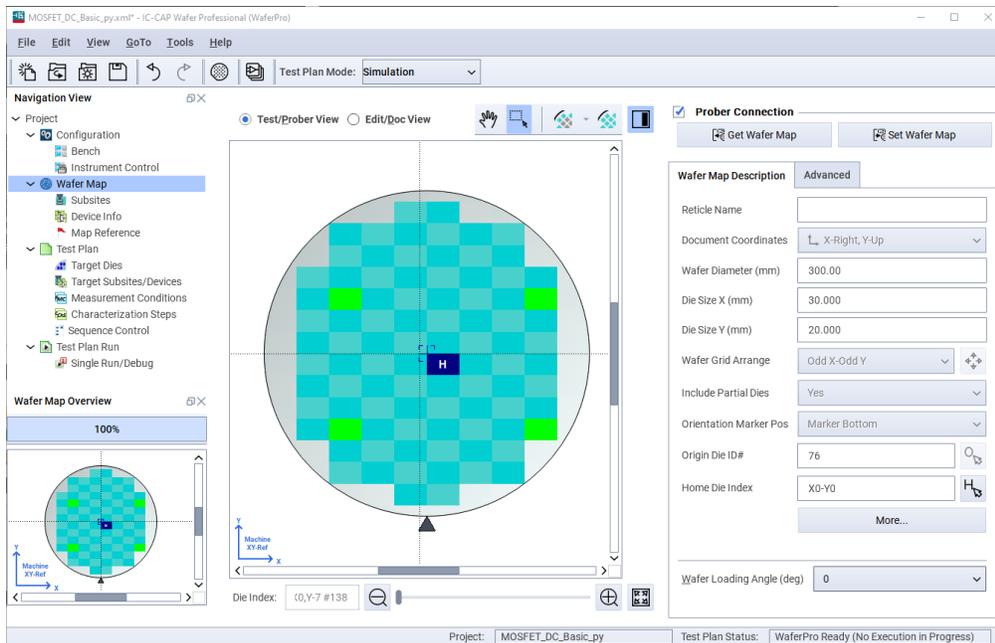


Figure 7. IC-CAP WaferPro's user interface enables prober control with wafer map visualization.

IC-CAP DataPro

IC-CAP's Data Selection and Processing tool (DataPro) allow users to find the "golden" or most typical die for modeling. DataPro applies statistical analysis on measured data and custom critical figures of merit (such as gm, ring oscillator freq). Based on statistical distribution and variances, DataPro identifies outliers devices and excludes them from further analysis. DataPro identifies the typical (mean) and corner devices and exports their data to other tools for target, typical, or corner modeling. The W7300B PathWave IC-CAP Device Modeling Platform Bundle includes IC-CAP DataPro at no additional cost.

Data viewer for WaferPro and WaferPro express data

An automated test plan in IC-CAP WaferPro or WaferPro Express software produces measurement data organized into a deep hierarchy of subdirectories based on lot name, wafer, temperature, device type, polarity, reticle location, and specific measurement parameter groups. In plotting device data from different reticles side-by-side, this hierarchy can be cumbersome to navigate. The IC-CAP Data Viewer automatically navigates the directory structure, finds relevant data, and generates plots, making it easier to study data across different devices. The most common plots are now easily generated, including measurements over bias and statistical data from across the wafer, presented either as a histogram or a wafer map distribution.

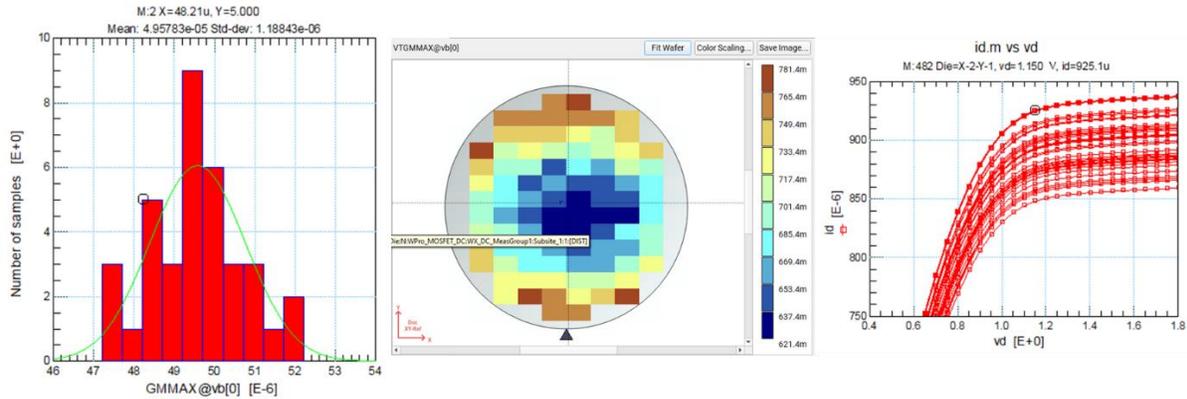


Figure 8. IC-CAP Data Viewer lets you sort and display measured data generated by WaferPro. The picture shows the various type of diagrams used to study the data distribution across the wafer: histograms, color/bin mapping, and multi-curves.

Extracting Models with IC-CAP

A typical modeling flow first involves selecting a model based on the device technology and its final circuit application (e.g., DC, high frequency, or both). Next, make the necessary measurements to characterize a device or a set of devices, and then finally apply an extraction algorithm to calculate the model parameters. This last step is achieved by either calculating the parameters using built-in or custom model equations from measured data or by tuning or optimization techniques.

With IC-CAP, you can either investigate and implement your extraction methodology or use one of the turnkey modeling extraction packages and be up and running by day one. Keysight and third-party modeling experts provide an extensive collection of extraction packages and examples.

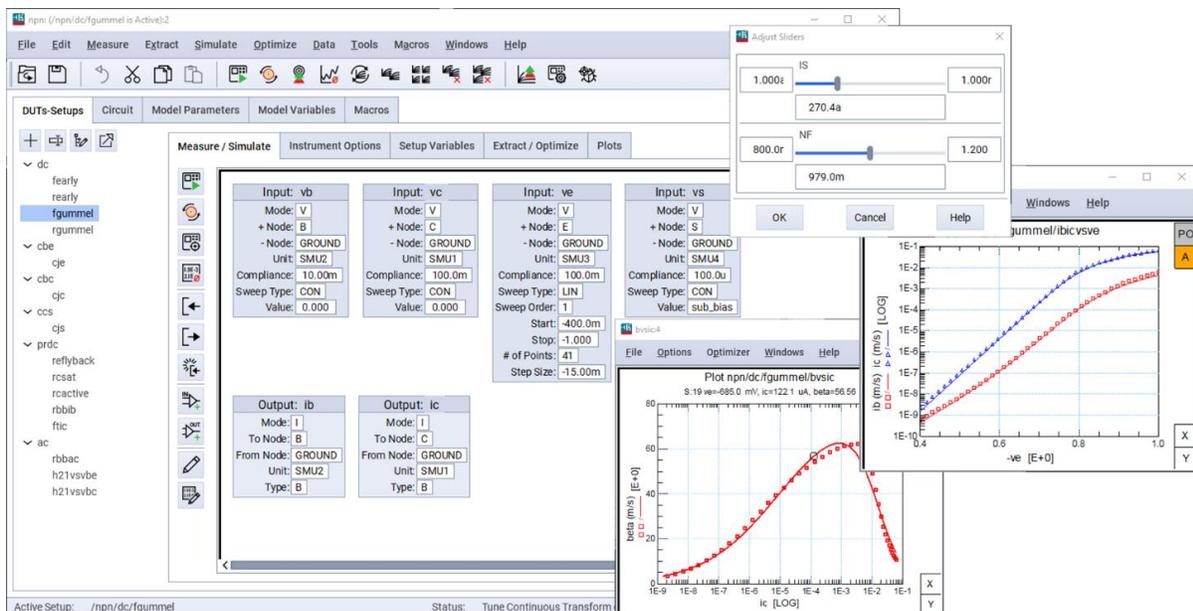


Figure 9. Tuning model parameters to match measured and simulated data of a basic BJT model.

Model Generator (MG)

The IC-CAP-based modeling flow described above becomes less convenient when dealing with large amounts of data based on different devices size. Modeling engineers need to do much programming to achieve their goals. The Model Generator can help them to manage data better and maintain flexibility while reducing programming effort.

The MG framework allows you:

- Define project parameters, set model flags and device constants related to your device and project
- One-click loading of the measured data from a highly structured directory
- Run the manual or automatic model extraction to get to the final model parameters
- Easily create scaling plots for target modeling and device extraction analysis
- Implement RF de-embedding and assign the de-embedding plots in the extraction flow
- Use Recentering Tool to adjust model parameters, align with new targets, display statistical corners, and run 30 ADS processes at once
- Automatic loading of circuits and sub-circuits. Users can also import or export Circuit Netlist (.cir) to or from a Model Generator project
- Review model accuracy by defining model quality checks and plots
- Export the device model parameters and create HTML and PDF reports based on the defined “Verify” plots
- Include multiple examples with different models to help users

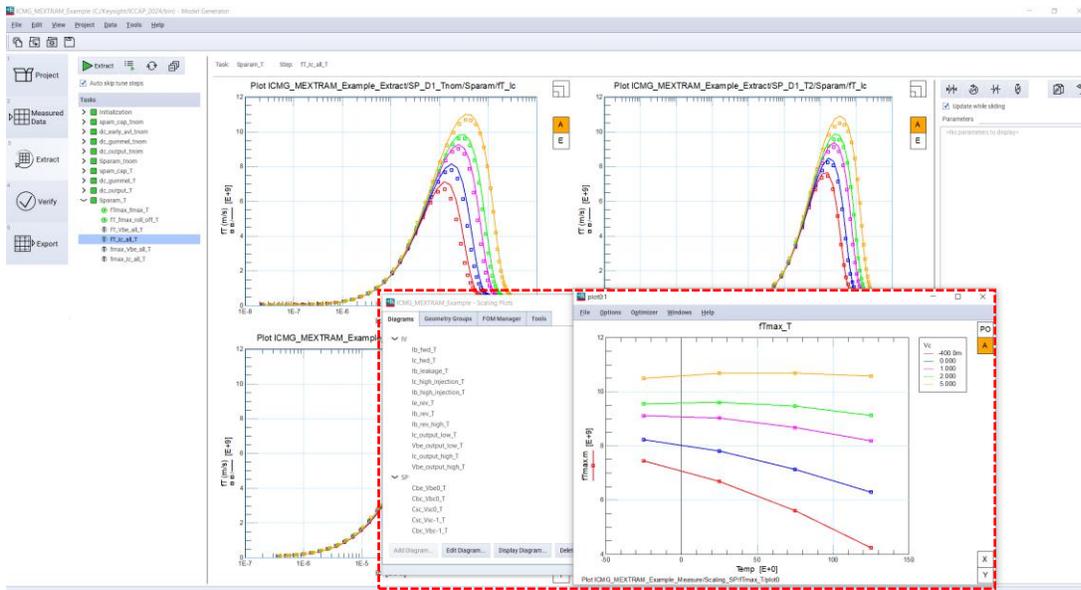


Figure 10. Extraction flow with Mextram model including scaling plots in Model Generator.

RF De-embedding tool

The De-embedding tool performs RF de-embedding based on the imported measured S-parameters data and the selected de-embedding method. The tool also displays the S-parameters before and after de-embedding. Three default de-embedding methods are provided, and you can also use the customized methods.

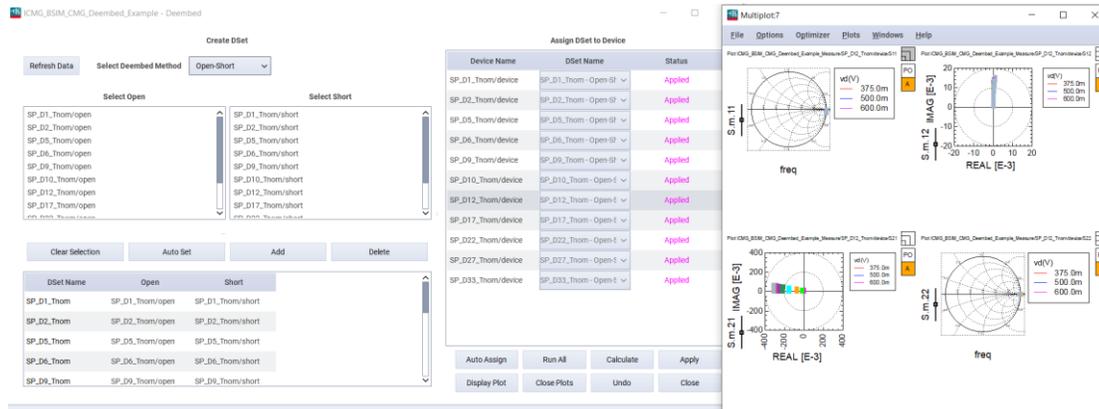


Figure 13. Using De-embedding tool in Model Generator to perform RF de-embedding.

CMOS modeling

IC-CAP provides powerful turnkey extraction packages for CMC industry-standard CMOS models: BSIM3, BSIM4, BSIMSOI, PSP, HiSIM, BSIM-BULK (formerly BSIM6), and HiSIM_HV. The CMOS Extraction packages share the same architecture, making it possible to use the same measured data to extract different CMOS models.

The packages provide the following key features:

- DC, CV, and temperature-dependent modeling with geometry scaling and binning
- An easy-to-use user interface makes CMOS modeling easy and convenient
- Open and flexible extraction methodologies. All packages come with a robust extraction methodology that can be highly customized to adapt to specific process technologies
- Where fine-tuning and optimization are necessary, the extraction process guides you through predefined optimizer and tuner steps
- Powerful multiplot data display allows users to create and customize plots, including geometry and temperature scaling plots
- Automatic generation of complete model documentation in HTML format
- Automatic failure tracking and reporting during measurement and extraction process
- Highly accurate RF extraction methodologies with enhanced, scalable RF gate and substrate resistance models
- Target modeling capabilities. Target modeling allows users to extract a preliminary model based on targets (spot data) or re-center an existing model to match new process specs.

- Based on process parameters variations and their typical values, the Corner modeling extraction package allows the user to extract corner libraries for a CMOS process. The final library is verified for a range of devices, temperatures, and bias conditions on all supported simulators.

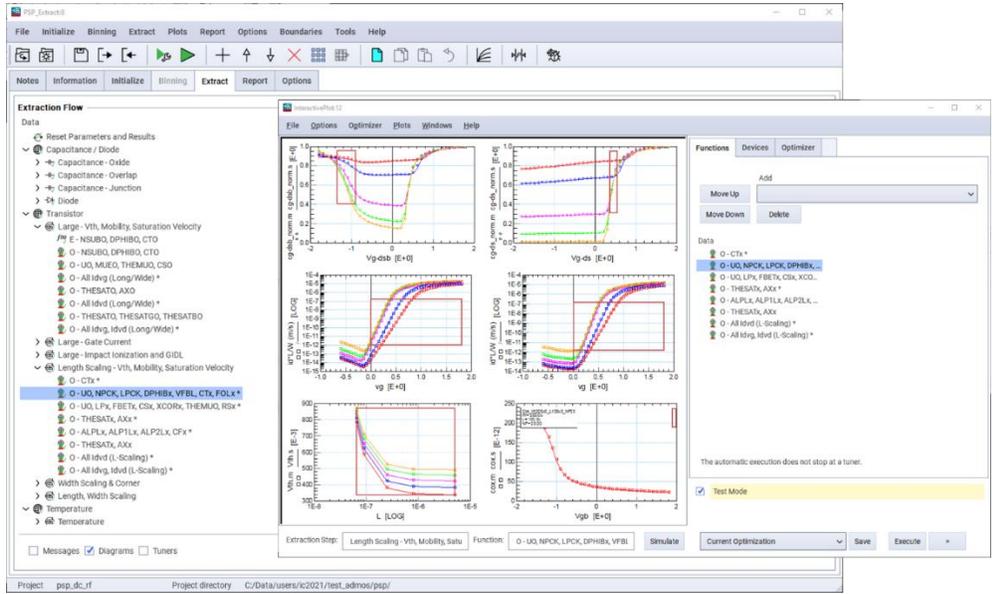


Figure 14. The CMOS PSP Modeling Package. On the left, the PSP Extract Main window shows the extraction flow. The display window shows the extraction step, an optimization, under execution.

The CMOS extraction packages are regularly updated to support the latest model versions. Please check the product documentation for updated information on supported versions and simulators.

In addition to the CMC compact models, IC-CAP provides legacy extraction packages for the NXP's MOS Model 9, UCB level 2, 3 models, and the data-based Root MOS Model. Please see Table 3 for a complete list of CMOS modeling packages and their product numbers.

BJT and HBT modeling

The W7011E PathWave IC-CAP BJT Plus Extraction Package Plus Add-on includes VBIC, Gummel-Poon, Keysight ADS HBT, and Mextram models.

BCTM VBIC BJT model

VBIC is an abbreviation for Vertical Bipolar Inter-Company, a public domain model developed by the Bipolar Circuits and Technology Meeting (BCTM) consortium. It models quasi-saturation, avalanche, and substrate effects. The latest release includes self-heating effects.

Gummel-Poon and EEBJT2 models

The Gummel-Poon (GP) model has been the industry-standard model for BJT devices for decades. IC-CAP includes an RF-based version of the GP model extraction, which is well suited for RF applications. Here, CV measurements are replaced with S-parameter measurements, making the junction capacitance extraction more accurate. The GP example includes improved methods for extracting ideality, base resistance, and reverse early voltage. In addition to the standard GP model, the BJT package includes the Keysight EEBJT2 model, a modified GP model to improve AC and DC accuracy.

ADS HBT modeling package

The Keysight ADS HBT (AHBT) extraction package provides comprehensive measurement and extraction procedures for III-V HBT device modeling. AHBT is a complete HBT model explicitly designed for GaAs, SiGe, and InP processes, supporting both single and double heterojunctions. The turnkey package includes comprehensive measurement and extraction procedures.

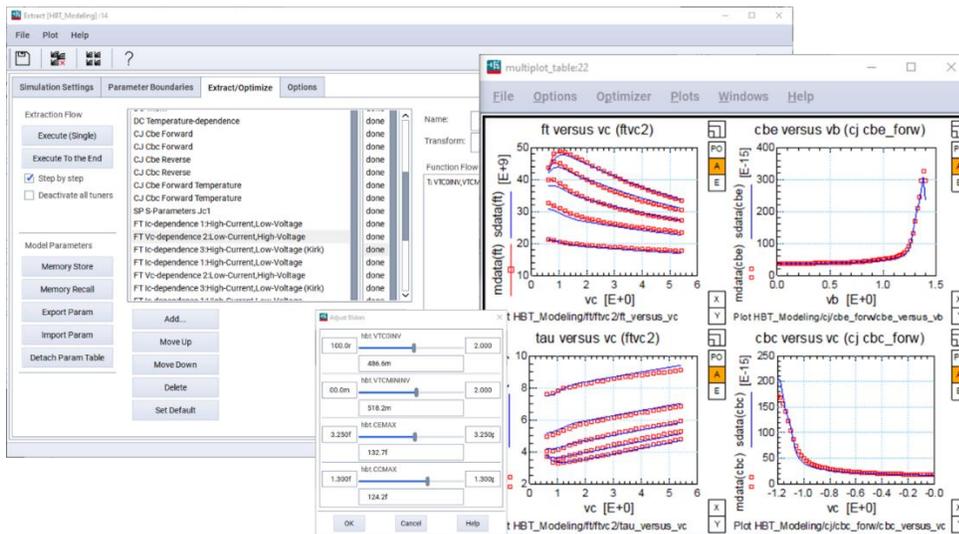


Figure 15. The extraction flow of the Keysight ADS HBT Modeling Package.

The W7011E package also includes the extraction of the Mextram CMC industry-standard bipolar model. Its extraction has been implemented in IC-CAP through work jointly carried out by Philips/NXP Research Labs, TU Delft, and Keysight EDA. Extensively used within Philips/NXP, the model has proven to be highly robust and accurate.

GaN modeling

CMC ASM-HEMT and MVSG GaN modeling examples

The accurate modeling of Gallium Nitride (GaN) devices is challenging due to trapping and thermal effects on the device's electrical characteristics. Empirical GaAs models may not be accurate enough for certain types of nonlinear applications. After an extensive qualification process, the Compact Modeling Council (CMC) has promoted two new physics-based models to CMC standards: the ASM-HEMT developed by UC Berkeley in collaboration with IIT Kanpur and the MVSG_CMC model, originally developed by MIT and now maintained by the University of Waterloo. If you are new to GaN modeling or the new CMC models, the IC-CAP CMC GaN basic extraction flow examples for ASM-HEMT and MVSG_CMC can save you valuable time. Keysight developed the extraction flow in collaboration with the model developers. The step-by-step extraction guide lets you quickly learn the model fundamentals and how to extract the core parameters. ASM-HEMT 101.3 and MVSG_CMC 3.1.0 are used in the examples individually.

The framework is open and flexible and allows you to:

- Organize and import measured data automatically
- Execute the flow, step by step using a novel Modeling UI
- Review overall fit, save intermediate and final steps
- Quickly customize the flow to your specific GaN process
- Extract self-heating and trapping effects

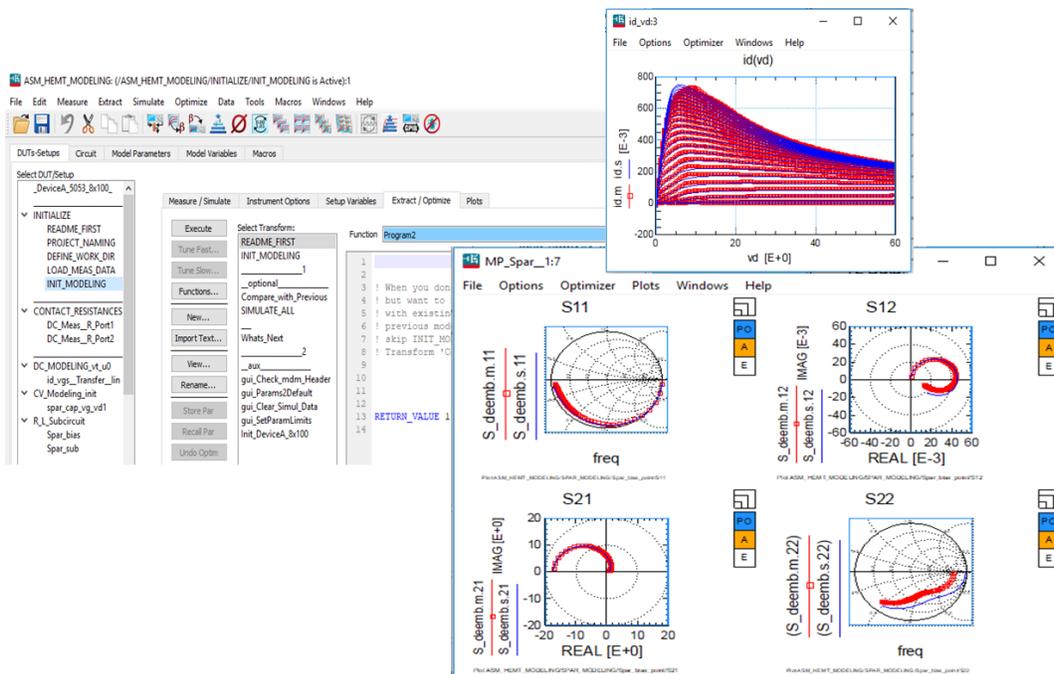


Figure 16. The CMC GaN Modeling Package (ASM-HEMT model shown).

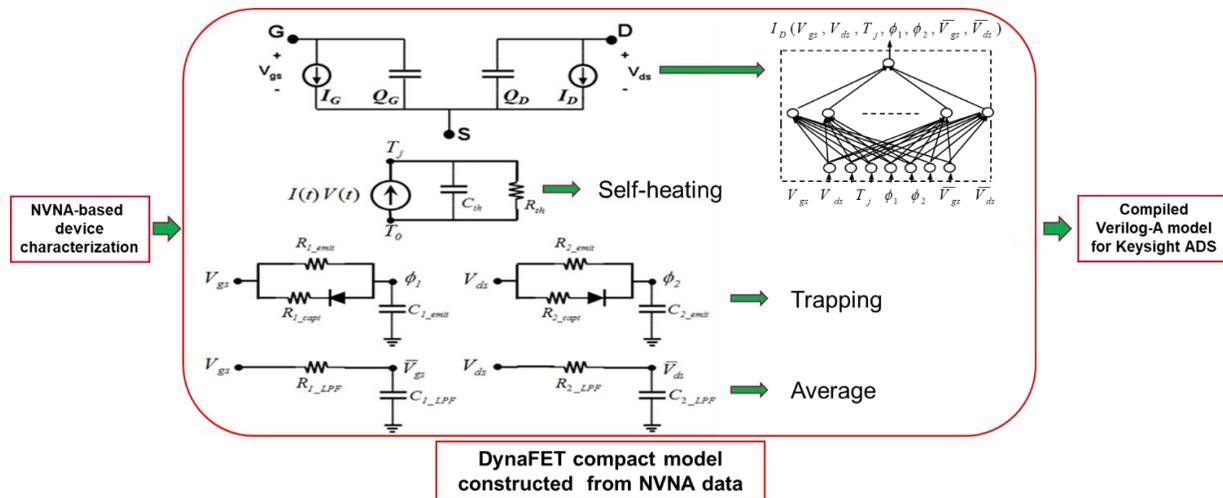


Figure 18. The DynaFET equivalent circuit and the Artificial Neural Network describing the current and charges constitutive equations.

The DynaFET model applies to high-efficiency power amplifiers, radar applications, and low noise amplifiers of GaN and GaAs FETs / HEMTs. It does not cover switching or passive mixer applications. DynaFET inherits all the advantages of NeuroFET but adds some key benefits:

- Accurate results vs. temperature with dynamic self-heating and temperature dependence
- Improved broadband performance versus frequency across the entire device operating range
- Improved prediction of large-signal nonlinearities (distortion, load-pull, PAE) and long-term memory effects
- Improved transient performance by independently modeling each dynamic device mechanism and correctly coupling them together self-consistently

Demonstrated large-signal validation for harmonic tuning at both input and output ports.

The W7020A PathWave IC-CAP DynaFET Model Generation software add-on controls DC and S-parameters measurements, imports nonlinear measured data from the Keysight NVNA, and extracts the DynaFET ANN for simulator use. DynaFET model extraction and training are also available as a service.

MESFET and HEMT modeling

The W7012E PathWave IC-CAP FET Plus Extraction Package includes extractions for the following models and packages except for the NeuroFET, which is offered as a standalone product.

Curtice, Statz MESFET models

The package includes extraction routines for three popular industry-standard MESFET models: the Curtice quadratic, Curtice cubic, and Statz (Raytheon). The three models' differences are in the empirical relationships that describe the device's DC and AC characteristics. IC-CAP extracts the model parameters from a combination of DC and S-parameter measurements.

EEFET3/EEHEMT1 models

These are empirical, nonlinear models for general GaAs FET or HEMT applications, including large-signal, three-terminal IC, and packaged devices. They accurately model DC and bias-dependent S-parameters, time delay, sub-threshold current, and dispersion of R_{ds} . Also included is the drain current model based on Keysight EDA original equations and advanced models for C_{gs} and C_{gd} , including transcapacitance effects.

The EE models also take into account static self-heating effects in the drain current. The modules provide highly automated parameter extraction techniques with package parasitics extracted automatically. HEMTs are similar to MESFETs, but with one distinguishing difference in the behavior of G_m versus V_{gs} . EEHEMT1 is a superset of EEFET3 and has a set of analytic functions for modeling the G_m compression of a HEMT.

The W7012E PathWave IC-CAP FET Plus Extraction Package includes the Angelov-GaN Modeling Package. See the GaN Modeling section for details.

Root MESFET/HEMT models

Root Models are process and technology independent, data-based models for large-signal, three-terminal applications. They model nonlinearities of GaAs FETs and HEMTs, including frequency dispersion, and are scalable. The models feature automated data acquisition as well as high-speed model generation.

Keysight NeuroFET model

While the Root model was a significant invention for its day, it had some limitations due to the table-based representation of device currents and charges. With the table-based model, non-optimal trade-offs had to be made among modeling all the multi-variate capacitances and conductance over the full bias range of device operation. The NeuroFET model is an evolution of the Root model, with the same topology but with tables replaced by Artificial Neural Networks (ANN). ANNs are a computational paradigm based on how the brain works - they can smoothly approximate any nonlinear function using a network of highly interconnected nonlinear processing functions called neurons. A sophisticated machine learning (ML)

training algorithm identifies the weights between nodes to optimally and smoothly approximate the model nonlinear current and charge functions. Smooth derivatives are critical for accurately representing distortion behavior, particularly at low signal levels, where high-order derivatives must be continuous to achieve the best accuracy.

The NeuroFET model also adds a sophisticated extrapolation methodology beyond the boundaries of the measured data, resulting in robust DC and large-signal simulator convergence behavior. The resultant model is technology-independent and works well for both HEMT and FET devices in silicon and III-V materials. NeuroFET does not cover passive mixer or switching applications.

Advantages of the NeuroFET over table-based Root models:

- Improved DC and RF convergence
- Improved distortion simulation at low amplitude
- More accurate S-parameters vs. bias

The IC-CAP NeuroFET Extraction package controls DC and S-parameters measurements necessary to extract the model. The software then performs ANN training through a specific error optimization procedure. The output model file can then be simulated via ADS within the IC-CAP environment or exported to ADS for use with the built-in NeuroFET circuit simulation component.

Artificial Neural Network (ANN) modeling

The Artificial Neural Network (ANN) solution within PathWave Device Modeling (IC-CAP) provides the capability to create nonlinear model building blocks (e.g. controlled current sources and controlled charge sources). These can be easily configured to produce accurate device models from data, in the absence of a robust physically based or conventional empirical model. The ANN library provides rapid model generation to support the fastest possible device or circuit evaluations in any design phase and explore the circuit consequences of novel technologies where models are currently unavailable or simply not accurate enough.

The Keysight ANN library has unique capabilities compared to other ANN commercial solutions. The Keysight solution provides advanced fitting capabilities to train the ANN from either (x,y) and $(x, dy/dx)$ types of datasets, where x is the input, y is the output, and dy/dx is the (partial) derivative of the output. dy/dx can extract the charge models from capacitive information which is important for device modeling.

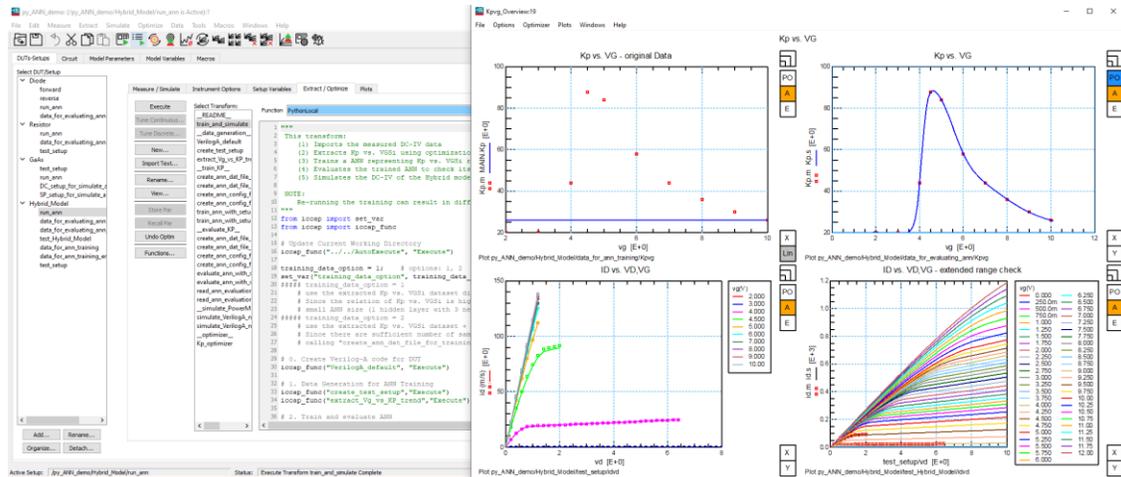


Figure 19. Artificial Neural Network (ANN) modeling toolkit.

Third-party models

The unique, open, and flexible IC-CAP framework enables third parties to design and develop models and related extraction packages that work as add-ons in the IC-CAP environment. The IC-CAP GUI Studio enables third parties to design and implement custom extraction packages with specific user interfaces and related extraction routines.

BSIMS013v2 and EKV 2.6 Extraction Packages are available from AdMOS www.admos.de

Product Configuration

PathWave device modeling bundles and elements

PathWave IC-CAP product includes 6 major components:

- Core Environment – Model Generator, GUI, IC-CAP GUI Studio, examples, Python/PEL programming environment, and graphics
- Simulation and Analysis – Simulation, optimization, tuning, and DataPro
- Instrument Connectivity – Instrument drivers
- Wafer Professional (WaferPro) – Automated Wafer-level measurements
- Add-on licensed extraction packages for various technologies (e.g. MOSFET, BJT, HEMT)

Table 1 lists the available bundles, and below is a description of the most popular bundles and core elements. Table 2 shows the core elements that can be used as an alternative to bundles. In addition to these, extraction packages and examples are also available as add-ons, as listed in Table 3.

Table 1. PathWave IC-CAP bundles

Model	Name	Description
W7300B	PathWave IC-CAP Device Modeling Platform	It contains all the tools to characterize and model semiconductor devices, including the software environment, all measurement drivers, optimization, Python/PEL programming, Model Generator, and all simulation links. The legacy W8503EP DataPro is included in this bundle.
W7301B	PathWave IC-CAP Model Generator Plus	Includes the W7300B and the W7008E Model Generator Recentering, enabling tuning and optimization techniques to efficiently adjust important parameters to adapt the model to the new targets.
W7311B	PathWave IC-CAP Wafer Professional Measurement	It enables automated wafer-level measurement with turnkey measurement drivers and Python/PEL programming to customize measurement algorithms and data processing. This bundle does not include simulation.
W7325B	PathWave IC-CAP CMC GaN RF Modeling	The CMC GaN bundle includes the W7300B and the GaN RF modeling extraction examples for the ASM-HEMT and the MVSG models.
W6325B	PathWave MBP/IC-CAP Device Model Extraction	This cross-platform bundle includes the W7001E and W7010E IC-CAP Core Environment and Analysis elements and the W6300B PathWave MBP core bundle.
W6347B	PathWave MQA/IC-CAP Modeling Bundle	This cross-platform bundle Includes the W7001E and W7010E IC-CAP Core Environment and Analysis elements and the W6800B PathWave MQA core bundle.

W7300B PathWave IC-CAP device modeling platform bundle

The Device Modeling Platform Bundle is our main bundle. It lets users set up custom extraction routines, measure data using instrument drivers, analyze results, perform simulations, and optimize extracted parameters to create and run any custom model extraction or unlicensed extraction examples. It provides

the essential tools you need to start measuring and modeling devices and circuits. It consists of the following components:

- W7001E PathWave IC-CAP Core environment
- W7010E PathWave IC-CAP Simulation, Analysis, and DataPro
- W7015E PathWave IC-CAP Instrument Connectivity

Numerous modeling examples and tutorials are available at no extra cost as starting points to build and design your extraction toolkits.

W7301B PathWave IC-CAP Model Generator Plus Bundle

W7301B includes the Device Modeling Platform bundle and the W7008E Model Generator Recentering, enabling tuning and optimization techniques to efficiently adjust important parameters to adapt the model to the new targets.

W7311B PathWave IC-CAP WaferPro Bundle

The W7311B provides the ability to run automated wafer-level DC, CV, and RF measurements using the WaferPro test executive. It includes the following components:

- W7001E PathWave IC-CAP Core environment
- W7015E PathWave IC-CAP Instrument Connectivity – See Table 4 for supported instrument drivers
- W7014E PathWave IC-CAP Wafer Professional (WaferPro)

IC-CAP WaferPro works within and in conjunction with the IC-CAP platform and lets users create and execute automated test plans. Note that this bundle does not include the W7010E Simulation and Analysis, which is needed to run the simulation mode's test plan.

W7325B PathWave IC-CAP CMC GaN modeling bundle

The W7325B provides all the necessary capabilities to characterize and extract GaN devices using the CMC ASM-HEMT and MVSG GaN models. Like the W7300B, it includes the IC-CAP environment, instrument connectivity, and analysis. It also includes the W7027E element enabling the ASM-HEMT and MVSG examples.

The MVSG and ASM-HEMT examples give you a head start in learning the basic capabilities of the models, i.e., learning the core parameters and how to extract them. As we gain more experience and insights into these new models, we will expand the flow to cover more advanced effects like thermal and trapping.

W7001E PathWave IC-CAP core environment

The W7001E is the necessary foundation element in all bundles. In addition to the UI framework, it provides the Python and PEL programming environments. It allows you to process data with mathematical transforms, display and customize plots, write Python and PEL macros, create and automate extraction routines, and write user-defined functions using the C programming language. Both Python and PEL include extensive math, utilities, and modeling function libraries. Also included is IC-CAP GUI Studio, which allows you to develop custom graphical user interfaces.

W7010E PathWave IC-CAP simulation, analysis, and DataPro

The analysis module is the IC-CAP simulator engine that provides the ability to simulate device or circuit performance using the default simulator ADS, the custom SPICE3 simulator, and by linking to a wide range of other external simulators. The W7010E includes Linear (DC, CV, and AC), nonlinear (Harmonic Balance), and transient simulation with ADS.

IC-CAP DataPro applies statistical analysis on selected measured data, and based on statistical distribution and variances, identifies golden and corner die. DataPro can import data directly from WaferPro or a simple IC-CAP project format. The statistical analysis applies to either sweep or spot types of measurements.

Table 2. PathWave IC-CAP core elements and extraction packages

Model	Name	Description
W7001E	PathWave IC-CAP Core Environment	Core, GUI, data display, PEL/Python programming, and Model Generator
W7008E	PathWave IC-CAP Model Generator Re-Centering and Corners	Enables tuning and optimization techniques to efficiently adjust important parameters to adapt the model to the new targets.
W7009E	PathWave IC-CAP ANN Modeling Toolkit	Adds ANN solution to substitute the standard compact model with ANN technology to improve the model's accuracy
W7010E	PathWave IC-CAP Simulation and Analysis	Adds Optimization/tuning, simulation with PathWave ADS (20-pack simulations), and all other supported simulators. Note that most commercial simulators require a license
W7014E	PathWave IC-CAP Wafer Professional (WaferPro) Add-on	Adds IC-CAP WaferPro for automated wafer-level measurement. Requires W7015 Instrument Connectivity
W7015E	PathWave IC-CAP Instrument Connectivity	Enables all supported instrument drivers

Table 3. PathWave IC-CAP extraction packages

Device Technology	Product Number	Product Name	Description
GaN, HEMT, MESFET	W7012E	PathWave IC-CAP FET Plus Extraction Package	EEHEMT, EEFET, Curtice, Root FET/Diode/MOS, and Angelov-GaN
	W7013E	PathWave IC-CAP GaN Model Extraction Package Plus	ASM-HEMT, MVSG, and Angelov-GaN models.
	W7019E	PathWave IC-CAP NeuroFET Extraction Package	Keysight ANN-based model, NeuroFET for GaAs devices
	W7020E	PathWave IC-CAP DynaFET Model Generation	Keysight ANN-based model, DynaFET for GaN RF devices
	W7027E	PathWave IC-CAP CMC GaN RF Modeling Add-on	GaN RF modeling extraction examples for the ASM-HEMT and the MVSG models.
BJT	W7011E	PathWave IC-CAP BJT Plus Extraction Package Plus	Keysight HBT, VBIC, Gummel-Poon, and Mextram models
MOSFET	W7021E	PathWave IC-CAP PSP Model Extraction Package	PSP Model Extraction Package, Target, and Corner modeling
	W7022E	PathWave IC-CAP HiSIM Model Extraction Package	HiSIM Model Extraction Package, Target, and Corner modeling
	W7023E	PathWave IC-CAP BSIMSOI Model Extraction Package	BSIMSOI Model Extraction Package, Target, and Corner modeling
	W7024E	PathWave IC-CAP BSIM3 Model Extraction Package	BSIM3 Model Extraction Package, Target, and Corner modeling
	W7025E	PathWave IC-CAP BSIM4 Model Extraction Package	BSIM4 Model Extraction Package, Target, and Corner modeling
	W7026E	PathWave IC-CAP HiSIM_HV Model Extraction Package	HiSIM_HV Model Extraction Package, Target, and Corner modeling
Power Electronics Models	W7016E	PathWave IC-CAP Power Electronics PowerMOS SiC Extraction Package	Keysight PowerMOS SiC model for SiC and Si power devices for power electronics applications
	W7017E	PathWave IC-CAP Power Electronics IGBT Extraction Package	Keysight IGBT model for power electronics applications
	W7018E	PathWave IC-CAP Power Electronics GaN Extraction Package	ASM-HEMT model for power electronics applications

Supported instruments, probes and switch matrices

Table 4. Supported instrument drivers in W7015E PathWave IC-CAP Instrument Connectivity

Instrument connectivity drivers	Supported instruments
DC Measurement Drivers	<p>Keysight B1500A Semiconductor Device Analyzer Keysight B1505A Power Device Analyzer/Curve Tracer Keysight E5270 Series parameter analyzer: E5270B, E5272A, and E5273A Keysight E5260 Modular Source/Measure Units Keysight B2900 Series Precision Source/Measure Unit Keysight M9601A PXIe Precision Source/Measure Unit Keysight N6705 DC Power Analyzer Keysight 4156x Semiconductor Parameter Analyzer¹ Keysight 4140 pA Meter/DC voltage source¹ Keysight 4141 DC source/monitor¹ Keysight 4142x Modular DC source/monitor¹ Keysight 4145x Semiconductor Parameter Analyzer¹ Keysight 4155x Semiconductor Parameter Analyzer¹ Keysight 4071A Parametric Semiconductor Tester</p> <p>Keithley instrument drivers for the 2410, 26xx Class SourceMeters and 4200-SCS are provided as open-source and are not covered by standard maintenance. ¹ Discontinued or obsolete product - Keysight provides limited or no maintenance effort.</p>
AC Measurement Drivers	<p>Keysight PNA-X Series (S-parameters, multi-port, gain compression and intermodulation) Keysight PNA Series (S-parameters, multi-port) Keysight ENA Series Keysight 3577 Network Analyzer¹ Keysight 8510 A/B/C/XF Network Analyzer¹ Keysight 8702 Network Analyzer¹ Keysight 87xx Network Analyzer¹ Keysight 89410 Vector Signal Analyzer Anritsu 37000 Network Analyzer¹ Anritsu VectorStar Network Analyzer² Rohde & Schwarz ZNA Series Vector Network Analyzers</p> <p>Please see the product documentation for an updated list of supported and discontinued Keysight PNA-X, PNA, and ENA network analyzers. ¹ Discontinued or obsolete product - Keysight provides limited or no maintenance effort. ² Provided by Anritsu. Contact Anritsu for questions/support about this driver.</p>
LCRZ Measurement Drivers	<p>Keysight E4991A1, E4991B RF Impedance/Material Analyzer Keysight E4990A Impedance Analyzer Keysight E4980A Precision LCR meter Keysight 4194 Impedance Analyzer¹ Keysight 4271 1 MHz digital capacitance meter¹ Keysight 4275 Multi-frequency LCR Meter¹ Keysight 4280 2 MHz Capacitance Meter¹ Keysight 4284 Precision LCR Meter¹ Keysight 4285 Precision LCR Meter¹ Keysight 4294A Precision Impedance Analyzer¹</p> <p>¹Discontinued or obsolete product - Keysight provides limited or no maintenance effort.</p>
Time-domain Measurement Drivers	<p>Keysight 54120T-54124T Digitizing Oscilloscopes¹ Keysight 54510 Digitizing Oscilloscopes¹Keysight 54750 TDR Oscilloscope¹ Keysight 8130 Pulse Generator¹ Keysight 8131 Pulse Generator¹ Keysight 54720 Module Real_Time Oscilloscope Keysight Infinium Oscilloscope 54800 Series Keysight 85124 Pulse System</p> <p>¹Discontinued or obsolete product - Keysight provides limited or no maintenance effort.</p>
Noise Measurement Drivers	<p>Keysight 35670A Dynamic Signal Analyzer¹</p> <p>¹Discontinued or obsolete product - Keysight provides limited or no maintenance effort.</p>

Table 5. Supported Wafer Probers, Switch Matrixes, and Thermal Controllers

Wafer probers	Switch matrixes	Thermal controllers
FormFactor Velox & VeloxPro FormFactor Nucleus ¹ 3 & 4 FormFactor PS21 ¹ FormFactor Summit ¹ 12K MPI ² Sentio Suss PA300 ¹ Accretech UF3000 ¹ Tokyo Electron (TEL) P8, P12 EG6000 Micromanipulator ² P300L	Keysight B2200A Keithley 707/708 Keysight HP4070 ¹ Keysight HP4080 ¹	FormFactor Velox & VeloxPro Tokyo Electron (TEL) Accretech Cascade ¹ Summit Cascade ¹ PS21 Temprotronic ¹ TP032A
¹ Discontinued or obsoleted product - Keysight provides limited or no maintenance effort. ² Contact manufacturer for support.		

Supported simulators

Table 6. Supported simulators

Simulator	Company	Licenses required
ADS (hpeesofsim, spmodeads, hspicemodeads)	Keysight	The W7010E PathWave Analysis and Simulation Element includes linear, transient, and Verilog-A simulations with ADS.
HSPICE	Synopsys	License required
SABER	Synopsys	License required
MMSIM (SPECTRE)	Cadence	License required
AFS	Mentor Graphics	License required
ELDO	Mentor Graphics	License required
SmartSpice	Silvaco	License required
Spice3, PSPICE, HPSPICE	Various	Included in the W7010E, however, these simulators are no longer actively supported (legacy simulators)

Please refer to the documentation for a detailed description of the analysis supported by each simulator.

License and Subscription Types

Each IC-CAP module is available in two license versions:

1. A node-locked version allowing the software to execute only on a single PC
2. A network-licensed version for execution on multiple workstations or PCs on a network, allowing various workgroups to share the software. Four types of floating licenses are available: N - Single site: 1-mile radius from the server; Q – Telecommuter: A time-zone in Americas, a country in Europe/Asia; R - Single region1: Americas, Europe, Asia, and W - Worldwide (export restriction identified in End User License Agreement (EULA))

Table 7. KeysightCare software support subscription

Subscription	Description
KeysightCare support subscription	<p>Perpetual2 licenses are sold with a 12 (default), 24, 36, or 60-month software support subscription. Support subscriptions can be renewed for a fee after that.</p> <p>Subscription licenses already include a software support subscription through the term of the license.</p>

1. Americas (North, Central, and South America, Canada); Europe (European Continent, Middle Eastern Europe, Africa, India); Asia (North and South Asia Pacific Countries, China, Taiwan, Japan).

2. The purchase of EDA perpetual licenses is no longer available. For more information, refer to <https://www.keysight.com/find/eda-perpetual-license-letter>

World Wide Web

Our Keysight EDA World Wide Web includes a dedicated Support Web area for downloadable patches, defects and solutions, and online technical support. In addition, Keysight EDA gives you access to other services, including:

- News and events
- Product information and online demos
- Product applications and examples
- Online product documentation
- Customer training classes
- Online and telephone technical support

World-Class Service, Support, and Training

Keysight EDA is committed to helping you achieve success with PathWave IC-CAP through customer education courses, technical support, and custom solution services.

To help you begin using IC-CAP quickly and productively, a comprehensive, Keysight offers a three-day training course held at various locations, including the Keysight EDA facility in Santa Rosa, California, USA.

A support contract also includes automatic software updates, literature, and documentation to bring you the latest product enhancements and features.

www.keysight.com/find/eesof

www.keysight.com/find/eesof-iccap



Keysight enables innovators to push the boundaries of engineering by quickly solving design, emulation, and test challenges to create the best product experiences. Start your innovation journey at www.keysight.com.

This information is subject to change without notice. © Keysight Technologies, 2021 – 2023, Published in USA, November 29, 2023, 5965-7742E